

M211xx — 72x72/144x144 Large Crosspoint Switches

Product Bulletin

Products Affected: M21131/M21151 (data sheet page 9, Table 3-2)

Products Affected: M21131V/M21151V (data sheet page 53, Table 3-5)

Products Affected: M21136/M21156 (data sheet page 9, Table 3-7)

Products Affected: M21141/M21161 (data sheet page 56, Table 3-2)

Parallel I/O Read Timing

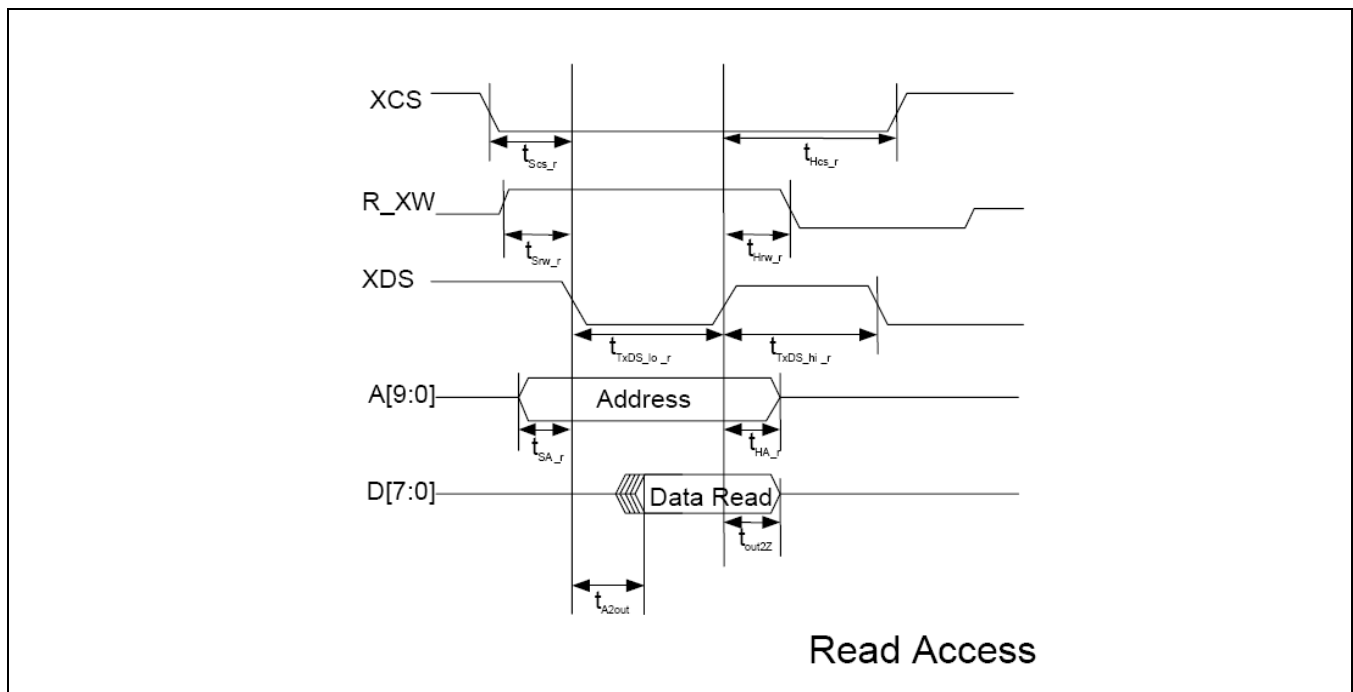
Description:

Setup time for XCS before XDS can be as low as 0 ns. All designs with previous specification (5 ns minimum) are still valid.

Table 1-1. Parallel I/O Read Timing

Parameter	Description	Minimum	Typical	Maximum
t_{Scs_r}	XCS falling edge before XDS falling edge	0 ns	—	—

Figure 1. Timing Diagram - Read Access



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