M211xx — 72x72/144x144 Large Crosspoint Switches

Product Bulletin
Products Affected: M21131/M21151 (data sheet page 9, Table 3-2)
Products Affected: M21131V/M21151V (data sheet page 53, Table 3-5)
Products Affected: M21136/M21156 (data sheet page 9, Table 3-7)
Products Affected: M21141/M21161 (data sheet page 56, Table 3-2)

Parallel I/O Read Timing

Description:
Setup time for XCS before XDS can be as low as 0 ns. All designs with previous specification (5 ns minimum) are still valid.

Table 1-1. Parallel I/O Read Timing

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>t_{s_{scs_r}}</td>
<td>XCS falling edge before XDS falling edge</td>
<td>0 ns</td>
<td>—</td>
<td>—</td>
</tr>
</tbody>
</table>

Figure 1. Timing Diagram - Read Access

Read Access