

Features

- Optimized for Cellular Base Station Applications
- Designed for Digital Predistortion Error Correction Systems
- High Terminal Impedances for Broadband Performance
- 48 V Capable Operation
- 100% RF Tested
- RoHS* Compliant

Description

The WSGPC01 GaN on SiC HEMT designed for base station applications and optimized for 2.496 - 2.690 GHz modulated signal operation. This device supports pulsed and linear operation with peak output power levels to 6 W (37.8 dBm) in a 4.0 x 4.5 mm DFN package.

Typical RF Performance

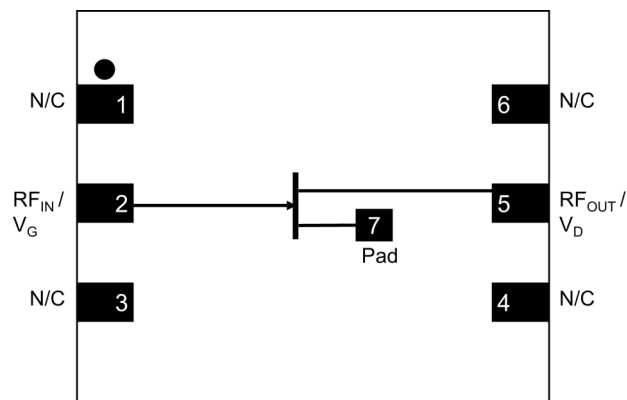
- WCDMA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF, $V_{DS} = 46\text{ V}$, $I_{DQ} = 10\text{ mA}$, $T_A = 25^\circ\text{C}$, $P_{OUT} = 23\text{ dBm}$.

Frequency (GHz)	G_P (dB)	η_D (%)	OPAR (dB)	ACPR (dBc)
2.496	18	13	9.7	-41
2.690	17	12	9.7	-41



4.0 x 4.5 mm DFN

Functional Schematic



Pin Configuration

Pin #	Function
1,3,4,6 ¹	No Connection ¹
2	RF Input / Gate
5	RF Output / Drain
7	Ground / Source

1. The pad on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information

Part Number	Package
WSGPC01-V1-R3K	3000 Piece Reel
FXA-WSGPC01V1-1	Driver Sample Board (tuned for 2.496-2.690GHz)

¹ * Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

RF Electrical Characteristics: $T_A = 25^\circ\text{C}$, $V_{DS} = 46\text{ V}$, $I_{DQ} = 10\text{ mA}$

Note: Performance in MACOM Single-ended Class-AB Evaluation Test Fixture, 50 Ω system.

Parameter	Test Conditions	Min.	Typ.	Max.	Units
Small Signal Gain	Pulsed ² , 2.6 GHz	-	18.5	-	dB
Saturation Output Power	Pulsed ² , 2.6 GHz	-	38	-	dBm
Drain Efficiency at Saturation	Pulsed ² , 2.6 GHz	-	50	-	%
Power Gain	WCDMA ³ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	18	-	dB
Drain Efficiency	WCDMA ³ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	12.5	-	%
Output CCDF @ 0.01%	WCDMA ³ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	9.7	-	dB
Adjacent Channel Power	WCDMA ³ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	-41	-	dBc
Input Return Loss	WCDMA ³ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	-6	-	dB
Ruggedness: Output Mismatch	All Phase Angles	VSWR = 5:1, No Device Damage			

RF Electrical Characteristics: $T_A = 25^\circ\text{C}$, $V_{DS} = 48\text{ V}$, $I_{DQ} = 10\text{ mA}$

Note: Performance in MACOM Single-ended Class-AB Production Test Fixture, 50 Ω system.

Parameter	Test Conditions	Min.	Typ.	Max.	Units
Power Gain	LTE ⁴ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	16	17.5	-	dB
Drain Efficiency	LTE ⁴ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	12.5	15	-	%
Output CCDF @ 0.01%	LTE ⁴ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	6	6.5	-	dB
Adjacent Channel Power Ratio	LTE ⁴ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	-37	-33	dBc
Input Return Loss	LTE ⁴ , 2.6 GHz, $P_{OUT} = 23\text{ dBm}$	-	-15	-7	dB

2. Pulse details: 20 μs pulse width, 0.2 ms period, 10% Duty Cycle

3. Modulated Signal: 3.84 MHz, WCDMA 3GPP TM1 64 DPCH, 9.9 dB PAR @ 0.01% CCDF

4. LTE Signal: 20 MHz, 8 dB PAR @ 0.01% CCDF

DC Electrical Characteristics: $T_A = 25^\circ\text{C}$

Parameter	Test Conditions	Min.	Typ.	Max.	Units
Drain-Source Leakage Current	$V_{GS} = -8\text{ V}, V_{DS} = 150\text{ V}$	-	-	0.28	mA
Gate-Source Leakage Current	$V_{GS} = -8\text{ V}, V_{DS} = 150\text{ V}$	-0.30	-	-	mA
Gate Threshold Voltage	$V_{DS} = 10\text{ V}, I_D = 0.88\text{ mA}$	-3.8	-2.8	-2.1	V

Recommended Operating Voltages

Parameter	Test Conditions	Min.	Typ.	Max.	Units
Drain Operating Voltage	—	0	-	50	V
Gate Quiescent Voltage	$V_{DS} = 48\text{ V}, I_D = 10\text{ mA}$	-3.6	-2.4	-2.1	V

Absolute Maximum Ratings^{5,6}

Parameter	Absolute Maximum
Drain Source Voltage, V_{DS}	150 V
Gate Source Voltage, V_{GS}	-10 V to +2 V
Operating Voltage	55 V
Gate Current I_G	0.88 mA
Drain Current, I_D	0.30 A
Junction Temperature	+225°C
Channel Operating Temperature	-40°C to +225°C
Storage Temperature	-65°C to +150°C

5. Exceeding any one or combination of these limits may cause permanent damage to this device.

6. MACOM does not recommend sustained operation near these survivability limits.

Bias Sequencing

Bias ON

1. Ensure RF is turned off
2. Apply pinch-off voltage of -5V to the gate
3. Apply nominal drain voltage
4. Bias gate to desired quiescent drain current
5. Apply RF

Bias OFF

1. Turn RF off
2. Apply pinch-off voltage to the gate
3. Turn off drain voltage
4. Turn-off gate voltage

Handling Procedures

Please observe the following precautions to avoid damage:

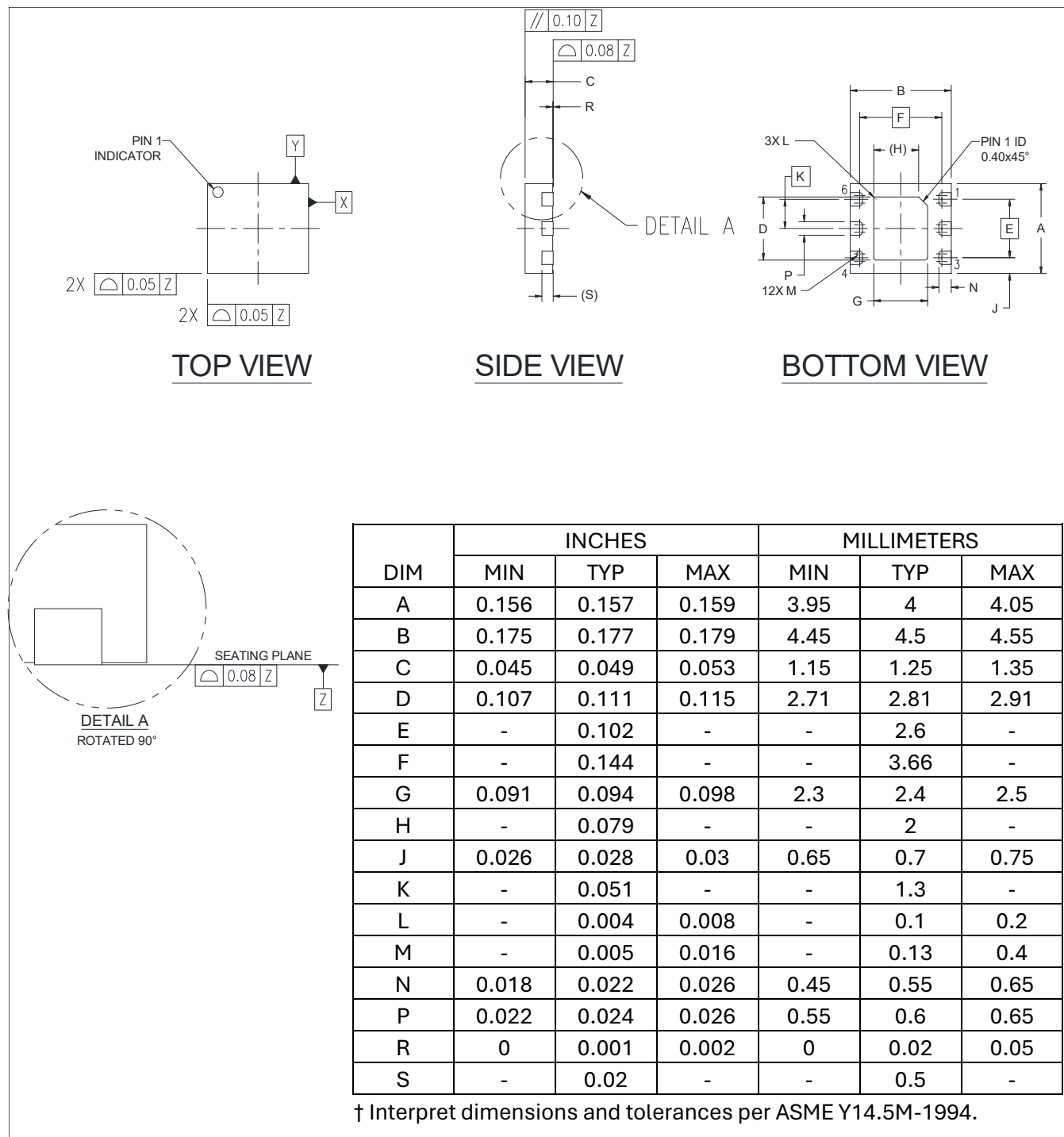
Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

ESD Characteristics

Test Methodology	Test Conditions
Human Body Model (per JS-001)	1A
Charge Device Model (per JS-002)	C1

Lead-Free 4.0 x 4.5 mm 6-Lead Package Dimensions[†]



5 [†] Meets JEDEC moisture sensitivity level (MSL) 3 requirements.

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