GaAs SPDT Switch
DC - 3.0 GHz

Features
- Low Insertion Loss: 0.4 dB @ 2.4 GHz
- Moderate Isolation: 27 dB @ 2.4 GHz
- Low Power Consumption: 5 μA @ +3.0 V
- Reduced Gate Lag for Fast Settling Time
- Lead-Free SC70-6LD Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description
M/A-COM’s MASW-008899 is a GaAs PHEMT MMIC SPDT switch in a lead-free SC-70 (SOT-363) surface mount plastic package. The MASW-008899 is ideally suited for applications where very small size and low cost are required.

Typical applications are transmit / receive (Tx / Rx) switching in linear systems such as WLAN 802.11b/g. Other applications include 1.9 GHz and 2.4 GHz DECT and linear systems operating up to 3.0 GHz.

The MASW-008899 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MASW-008899-000000</td>
<td>Bulk packaging</td>
</tr>
<tr>
<td>MASW-008899-TR3000</td>
<td>3000 piece reel</td>
</tr>
<tr>
<td>MASW-008899-001SMB</td>
<td>Sample Board, DC - 3.0 GHz Tuning</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power (0.5 - 3.0 GHz) 3 V Control</td>
<td>+30 dBm</td>
</tr>
<tr>
<td>Voltage</td>
<td>-8.5 V ≤ Vc ≤ +8.5 V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
</tbody>
</table>

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Electrical Specifications: \( T_A = 25^\circ C, V_C = 0 \text{ V} / 3 \text{ V}, Z_0 = 50 \Omega \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss(^6)</td>
<td>1.0 GHz</td>
<td>dB</td>
<td>0.3</td>
<td>0.4</td>
<td>0.5</td>
</tr>
<tr>
<td></td>
<td>2.4 GHz</td>
<td>dB</td>
<td>0.3</td>
<td>0.4</td>
<td>0.5</td>
</tr>
<tr>
<td>Isolation</td>
<td>1.0 GHz</td>
<td>dB</td>
<td>24</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>2.4 GHz</td>
<td>dB</td>
<td>25</td>
<td>27</td>
<td></td>
</tr>
<tr>
<td>VSWR</td>
<td>0.05 - 3.0 GHz</td>
<td>Ratio</td>
<td>1.2:1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>IP2</td>
<td>Two Tone, +5 dBm / Tone, 5 MHz Spacing 2.4 GHz</td>
<td>dBm</td>
<td>80</td>
<td></td>
<td></td>
</tr>
<tr>
<td>IP3</td>
<td>Two Tone, +5 dBm / Tone, 5 MHz Spacing 2.4 GHz</td>
<td>dBm</td>
<td>48</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Linear Pout</td>
<td>2.5 GHz, OFDM, QAM-64,54Mbps, EVM=2.5% 3.0 V</td>
<td>dBm</td>
<td>22.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>dBm</td>
<td>24.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>dBm</td>
<td>28.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>P1dB</td>
<td></td>
<td>dBm</td>
<td>28</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Trise, Tfall</td>
<td>10% to 90% RF and 90% to 10% RF ns</td>
<td></td>
<td>35</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Ton, Toff</td>
<td>50% control to 90% RF, 50% control to 10% RF ns</td>
<td></td>
<td>40</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Transients</td>
<td></td>
<td>mV</td>
<td>10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Current</td>
<td>( V_C = 3.0 \text{ V} ) ( \mu \text{A} )</td>
<td>\mu \text{A}</td>
<td>5</td>
<td>10</td>
<td></td>
</tr>
<tr>
<td>R(_{ON})</td>
<td>( t &gt; 10 \text{ ms after OFF to ON Switching (settled)} ) ( \Omega )</td>
<td>\Omega</td>
<td>1.5</td>
<td>2.5</td>
<td></td>
</tr>
<tr>
<td>Gate Lag</td>
<td>(</td>
<td>\Delta R_{ON}</td>
<td>) between 200 \mu s and 10 ms after OFF to ON Switching ( \Omega )</td>
<td>\Omega</td>
<td>0.15</td>
</tr>
</tbody>
</table>

5. For positive voltage control, external DC blocking capacitors are required on all RF ports.
6. Insertion Loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for 100 MHz – 1.0 GHz, 39 pF for 0.5 - 3.0 GHz.

Truth Table\(^7,8\)

<table>
<thead>
<tr>
<th>Control V1</th>
<th>Control V2</th>
<th>RFC-RF1</th>
<th>RFC-RF2</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>On</td>
<td>Off</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>Off</td>
<td>On</td>
</tr>
</tbody>
</table>

7. Differential voltage, \( V \) (state 1) - \( V \) (state 0), must be +2.3 V minimum and must not exceed 8.5 V.
8. \( 0 = 0 \text{ V } \pm 0.2 \text{ V}, 1 = +2.5 \text{ V to 5.0 V} \)

Qualification

Handling Procedures
The following precautions should be observed to avoid damage:

Static Sensitivity
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.
Typical Performance Curves

**Insertion Loss**

- **Frequency (GHz)**
  - 0.5
  - 1.0
  - 1.5
  - 2.0
  - 2.5
  - 3.0

- **Insertion Loss (dB)**
  - 0.2
  - 0.3
  - 0.4
  - 0.5
  - 0.6

- **Temperature**
  - +25°C
  - -40°C
  - +85°C

**Isolation**

- **Frequency (GHz)**
  - 0.5
  - 1.0
  - 1.5
  - 2.0
  - 2.5
  - 3.0

- **Isolation (dB)**
  - 22
  - 24
  - 26
  - 28
  - 30

- **Temperature**
  - +25°C
  - -40°C
  - +85°C

**VSWR**

- **Frequency (GHz)**
  - 0.5
  - 1.0
  - 1.5
  - 2.0
  - 2.5
  - 3.0

- **VSWR**
  - 1.0
  - 1.1
  - 1.2
  - 1.3
  - 1.4

- **Temperature**
  - +25°C
  - -40°C
  - +85°C

**EVM vs. Pout @ 2.5 GHz**

- **Pout (dBm)**
  - 10
  - 15
  - 20
  - 25
  - 30
  - 35

- **EVM (%)**
  - 0
  - 2
  - 4
  - 6
  - 8

- **Voltage**
  - 3.0 V
  - 3.3 V
  - 5.0 V

For further information and support please visit: [https://www.macom.com/support](https://www.macom.com/support)
**Lead-Free SC70-6LD (SOT-363)†**

† Reference Application Note M538 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.