Features
- Positive Voltage Control
- High Isolation:
  - 62 dB @ 1 GHz
  - 65 dB @ 2 GHz
- Low Insertion Loss:
  - 0.65 dB @ 1 GHz
  - 0.70 dB @ 2 GHz
- 50 Ω Internal Terminations
- Fast Settling for Low Gate Lag requirements
- Lead-Free MSOP-8-EP Package
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description
The MASW-008543 GaAs monolithic switch provides high isolation in a lead-free, plastic surface mount package. This device is ideal for applications across a broad range of frequencies.

MACOM fabricates the MASW-008543 using a 0.5-micron gate length pHEMT process. The process features full chip passivation for performance and reliability.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MASW-008543-000000</td>
<td>Bulk Packaging</td>
</tr>
<tr>
<td>MASW-008543-TR3000</td>
<td>3000 piece reel</td>
</tr>
<tr>
<td>MASW-008543-001SMB</td>
<td>Sample Board</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

SPDT High Isolation Terminated Switch
0.01- 4.0 GHz
Rev. V4

MASW
- 008543

Electrical Specifications\(^4\): \(T_A = 25^\circ \text{C}, V_{\text{HIGH}} = 3 \, \text{V}, V_{\text{LOW}} = 0 \, \text{V}, Z_0 = 50 \, \Omega\)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss</td>
<td>1.0 GHz</td>
<td>dB</td>
<td>—</td>
<td>0.65</td>
<td>0.95</td>
</tr>
<tr>
<td></td>
<td>2.0 GHz</td>
<td></td>
<td>0.70</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3.0 GHz</td>
<td></td>
<td>0.85</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4.0 GHz</td>
<td></td>
<td>1.10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Isolation</td>
<td>1.0 GHz</td>
<td>dB</td>
<td>—</td>
<td>62</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>2.0 GHz</td>
<td></td>
<td>65</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>3.0 GHz</td>
<td></td>
<td>50</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4.0 GHz</td>
<td></td>
<td>45</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Return Loss</td>
<td>0.5 - 4.0 GHz</td>
<td>dB</td>
<td>—</td>
<td>20</td>
<td>—</td>
</tr>
<tr>
<td>Input IP(_3)</td>
<td>2-Tone, 2.1 GHz, 10 MHz spacing</td>
<td>dBm</td>
<td>—</td>
<td>53</td>
<td>—</td>
</tr>
<tr>
<td>P1dB</td>
<td>2.1 GHz, (V_{\text{HIGH}} = 3 , \text{V})</td>
<td>dBm</td>
<td>—</td>
<td>25</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>2.1 GHz, (V_{\text{HIGH}} = 5 , \text{V})</td>
<td></td>
<td>30</td>
<td></td>
<td></td>
</tr>
<tr>
<td>P0.1dB</td>
<td>2.1 GHz, (V_{\text{HIGH}} = 3 , \text{V})</td>
<td>dBm</td>
<td>—</td>
<td>20</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td>2.1 GHz, (V_{\text{HIGH}} = 5 , \text{V})</td>
<td></td>
<td>25</td>
<td></td>
<td></td>
</tr>
<tr>
<td>(T_{\text{RISE}}, T_{\text{FALL}})</td>
<td>10% to 90% RF &amp; 90% to 10% RF</td>
<td>ns</td>
<td>—</td>
<td>30</td>
<td>—</td>
</tr>
<tr>
<td>(T_{\text{ON}}, T_{\text{OFF}})</td>
<td>50% of (V_C) to 10% / 90% RF</td>
<td>ns</td>
<td>—</td>
<td>52</td>
<td>—</td>
</tr>
<tr>
<td>Transients</td>
<td>In-band</td>
<td>mV</td>
<td>—</td>
<td>12</td>
<td>—</td>
</tr>
<tr>
<td>Control Current</td>
<td>-</td>
<td>µA</td>
<td>—</td>
<td>&lt;1</td>
<td>5</td>
</tr>
</tbody>
</table>

4. External DC blocking capacitors are required on all RF ports (39 pF capacitors are recommended).

Absolute Maximum Ratings\(^5,6\)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power, (V_{\text{HIGH}} = 3 , \text{V}) (0.5 - 3.0 GHz)</td>
<td>33 dBm</td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>8 V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
</tbody>
</table>

5. Exceeding any one or combination of these limits may cause permanent damage to this device.
6. MACOM does not recommend sustained operation near these survivability limits.

Truth Table\(^7,8,9\)

<table>
<thead>
<tr>
<th>V1</th>
<th>V2</th>
<th>RFC-RF1</th>
<th>RFC-RF2</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{\text{HIGH}})</td>
<td>(V_{\text{LOW}})</td>
<td>On</td>
<td>Off</td>
</tr>
<tr>
<td>(V_{\text{LOW}})</td>
<td>(V_{\text{HIGH}})</td>
<td>Off</td>
<td>On</td>
</tr>
</tbody>
</table>

7. \(V_{\text{LOW}} = 0 \, \text{V} \pm 0.2 \, \text{V}, V_{\text{HIGH}} = 1.8 \, \text{V} \text{ to } +5 \, \text{V}, \text{ minimum } V_{\text{HIGH}} - V_{\text{LOW}} = 1.8 \, \text{V}, \text{ maximum } V_{\text{HIGH}} - V_{\text{LOW}} = 8.0 \, \text{V}.
8. For use at low voltage, MACOM recommends connecting a 20K pull up resistor on pin 3 to a voltage equal to the most positive control voltage.
9. Negative control voltage may be used. The \(V_{\text{HIGH}}\) in the table would be the most positive (0 V) and the \(V_{\text{LOW}}\) would be the most negative (-3 V for example).
Typical Performance Curves

**Insertion Loss**

![Insertion Loss Graph](image)

**Isolation**

![Isolation Graph](image)

**Output Return Loss (on state)**

![Output Return Loss (on state) Graph](image)

**Output Return Loss (off state)**

![Output Return Loss (off state) Graph](image)

**Input Return Loss (on state)**

![Input Return Loss (on state) Graph](image)
Applications Section—Low Frequency Measurement

Insertion Loss

-0.8
-0.6
-0.4
-0.2
0.0
-1.0
0.0 0.2 0.4 0.6 0.8 1.0
Frequency (GHz)

S21 (dB)

Insertion Loss RFC-RF1 (dB)
Insertion Loss RFC-RF2 (dB)

Isolation

-65
-60
-55
-50
-45
0.0 0.2 0.4 0.6 0.8 1.0
Frequency (GHz)

S21 (dB)

Isolation (RFC-RF1) S21 (dB)
Isolation (RFC-RF2) S21 (dB)

Return Loss

-30
-25
-20
-15
-10
-5
0
0.0 0.2 0.4 0.6 0.8 1.0
Frequency (GHz)

Return Loss (dB)

RFC Return Loss (dB)
RF Port Return Loss (dB)
Terminated Port Return Loss (dB)

This data shows the MASW-008543 measured on an evaluation board with 0 Ω resistors. The board and connector loss have been removed.

0 Ω resistors can be used if negative control is available. To avoid changing the device bias points, the device should not be exposed to DC potentials on the RF ports.

With positive control MACOM recommends using DC-Blocking capacitors large enough that their $X_c$ is insignificant at the frequency of use. At 50 MHz a capacitor value greater than 1000 pF is recommended.
Lead-Free MSOP-8-EP†

† Reference Application Note M538 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Plating is 100% matte tin over copper.