MASW-007921

High Power GaAs SPDT Switch
DC - 7.0 GHz

Features
- Ideal for high power SPDT switch applications including WiMAX, WLAN, Mesh Networks, and Fixed Wireless Access
- Broadband Performance: DC - 7.0 GHz
- Low Insertion Loss:
  - 0.65 dB @ 3.5 GHz, RFC to TX
  - 0.70 dB @ 3.5 GHz, RCF to RX
- High P0.1dB Compression:
  - 40 dBm @ 3V, 3.5 GHz, RFC to TX
- Fast Settling for Low Gate Lag requirements
- Lead-Free 2 mm 8-Lead PDFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible
- Asymmetrical Design for optimized performance

Description
M/A-COM’s MASW-007921 is a broadband GaAs pHEMT MMIC SPDT switch available in a lead-free 2 mm 8-Lead PDFN package. The MASW-007921 is ideally suited for applications where very small size and high linearity are required. Typical applications include WiMAX, WLAN, Mesh networks, fixed wireless access, and other higher power systems. This switch has a very high initial compression point, ideal for complex modulations such as OFDM, with large peak average power levels.

The MASW-007921 is fabricated using a 0.5 micron gate length GaAs pHEMT process. The process features full passivation for performance and reliability.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MASW-007921-TR3000</td>
<td>3000 piece reel</td>
</tr>
<tr>
<td>MASW-007921-001SMB</td>
<td>Sample Board 2.0 - 6.0 GHz Tuning</td>
</tr>
<tr>
<td>MASW-007921-002SMB</td>
<td>Sample Board 0.05 - 2.0 GHz Tuning</td>
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</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.


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## Electrical Specifications:  \( T_A = 25^\circ C, Z_0 = 50 \, \Omega, V_C = 0 \, V / 3 \, V, 4.7 \, pF \text{ Capactor}^{8,9} \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
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<tr>
<td>Insertion Loss RFC to ( R_X )</td>
<td>0.05 - 2 GHz(^{10})</td>
<td>dB</td>
<td>—</td>
<td>0.5</td>
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<tr>
<td></td>
<td>2.4 GHz</td>
<td>dB</td>
<td>—</td>
<td>0.6</td>
<td>0.85</td>
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<tr>
<td></td>
<td>3.5 GHz</td>
<td>dB</td>
<td>—</td>
<td>0.70</td>
<td>—</td>
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<tr>
<td></td>
<td>5.8 GHz</td>
<td>dB</td>
<td>—</td>
<td>0.8</td>
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<tr>
<td>Insertion Loss RFC to ( T_X )</td>
<td>0.05 - 2 GHz(^{10})</td>
<td>dB</td>
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<td></td>
<td>2.4 GHz</td>
<td>dB</td>
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<td>0.65</td>
<td>0.85</td>
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<td></td>
<td>3.5 GHz</td>
<td>dB</td>
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<td>0.65</td>
<td>—</td>
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<tr>
<td></td>
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<td>dB</td>
<td>—</td>
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<td>Isolation</td>
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<td>—</td>
<td>30</td>
<td>—</td>
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<tr>
<td>Return Loss</td>
<td>DC - 6.0 GHz</td>
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<td>10 MHz Spacing, +20 dBm</td>
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<tr>
<td>Input IP2 ( T_X )</td>
<td>10 MHz Spacing, +20 dBm</td>
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<td>dBm</td>
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<td>5.8 GHz</td>
<td>dBm</td>
<td>—</td>
<td>39</td>
<td>—</td>
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<tr>
<td>Input P0.1dB ( T_X )</td>
<td>2.4 GHz</td>
<td>dBm</td>
<td>—</td>
<td>38</td>
<td>—</td>
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<tr>
<td></td>
<td>3.5 GHz</td>
<td>dBm</td>
<td>—</td>
<td>40</td>
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<tr>
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<td>5.8 GHz</td>
<td>dBm</td>
<td>—</td>
<td>39</td>
<td>—</td>
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<tr>
<td>Trise, Tfall</td>
<td>10% to 90% RF</td>
<td>nS</td>
<td>—</td>
<td>21</td>
<td>—</td>
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<tr>
<td></td>
<td>90% to 10% RF</td>
<td>nS</td>
<td>—</td>
<td>18</td>
<td>—</td>
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<tr>
<td>Ton, Toff</td>
<td>50% control to 90% and 50% control to 10% RF</td>
<td>nS</td>
<td>—</td>
<td>40</td>
<td>—</td>
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<tr>
<td>Transients</td>
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<td>mV</td>
<td>—</td>
<td>100</td>
<td>—</td>
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<tr>
<td>Control Current</td>
<td></td>
<td>µA</td>
<td>—</td>
<td>2.0</td>
<td>10.0</td>
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</tbody>
</table>

8. For positive voltage control, external DC blocking capacitors are required on all RF ports.
9. Performance can be optimized by varying the DC blocking capacitor value. See application schematic for details.
10. 0.05 - 2.0 GHz specifications apply with 1000 pF blocking capacitors. See applications section for typical data.
Typical Performance Curves Over Temp.: $Z_0 = 50 \, \Omega$, 4.7 pF Blocking Caps on all RF Ports

**RFC to Rx Insertion Loss**

S21 (dB)

-2.0 -1.5 -1.0 -0.5 0.0

2 3 4 5 6 7 8 Frequency (GHz)

+25°C

-45°C

+85°C

**RFC to Tx Insertion Loss**

S21 (dB)

-2.0 -1.5 -1.0 -0.5 0.0

2 3 4 5 6 7 8 Frequency (GHz)

+25°C

-45°C

+85°C

**RFC to Rx Isolation**

S21 (dB)

-60 -45 -30 -15 0

2 3 4 5 6 7 8 Frequency (GHz)

+25°C

-45°C

+85°C

**RFC to Tx Isolation**

S21 (dB)

-60 -45 -30 -15 0

2 3 4 5 6 7 8 Frequency (GHz)

+25°C

-45°C

+85°C

**Rx Return Loss**

S22 (dB)

-40 -30 -20 -10 0

2 3 4 5 6 7 8 Frequency (GHz)

+25°C

-45°C

+85°C

**Tx Return Loss**

S22 (dB)

-40 -30 -20 -10 0

2 3 4 5 6 7 8 Frequency (GHz)

+25°C

-45°C

+85°C

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Truth Table

<table>
<thead>
<tr>
<th>Control VC1</th>
<th>Control VC2</th>
<th>RFC - TX</th>
<th>RFC - RX</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>On</td>
<td>Off</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>Off</td>
<td>On</td>
</tr>
</tbody>
</table>

12. Differential voltage, V (state 1) - V (state 0), must be +2.7 V minimum and must not exceed 8.0 V.
13. Positive Control: 1 = +2.9 V to +8 V, 0 = 0 V ± 0.2 V.
   Negative Control: 1 = 0 V ± 0.2 V, 0 = -2.9 V to -8.0 V.

Qualification

Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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Application Schematic

11. M/A-COM recommends connecting unused package pins (N/C) to ground as shown.
High Power GaAs SPDT Switch
DC - 7.0 GHz

Lead Free 2 mm 8-lead PDFN

Reference Application Note M538 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Applications Section

Typical Low Frequency Performance Curves: $T_A = 25^\circ C$, $Z_0 = 50 \Omega$, 1000 pF Blocking Caps on all RF Ports

**Insertion Loss**

![Insertion Loss Graph](image1)

**RFC Return Loss**

![RFC Return Loss Graph](image2)

**Isolation**

![Isolation Graph](image3)

**Tx and Rx Return Loss**

![Tx and Rx Return Loss Graph](image4)
High Power GaAs SPDT Switch
DC - 7.0 GHz

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