MAPDCC0005

Low Cost Three Way Power Splitter/Combiner
824 – 960 MHz

Features

- Small Size and Low Profile
- Excellent Insertion Loss 0.6 dB Typical
- Superior Repeatability
- Low Cost
- CSM, AMPS, CDPD, ARDIS, RAM Frequency Coverage
- Lead-Free SOIC-8 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of DS53-0001

Description

M/A-COM’s MAPDCC0005 is an IC-based monolithic power divider in a low cost SOIC-8 plastic package. This 3-way power divider is ideally suited for applications where small size, low profile, and low cost without sacrificing Performance, are required. Typical applications include Base Stations, portables and PCMCIA cards for cellular applications. Available in Tape and Reel.

The MAPDCC0005 is fabricated using a passive-integrated circuit process. The process features full-chip passivation for increased performance and reliability.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAPDCC0005</td>
<td>Bulk Packaging</td>
</tr>
<tr>
<td>MAPDCC0005STR</td>
<td>1000 piece reel</td>
</tr>
<tr>
<td>MAPDCC0005-TB</td>
<td>Sample Test Board</td>
</tr>
</tbody>
</table>

Functional Block Diagram

1. All unused pins must be RF and DC grounded.

Pin Configuration

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Function</th>
<th>Pin No.</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>GND</td>
<td>5</td>
<td>RF OUT</td>
</tr>
<tr>
<td>2</td>
<td>RF IN</td>
<td>6</td>
<td>GND</td>
</tr>
<tr>
<td>3</td>
<td>GND</td>
<td>7</td>
<td>RF OUT</td>
</tr>
<tr>
<td>4</td>
<td>GND</td>
<td>8</td>
<td>RF OUT</td>
</tr>
</tbody>
</table>

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Electrical Specifications: \( T_A = 25°C, Z_0 = 50\Omega \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Units</th>
<th>Min</th>
<th>Typ</th>
<th>Max</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss above 4.78 dB</td>
<td>dB</td>
<td>—</td>
<td>0.6</td>
<td>0.7</td>
</tr>
<tr>
<td>Isolation</td>
<td>dB</td>
<td>15</td>
<td>18</td>
<td>—</td>
</tr>
<tr>
<td>VSWR</td>
<td></td>
<td>—</td>
<td>1.4:1</td>
<td>1.6:1</td>
</tr>
<tr>
<td>Amplitude Balance</td>
<td>dB</td>
<td>—</td>
<td>0.6</td>
<td>0.8</td>
</tr>
<tr>
<td>Phase Balance</td>
<td>Deg</td>
<td>—</td>
<td>2</td>
<td>4</td>
</tr>
</tbody>
</table>

Absolute Maximum Ratings\(^{2,3}\)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power(^4)</td>
<td>1W CW</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
</tbody>
</table>

2. Exceeding any one or combination of these limits may cause permanent damage to this device.
3. M/A-COM does not recommend sustained operation near these survivability limits.
4. With internal load dissipation of 0.125 W Maximum.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

GMIC Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.
Typical Performance Curves

**Insertion Loss vs. Frequency**

**VSWR Input vs. Frequency**

**Isolation vs. Frequency**

**VSWR Output vs. Frequency**
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Lead-Free, SOIC-8†

NOTES:
1. REFERENCE JEDEC MS-012-AA; FOR ADDITIONAL DIMENSIONAL AND TOLERANCE INFORMATION.
2. REFERENCE M538 APPLICATION NOTE FOR FOOTPRINT INFORMATION.
3. ALL DIMENSIONS SHOWN AS INCHES/MM.

† Reference Application Note M538 for lead-free solder reflow recommendations.
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