

MAPC-A2004-B

Rev. V1

Features

- Optimized for Cellular Base Station Applications
- Designed for Digital Predistortion Error Correction Systems
- High Terminal Impedances for Broadband Performance
- 50 V Operation
- Compatible with MACOM Power Management Bias Controller/Sequencer MABC-11040
- 100% RF Tested
- RoHS* Compliant

Description

The MAPC-A2004-B is a high power GaN on Silicon Carbide HEMT D-mode amplifier designed for 5G base station applications and optimized for 3.3 - 3.8 GHz modulated signal operation. This device supports pulsed and linear operation with peak output power levels to 90 W (49.5 dBm) in an 7.0 x 6.5 mm DFN package.

Typical RF Performance

WCDMA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF, V_{DS} = 50 V, I_{DQCAR} = 100 mA, V_{GSP} = -4.4 V, T_C = 25°C, P_{OUT} = 40.3 dBm

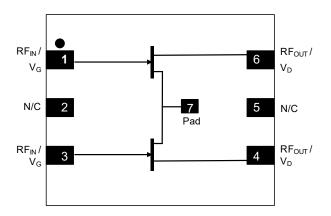
Frequency (GHz)	G _P (dB)	η _D (%)	Output PAR (dB)	ACPR (dBc)
3.4	14.1	46	7.9	-29
3.6	13.7	43	8.4	-40
3.8	13.1	48	7.9	-30

Ordering Information

Part Number	Package
MAPC-A2004-BD000	Bulk Quantity
MAPC-A2004-BDTR1	Tape and Reel
MAPC-A2004-BDSB1	Doherty Sample Board



Functional Schematic



Pin Configuration

Pin#	Pin Name	Function		
1	RF _{IN} / V _G	RF Input / Gate (Carrier)		
2,5	N/C	No Connection		
6	RF _{OUT} / V _D	RF Output / Drain (Carrier)		
3	RF _{IN} / V _G	RF Input / Gate (Peaking)		
4	RF _{OUT} / V _D	RF Output / Drain (Peaking)		
7	Pad ¹	Ground / Source		

 The pad on the package bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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RF Electrical Characteristics: T_C = 25°C, V_{DS} = 50 V , I_{DQCAR} = 100 mA, V_{GSP} = -4.4 V Note: Performance in MACOM Doherty Evaluation Test Fixture, 50 Ω system

Parameter	Test Conditions	Test Conditions Symbol		Тур.	Max.	Units
Small Signal Gain	Pulsed ² , 3.6 GHz	ed ² , 3.6 GHz G _{SS} - 15.1		-	dB	
Saturated Output Power	Pulsed ² , 3.6 GHz	P _{SAT}	-	49.3	-	dBm
Drain Efficiency at Saturation	Pulsed ² , 3.6 GHz	η _{SAT}	-	58	-	%
AM/PM	Pulsed ² , 3.6 GHz	Φ	-	4	-	0
Modulated Peak Power	WCDMA ³ , 3.6 GHz	P2.5dB ⁴	-	49.8	-	dBm
Gain Flatness in 60 MHz	WCDMA ³ , P _{OUT} = 40.3 dBm	G _F	-	0.3	-	dB
Gain Variation (-25°C to +105°C)	WCDMA ³ , 3.6 GHz, P _{OUT} = 40.3 dBm	ΔG	-	0.01	-	dB/°C
Power Variation (-25°C to +105°C)	Pulsed ² , 3.6 GHz	Δ P2.5dB ⁴	-	0.01	-	dB/°C
Power Gain	WCDMA ³ , 3.6 GHz, P _{OUT} = 40.3 dBm	G _P	-	13.7	-	dB
Drain Efficiency	WCDMA ³ , 3.6 GHz, P _{OUT} = 40.3 dBm	η	-	43	-	%
Output PAR @ 0.01% CCDF	WCDMA ³ , 3.6 GHz, P _{OUT} = 40.3 dBm	PAR	-	8.4	-	dB
Adjacent Channel Power Ratio	WCDMA ³ , 3.6 GHz, P _{OUT} = 40.3 dBm	ACPR	-	-40	-	dBc
Input Return Loss	WCDMA ³ , 3.6 GHz, P _{OUT} = 40.3 dBm	IRL	-	-16	-	dB
Ruggedness: Output Mismatch	All phase angles	Ψ	VSWR = 10:1, No Device Damage		vice	

RF Electrical Characteristics: $T_A = 25$ °C, $V_{DS} = 50$ V, $I_{DQCAR} = 90$ mA, $V_{GSP} = -3.6$ V Note: Performance in MACOM Doherty Production Test Fixture, 50 Ω system

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Power Gain	WCDMA ³ , 3.65 GHz, P _{OUT} = 40.3 dBm	G_P	10.4	11.7	-	dB
Drain Efficiency	WCDMA ³ , 3.65 GHz, P _{OUT} = 40.3 dBm	η	27	31	-	%
Output PAR @ 0.01% CCDF	WCDMA ³ , 3.65 GHz, P _{OUT} = 40.3 dBm	PAR	6.7	7.7	-	dB
Input Return Loss	WCDMA ³ , 3.65 GHz, P _{OUT} = 40.3 dBm	IRL	-	-12	-5	dB

^{2.} Pulse details: 100 µs pulse width, 10% Duty Cycle

Modulated Signal: 3.84 MHz, WCMDA 3GPP TM1 64 DPCH, 9.9 dB PAR @ 0.01% CCDF
P2.5dB = P_{OUT} + 7.5 dB where P_{OUT} is the average output power measured using a modulated signal³ where the output PAR is compressed to 7.5 dB @ 0.01% probability CCDF.



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DC Electrical Characteristics: T_c = 25°C

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units	
Carrier Amplifier							
Drain-Source Breakdown Voltage	$V_{GS} = -8 \text{ V}, I_D = 3.7 \text{ mA}$	V_{BDS}	130	-	-	V	
Gate-Source Leakage Current	V _{GS} = -8 V, V _{DS} = 0 V	I _{GLK}	-	0.003	-	mA	
Gate-Source Leakage Current	V _{GS} = -8 V, V _{DS} = 50 V	I _{GLK}	-	-	0.3	mA	
Gate Threshold Voltage	$V_{DS} = 50 \text{ V}$, $I_{D} = 3.7 \text{ mA}$	V _T	-4.0	-2.9	-	V	
Gate Quiescent Voltage	V_{DS} = 50 V , I_{D} = 90 mA	V_{GSQ}	-2.9	-2.5	-1.9	V	
Maximum Drain Current	V_{DS} = 7 V pulsed, pulse width 300 µs	I _{D,MAX}	-	3.1	-	Α	
	Peaking Amplifier						
Drain-Source Breakdown Voltage	$V_{GS} = -8 \text{ V}, I_D = 6.72 \text{ mA}$	V_{BDS}	130	-	•	V	
Gate-Source Leakage Current	V _{GS} = -8 V, V _{DS} = 0 V	I _{GLK}	-	0.006	-	mA	
Gate-Source Leakage Current	V _{GS} = -8 V, V _{DS} = 50 V	I _{GLK}	-	-	0.55	mA	
Gate Threshold Voltage	$V_{DS} = 50 \text{ V}$, $I_{D} = 6.72 \text{ mA}$	V _T	-4.0	-2.9	-	V	
Gate Quiescent Voltage	V _{DS} = 50 V , I _D = 120 mA	V_{GSQ}	-2.9	-2.5	-1.9	V	
Maximum Drain Current	V _{DS} = 7 V pulsed, pulse width 300 μs	I _{D,MAX}	-	5.7	-	Α	



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Absolute Maximum Ratings^{5,6,7,8,9}

Parameter	Absolute Maximum
Drain Source Voltage, V _{DS}	130 V
Gate Source Voltage, V _{GS}	-10 to 3 V
Gate Current (Carrier), I _G	3.7 mA
Gate Current (Peaking), I _G	6.72 mA
Storage Temperature Range	-65°C to +150°C
Case Operating Temperature Range	-40°C to +120°C
Channel Operating Temperature Range, T _{CH}	-40°C to +225°C
Absolute Maximum Channel Temperature	+250°C

Exceeding any one or combination of these limits may cause permanent damage to this device.

Thermal Characteristics 10

Parameter	Test Conditions	Symbol	Typical	Units
Thermal Resistance using Finite Element Analysis	$V_{DS} = 50 \text{ V}$ $T_{C} = 85^{\circ}\text{C}, T_{CH} = 225^{\circ}\text{C}$	$R_{\theta}(FEA)$	5.2	°C/W
Thermal Resistance using Infrared Measurement of Die Surface Temperature	V _{DS} = 50 V T _C = 85°C,T _{CH} = 225°C	$R_{\theta}(IR)$	4.1	°C/W

^{10.} Case temperature measured using thermocouple embedded in heat-sink. Contact local applications support team for more details on this measurement.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Nitride Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

MACOM does not recommend sustained operation above maximum operating conditions.

Operating at drain source voltage $V_{DS} < 55 \text{ V}$ will ensure MTTF > 2 x 10^6 hours. Operating at nominal conditions with $T_{CH} \le 225^{\circ}\text{C}$ will ensure MTTF > 2 x 10^6 hours. MTTF may be estimated by the expression MTTF (hours) = A $e^{[B+C/(T+273)]}$ where T is the channel temperature in degrees Celsius, A = 1.93, B = -45.31, and C = 29,585.



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Pulsed² Load-Pull Performance: Reference Plane at Device Leads

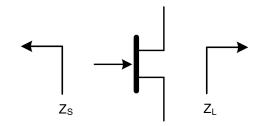
		Carrier Amplifier: Maximum Output Power						
			V _{DS} = 50 V, I _{DQ} = 70 mA, T _C = 25°C, P2.5dB					
Frequency (GHz)	Z _{source} (Ω)	Z _{LOAD} ¹¹ (Ω)	Gain (dB)	P _{OUT} (dBm)	P _{OUT} (W)	η _□ (%)	AM/PM (°)	
3.3	25.9 - j55.7	11.5 + j0.5	16.0	46.0	42.7	59	-36.5	
3.6	66.3 - j13.9	10.3 - j1.2	15.5	45.9	43.7	60	-75.9	
3.8	30.8 - j1.1	9.7 - j1.3	15.2	46.0	42.7	64	-112.0	

		Carrier Amplifier: Maximum Drain Efficiency						
			$V_{DS} = 50 \text{ V}, I_{DQ} = 70 \text{ mA}, T_{C} = 25^{\circ}\text{C}, P2.5 dB}$					
Frequency (GHz)	Z _{source} (Ω)	Z _{LOAD} ¹² (Ω)	Gain (dB)	P _{OUT} (dBm)	P _{OUT} (W)	η₀ (%)	AM/PM (°)	
3.3	29.0 - j64.6	7.3 + j5.5	17.6	44.8	24.5	65	-42.7	
3.6	71.1 + j0.8	6.7 + j3.6	17.1	44.7	30.9	68	-90.6	
3.8	24.3 - j1.6	6.3 + j3.3	16.5	44.4	25.1	72	-129.0	

		Peaking Amplifier: Maximum Output Power						
			V _{DS} = 50 V, I _{DQ} = 134 mA, T _C = 25°C, P2.5dB					
Frequency (GHz)	Z _{SOURCE} (Ω)	Z _{LOAD} ¹¹ (Ω)	Gain (dB)	P _{OUT} (dBm)	P _{OUT} (W)	η _D (%)	AM/PM (°)	
3.3	7.6 - j43.6	6.6 - j6.9	16.1	48.1	81.3	54	-27.7	
3.6	25.7 - j54.6	6.2 - j9.0	15.5	48.1	79.4	55	-58.3	
3.8	44.8 - j36.8	5.8 - j9.8	15.3	48.1	81.3	65	-86.1	

		Peaking Amplifier: Maximum Drain Efficiency							
			V _{DS} = 50 V, I _{DQ} = 134mA, T _C = 25°C, P2.5dB						
Frequency (GHz)	Z _{SOURCE} (Ω)	Z _{LOAD} ¹² (Ω)	Gain (dB)	P _{OUT} (dBm)	P _{OUT} (W)	η _□ (%)	AM/PM (°)		
3.3	7.5 - j45.3	5.2 - j3.9	17.2	47.3	66.1	59	-39.9		
3.6	30.1 - j56.8	4.8 - j6.4	16.5	47.3	61.7	61	-72.2		
3.8	50.2 - j27.1	4.2 - j7.4	16.5	47.1	50.1	65	-105.7		

Impedance Reference



- Z_{SOURCE} = Measured impedance presented to the input of the device at package reference plane. Z_{LOAD} = Measured impedance presented to the output of the
- device at package reference plane.
- 11. Load Impedance for optimum output power.
- 12. Load Impedance for optimum efficiency.

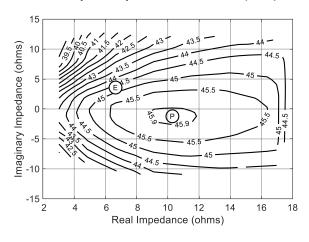


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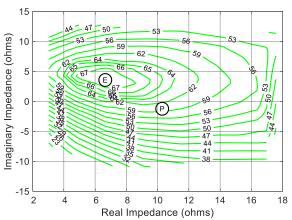
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Pulsed² Load-Pull Performance: Carrier Amplifier 3.6 GHz

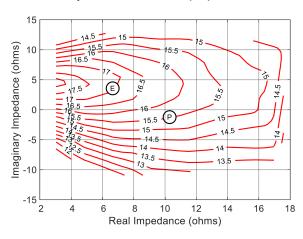
P2.5dB Loadpull Output Power Contours (dBm)



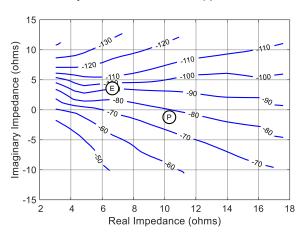
P2.5dB Loadpull Drain Efficiency Contours (%)



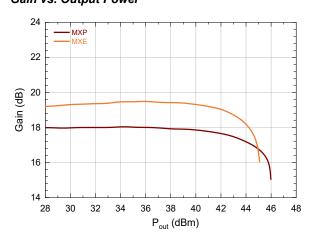
P2.5dB Loadpull Gain Contours (dB)



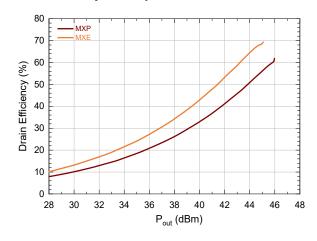
P2.5dB Loadpull AM/PM Contours (°)



Gain vs. Output Power



Drain Efficiency vs. Output Power



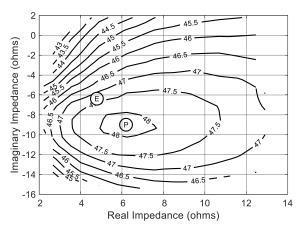


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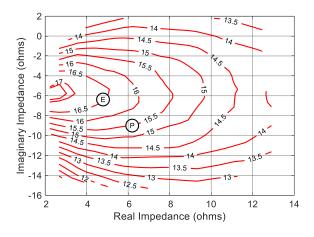
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Pulsed² Load-Pull Performance: Peaking Amplifier 3.6 GHz

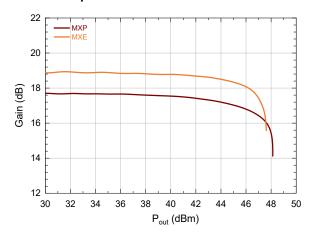
P2.5dB Loadpull Output Power Contours (dBm)



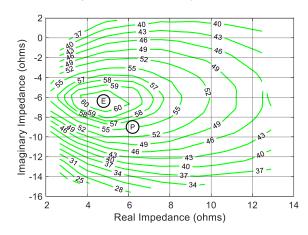
P2.5dB Loadpull Gain Contours (dB)



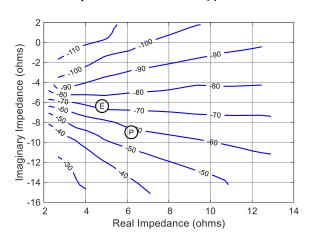
Gain vs. Output Power



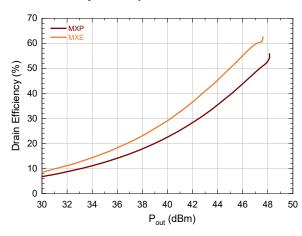
P2.5dB Loadpull Drain Efficiency Contours (%)



P2.5dB Loadpull AM/PM Contours (°)



Drain Efficiency vs. Output Power

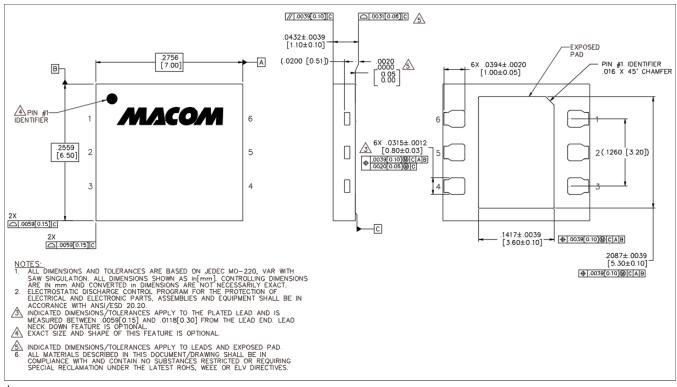




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Lead-Free 7.0 x 6.5 mm 6-Lead Package Dimensions[†]



^T Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level (MSL) 3 requirements. Plating is NiPdAu.

GaN Amplifier 50 V, 90 W 3.3 - 3.8 GHz



MACOM PURE CARBIDE

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