

# GaN on SiC Transistor, 1200 W, 65 V

## 1.2 - 1.4 GHz



**MACOM PURE CARBIDE™**

**MAPC-A1558-AB**

Rev. V1

### Features

- Saturated Power: 1200 W
- Drain Efficiency: 68%
- Small Signal Gain: 16 dB
- Lead-Free Air Cavity Ceramic Package
- RoHS\* Compliant

### Applications

- Avionics - TACAN, DME, IFF
- L-band Radar
- General Amplification

### Description

The MAPC-A1558-AB is an 1200W packaged, partially-matched amplifier. The MAPC-A1558-AB operates up to 1.4 GHz and supports both defense and commercial-related avionics and radar applications. Under 65 V operation, the MAPC-A1558-AB typically achieves 1200 W of saturated output power with 16 dB of large signal gain and 68% drain efficiency via a 1.2 - 1.4 GHz reference design.

Packaged in a thermally-enhanced, flange package, the MAPC-A1558-AB provides superior performance under long pulse operation allowing customers to improve SWaP-C benchmarks in their next-generation systems.

### Typical RF Performance:

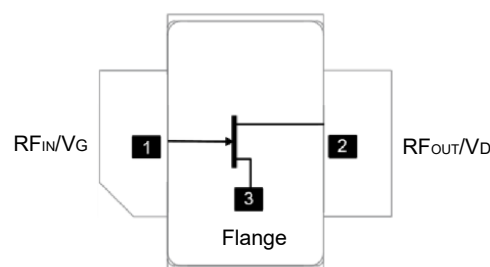
- Pulsed measurement,  $P_{IN} = 45$  dBm,  $V_{DS} = 65$  V,  $I_{DQ} = 800$  mA,  $T_C = 25^\circ\text{C}$

Frequency (GHz)	Output Power (dBm)	Gain (dB)	$\eta_D$ (%)
1.2	61.0	16.0	70.7
1.3	60.9	15.9	68.6
1.4	60.6	15.6	67.5



**AC-780B-2**

### Functional Schematic



### Pin Configuration

Pin #	Pin Name	Function
1	$RF_{IN} / V_G$	RF Input / Gate
2	$RF_{OUT} / V_D$	RF Output / Drain
3	Flange <sup>1</sup>	Ground / Source

1. The flange on the package bottom must be connected to RF, DC and thermal ground.

### Ordering Information

Part Number	MOQ Increment
MAPC-A1558-AB000	Bulk Quantity: Bolt-down
MAPC-A1558-ABSB1	Sample Board: Bolt-down

\* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

RF Electrical Specifications<sup>2</sup>: Freq. = 1.2 and 1.4 GHz,  
Pulse Width 100  $\mu$ s, 10% Duty Cycle,  $P_{IN} = 45$  dBm,  $T_A = +25^\circ\text{C}$ ,  $V_{DS} = 65$  V,  $I_{DQ} = 800$  mA

Parameter	Conditions	Symbol	Min.	Typ.	Max.	Units
Output Power	1.2 GHz 1.4 GHz	$P_{OUT}$	1000 1000	1230 1175	—	W
Drain Efficiency	1.2 GHz 1.4 GHz	$\eta$	69 65	75 71	—	%
Power Gain	1.2 GHz 1.4 GHz	$G_P$	15.0 15.0	15.9 15.7	—	dB

2. Final testing and screening for all transistor sales is performed using the MAPC-A1558-AB production test fixture at 1.2 GHz and 1.4 GHz.

### Absolute Maximum Ratings<sup>3,4</sup>

Parameter	Absolute Maximum
Drain-Source Voltage	150 V
Gate Voltage	-10 V, +2 V
Drain Current	24 A
Gate Current	133 mA
Input Power	47 dBm
Junction Temperature <sup>5,6</sup>	+225°C
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C
Mounting Temperature	+260°C

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- Operating at nominal conditions with  $T_J \leq +225^\circ\text{C}$  will ensure  $MTTF > 1 \times 10^6$  hours.
- Junction Temperature ( $T_J = T_C + \Theta_{jc} * (V * I)$ )  
Typical thermal resistance ( $\Theta_{jc}$ ) = 0.146 °C/W for pulse width = 100  $\mu$ s, Duty Cycle = 10%.  
a) For  $T_C = +85^\circ\text{C}$ ,  
 $T_J = 180^\circ\text{C} @ P_{DISS} = 650$  W

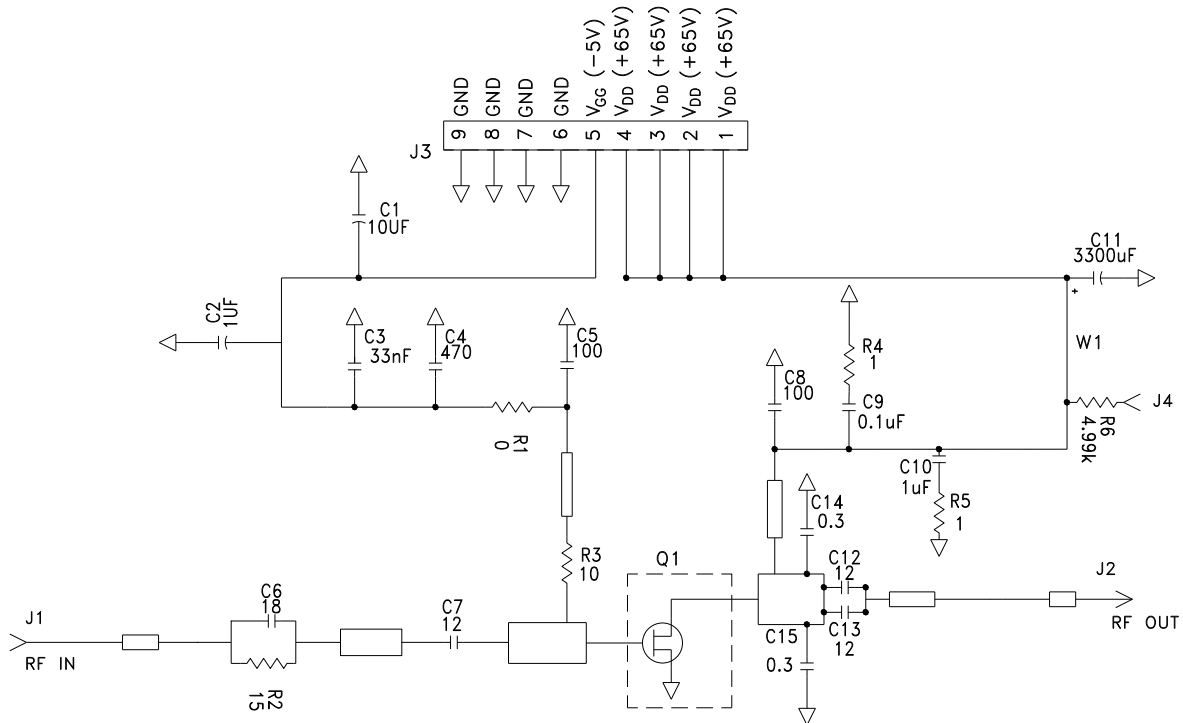
### Handling Procedures

Please observe the following precautions to avoid damage:

### Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Evaluation Test Fixture and Recommended Tuning Solution, 1.2 - 1.4 GHz



**Description**

Parts measured on evaluation board (25-mil thick RO3010). Matching is provided using a combination of lumped elements and transmission lines as shown in the simplified schematic above. Recommended tuning solution component placement, transmission lines, and details are shown on the next page.

**Biasing Sequence**

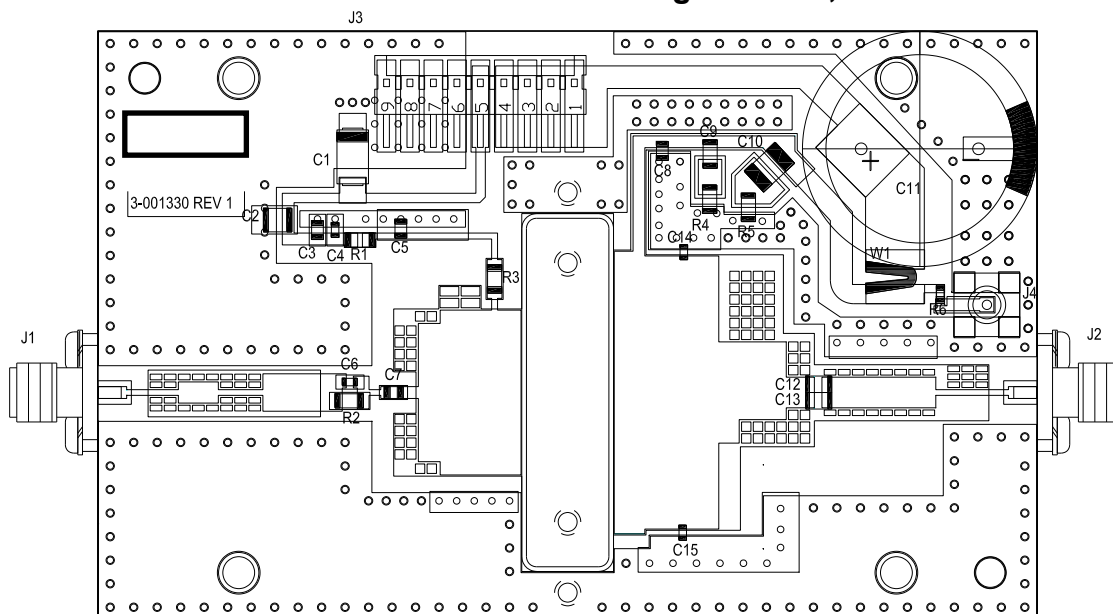
**Bias ON**

1. Ensure RF is turned off
2. Apply pinch-off voltage of -5 V to the gate
3. Apply nominal drain voltage
4. Bias gate to desired quiescent drain current
5. Apply RF

**Bias OFF**

1. Turn RF off
2. Apply pinch-off voltage of -5 V to the gate
3. Turn-off drain voltage
4. Turn-off gate voltage

**Evaluation Test Fixture and Recommended Tuning Solution, 1.2 - 1.4 GHz**



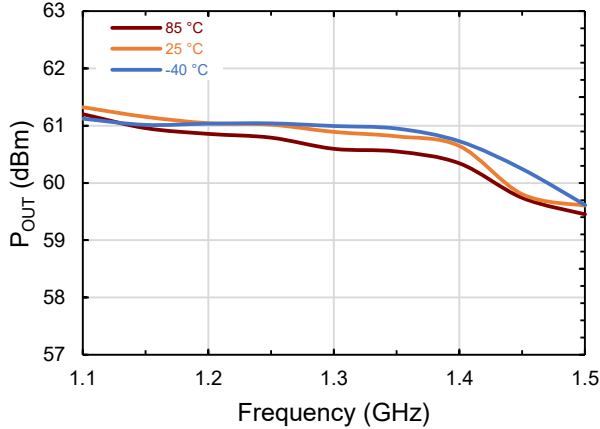
**Assembly Parts List**

Reference Designator	Description	Manufacturer	Part Number
R1	RES, 1/4W, 1206, 1%, 0 OHM	KOA	RK73Z2BTDD
R2	RES, 15 Ohm, 5%, 1/4W, 1206	KOA	RK73B2BTDD150J
R3	RES, 10 OHM, 5%, 1/4W 1206	Bourns	CR1206-JX100 E LF
R4, R5	RES, 1 Ohm 5%, 1/4W, 1206	XICON	263-1.0-RC
R6	RES, 1/16W, 0603, 1%, 4.99K OHMS	Vishay	CRCW06034K99FKTA
C1	CAP 10UF 16V TANTALUM	Kemet	T496C106K016ATE2K0
C2, C10	CAP, 1.0UF, 100V, 10%, X7R, 1210	Murata	GRM32ER72A105KA01L
C3	CAP, 0.033UF, 100V, X7R, 0805	Murata	GRM21BR72A333KA01L
C4	CAP, 470pF, 0603, 250V, C0G	Murata	GCM1885C1H471JA16J
C5, C8	CAP, 100PF, +/-5%, 250V, 0805, ATC 600F	KAVX	600F101JT250XT
C6	CAP, 18pF, +/-5%, 250V, 0603, ATC 600S	KAVX	600S180JT250XT
C7	CAP, 12 PF, +/- 5%, 250V, 0805, ATC 600F	KAVX	600F120JT250XT
C9	CAP, 0.1UF, 250V, X7R, 1206	KEMET	C1206C104KARAC7800
C11	CAP, 3300 UF, +/-20%, 100V, ELECTROLYTIC	NICHICON	UFW2A332MRD
C12, C13	CAP, 12PF, +/- 2%, 500V, ATC800B	KAVX	800B120GT500XT
C14, C15	CAP, 0.3PF, +/- 0.05pF, 0603, ATC 600S	KAVX	600S0R3AT250XT
J1, J2	CONN, SMA, PANEL MOUNT JACK, FLANGE, 4-HOLE, BLUNT POST	Amphenol RF	132150
J3	HEADER RT>PLZ .1CEN LK 9POS	AMP	640457-9
J4	CONNECTOR; SMB, Straight, JACK, SMD	JOHNSON	131-3711-201
Q1	MACOM GaN Power Amplifier		MAPC-A1558-AB
PCB	Rogers 3010, 25mil, 2 oz Cu		

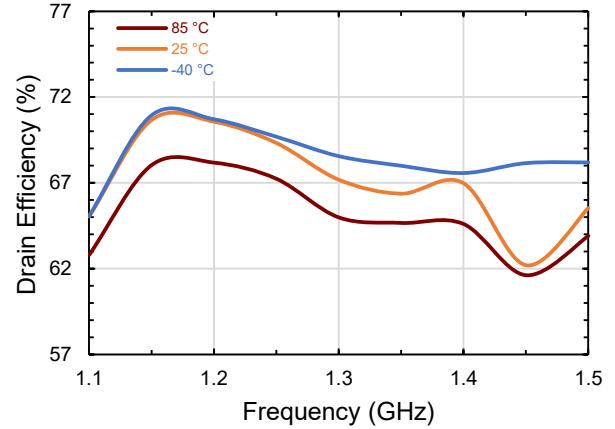
**Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture**

Pulsed 100  $\mu$ s 10%,  $P_{IN}$  = 45 dBm,  $V_{DS}$  = 65 V,  $I_{DQ}$  = 800 mA, Frequency = 1.3 GHz (unless otherwise noted)  
For Engineering Evaluation Only – This data does not Modify MACOM's Datasheet Limits.

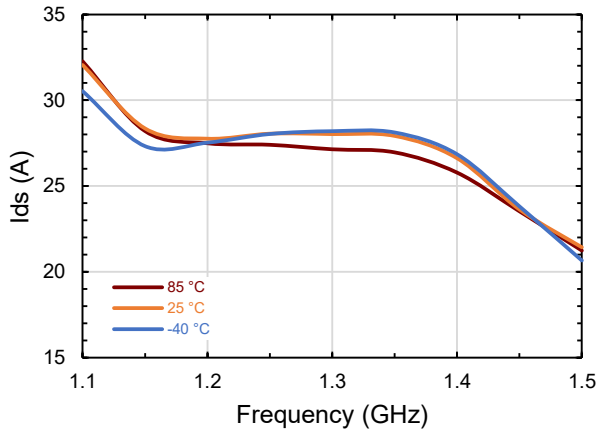
**Output Power vs. Temperature and Frequency**



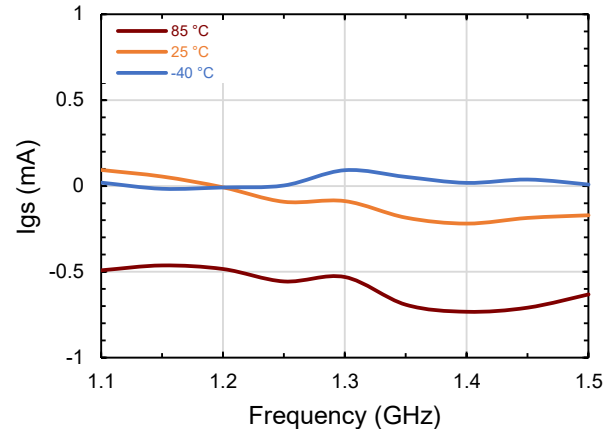
**Drain Efficiency vs. Temperature and Frequency**



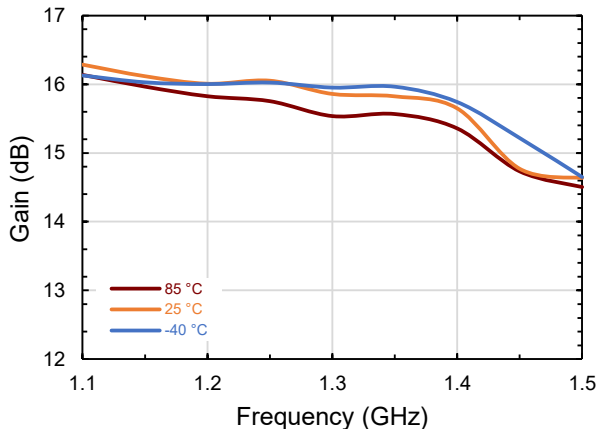
**Drain Current vs. Temperature and Frequency**



**Gate Current vs. Temperature and Frequency**



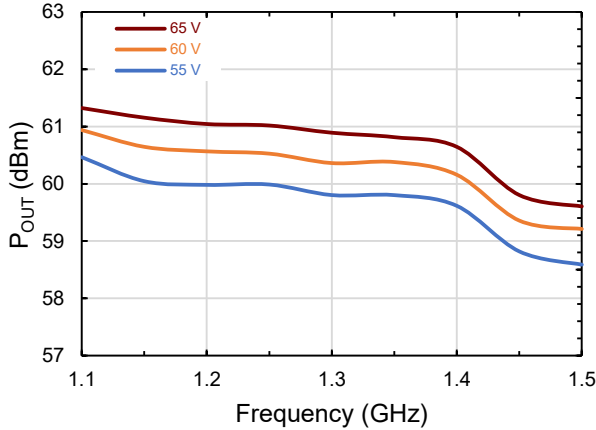
**Large Signal Gain vs. Temperature and Frequency**



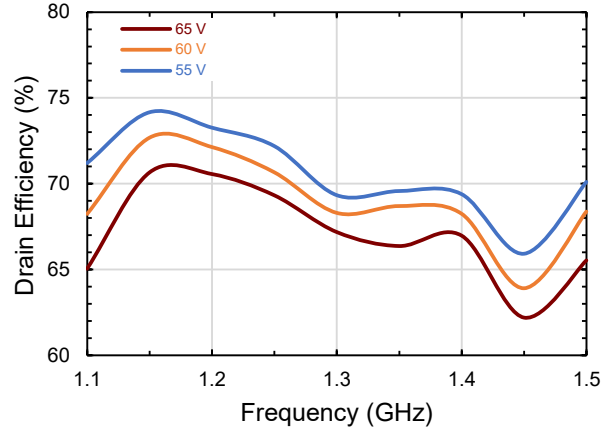
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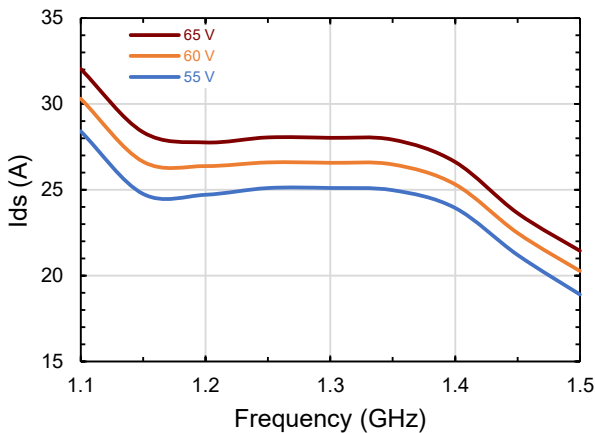
**Output Power vs.  $V_{DS}$  and Frequency**



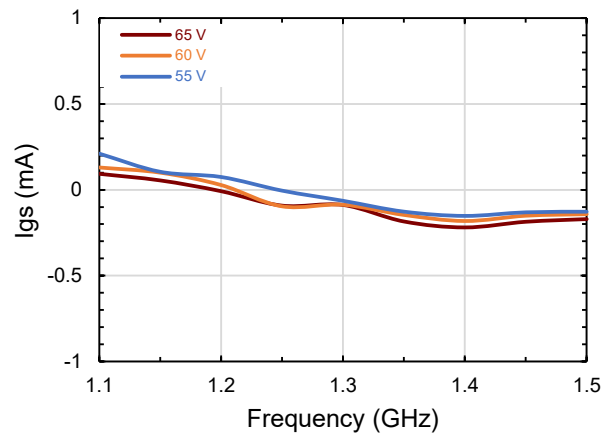
**Drain Efficiency vs.  $V_{DS}$  and Frequency**



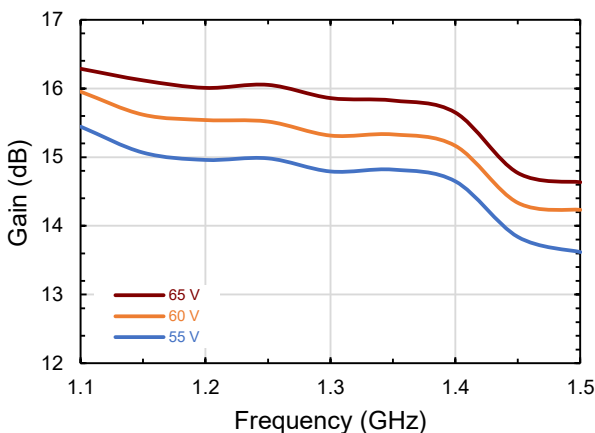
**Drain Current vs.  $V_{DS}$  and Frequency**



**Gate Current vs.  $V_{DS}$  and Frequency**



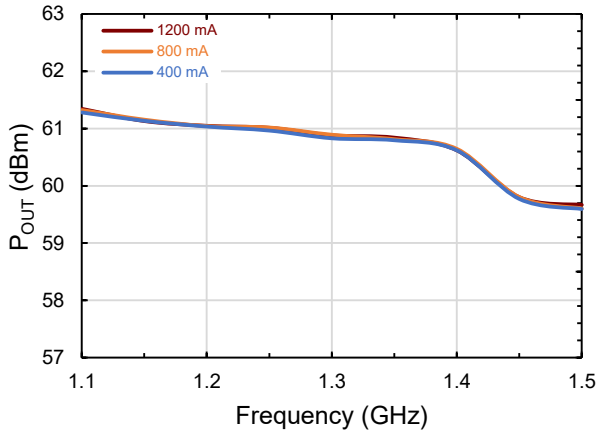
**Large Signal Gain vs.  $V_{DS}$  and Frequency**



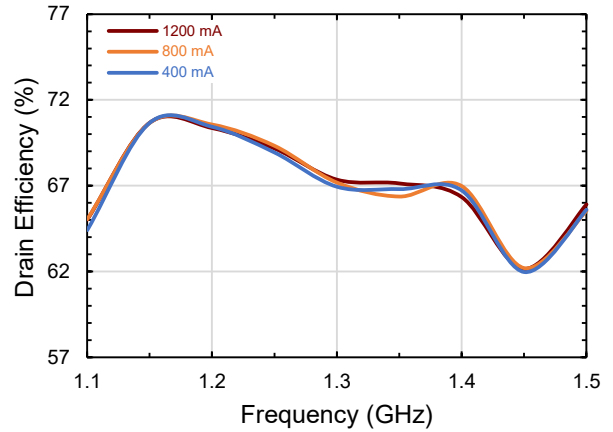
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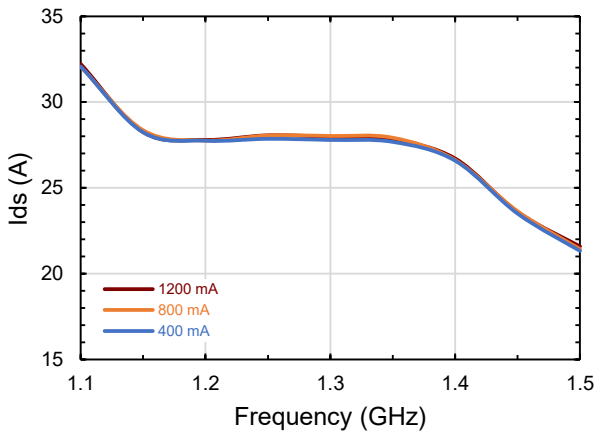
**Output Power vs.  $I_{DQ}$  and Frequency**



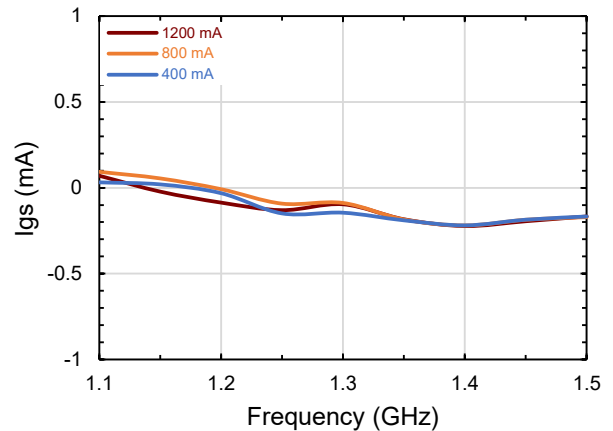
**Drain Efficiency vs.  $I_{DQ}$  and Frequency**



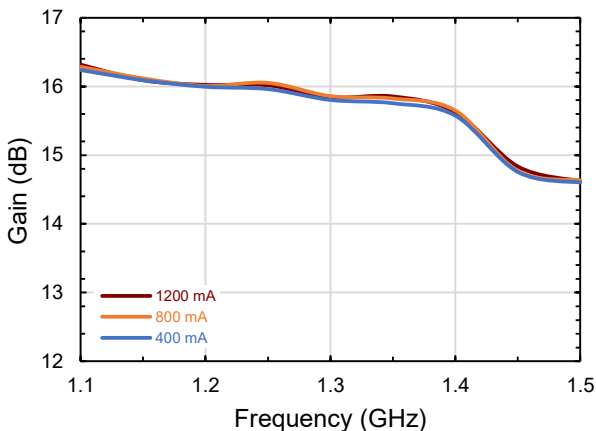
**Drain Current vs.  $I_{DQ}$  and Frequency**



**Gate Current vs.  $I_{DQ}$  and Frequency**



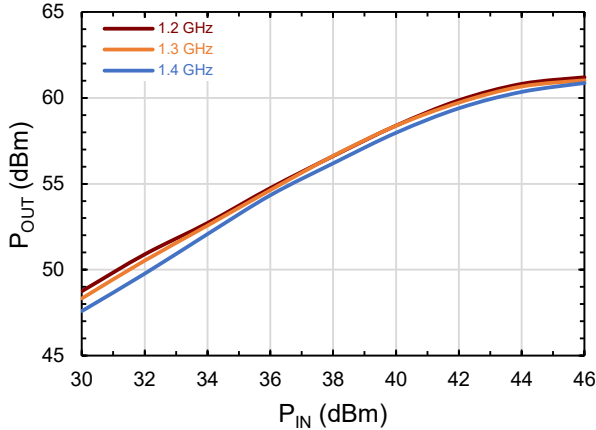
**Large Signal Gain vs.  $I_{DQ}$  and Frequency**



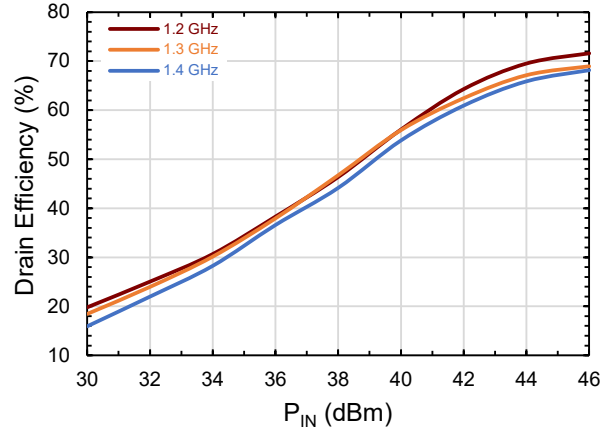
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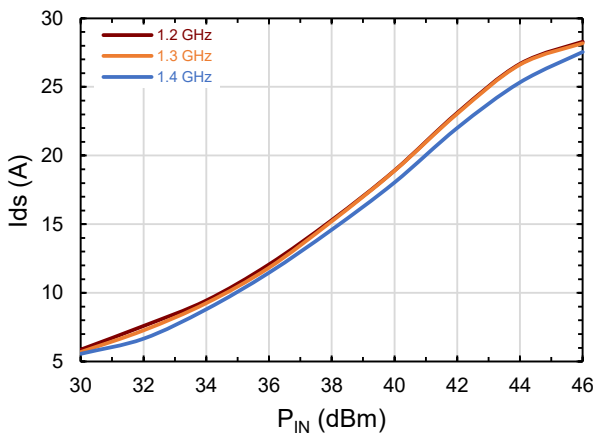
**Output Power vs. Frequency and  $P_{IN}$**



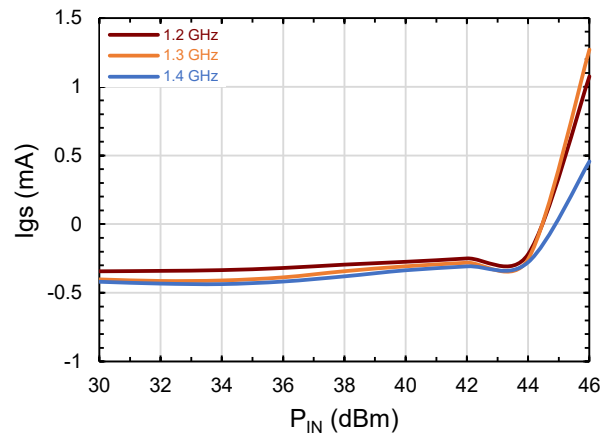
**Drain Efficiency vs. Frequency and  $P_{IN}$**



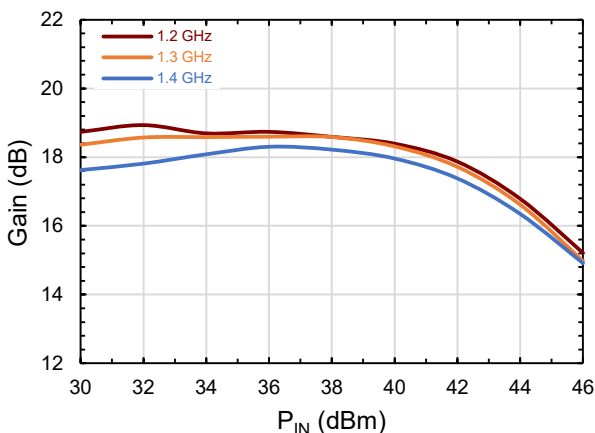
**Drain Current vs. Frequency and  $P_{IN}$**



**Gate Current vs. Frequency and  $P_{IN}$**



**Large Signal Gain vs. Frequency and  $P_{IN}$**

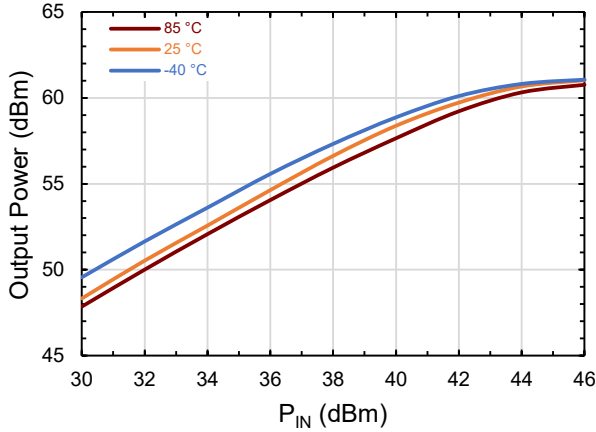




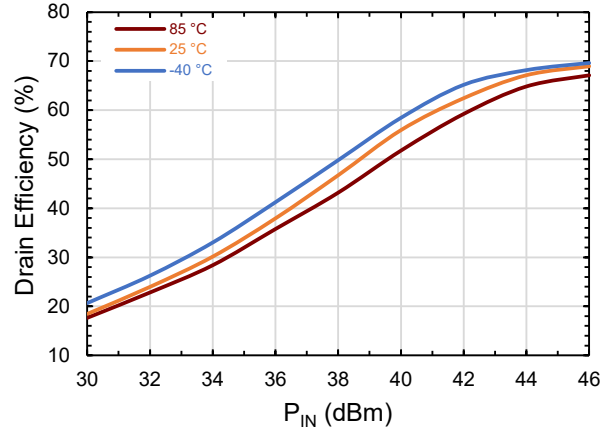
**Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture**

Pulsed 100  $\mu$ s 10%,  $P_{IN}$  = 45 dBm,  $V_{DS}$  = 65 V,  $I_{DQ}$  = 800 mA, Frequency = 1.3 GHz (unless otherwise noted)  
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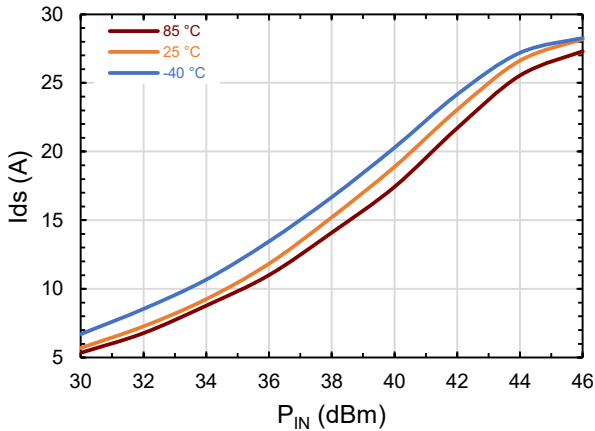
**Output Power vs. Temperature and  $P_{IN}$**



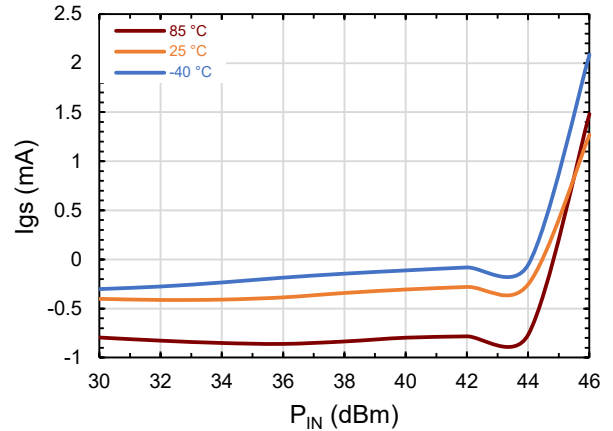
**Drain Efficiency vs. Temperature and  $P_{IN}$**



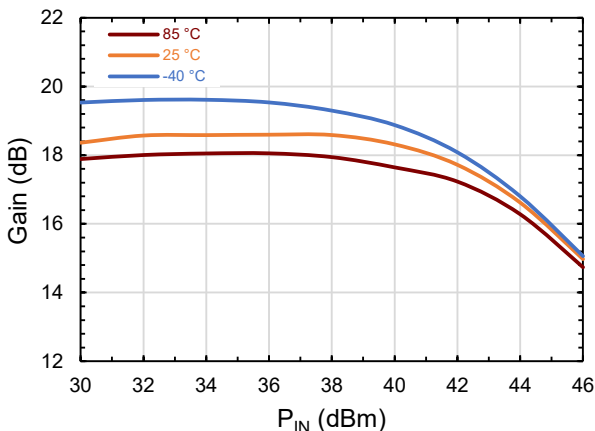
**Drain Current vs. Temperature and  $P_{IN}$**



**Gate Current vs. Temperature and  $P_{IN}$**



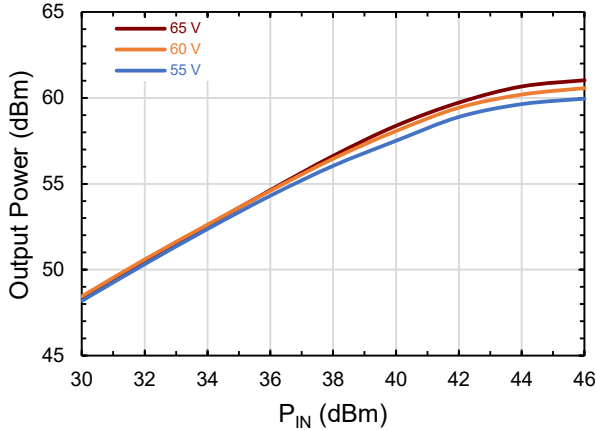
**Large Signal Gain vs. Temperature and  $P_{IN}$**



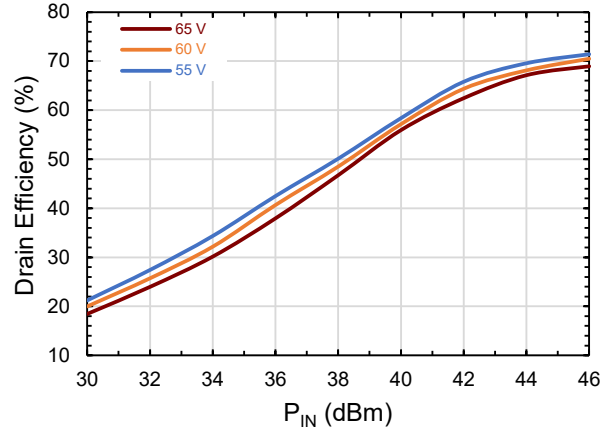
**Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture**

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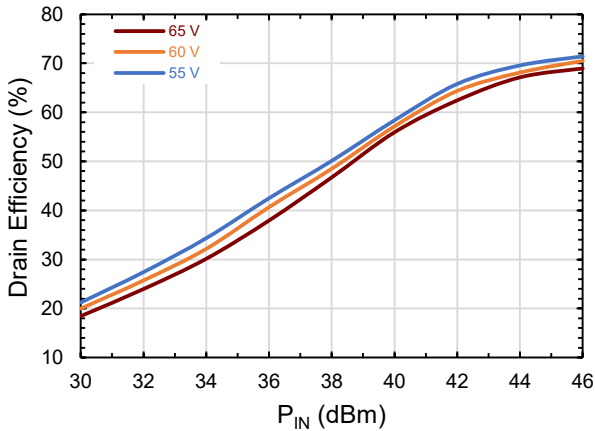
**Output Power vs.  $V_{DS}$  and  $P_{IN}$**



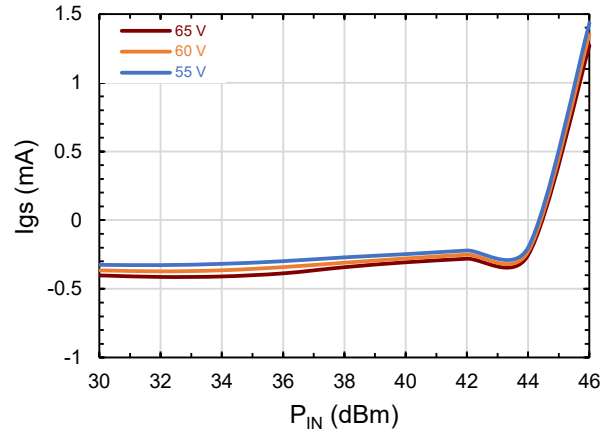
**Drain Efficiency vs.  $V_{DS}$  and  $P_{IN}$**



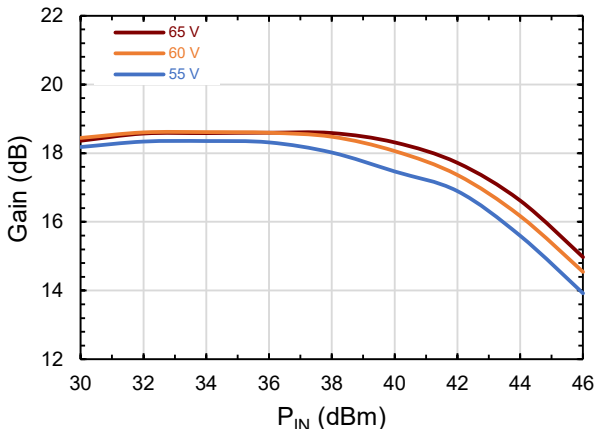
**Drain Current vs.  $V_{DS}$  and  $P_{IN}$**



**Gate Current vs.  $V_{DS}$  and  $P_{IN}$**



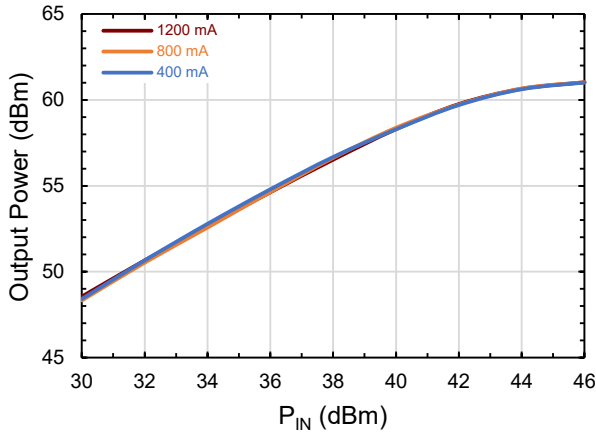
**Large Signal Gain vs.  $V_{DS}$  and  $P_{IN}$**



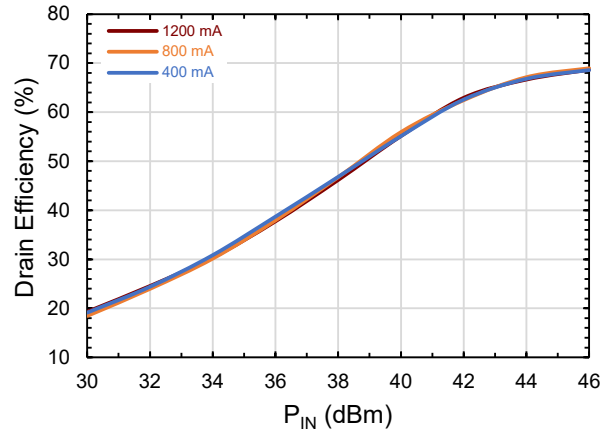
**Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture**

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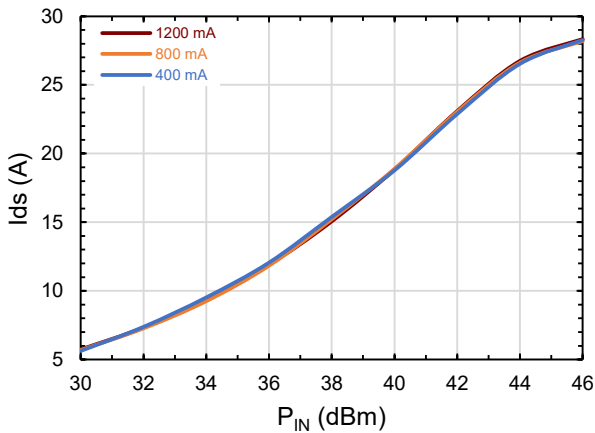
**Output Power vs.  $I_{DQ}$  and  $P_{IN}$**



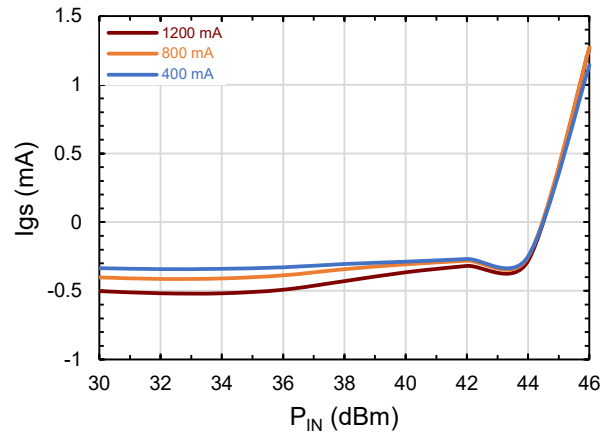
**Drain Efficiency vs.  $I_{DQ}$  and  $P_{IN}$**



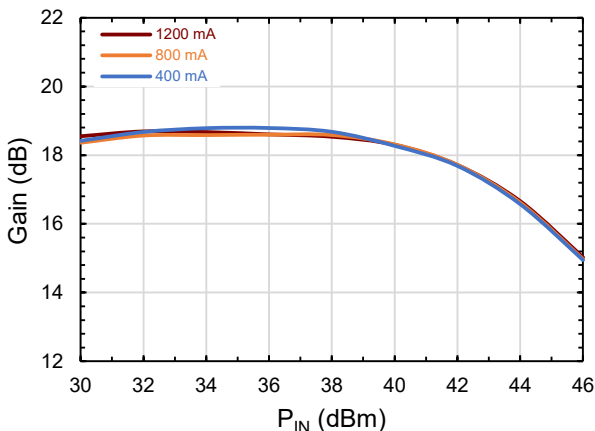
**Drain Current vs.  $I_{DQ}$  and  $P_{IN}$**



**Gate Current vs.  $I_{DQ}$  and  $P_{IN}$**



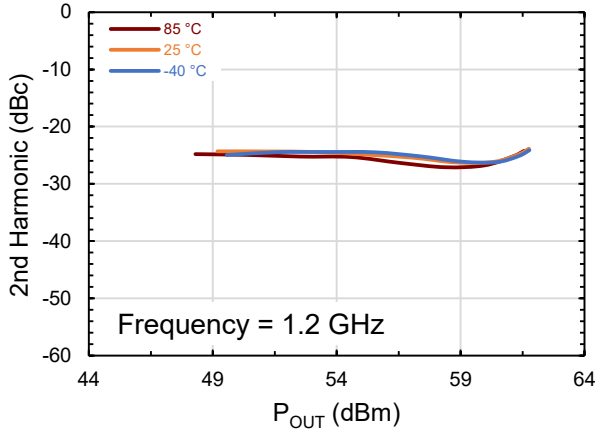
**Large Signal Gain vs.  $I_{DQ}$  and  $P_{IN}$**



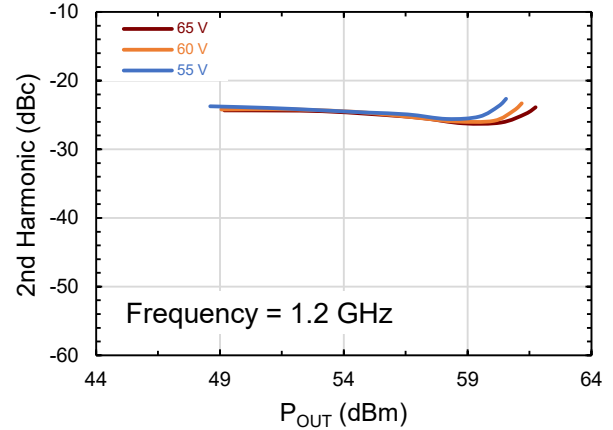
**Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture**

Pulsed 100  $\mu$ s 10%,  $P_{IN}$  = 45 dBm,  $V_{DS}$  = 65 V,  $I_{DQ}$  = 800 mA, Frequency = 1.3 GHz (unless otherwise noted)  
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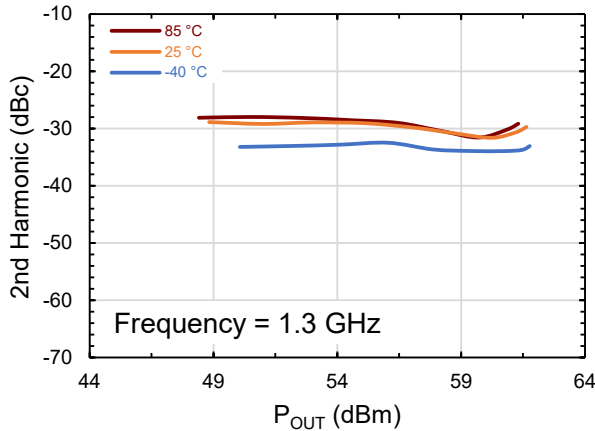
**2<sup>nd</sup> Harmonic vs. Temperature and  $P_{IN}$**



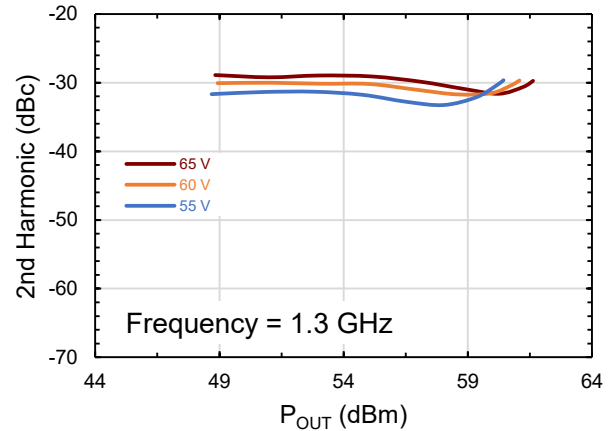
**2<sup>nd</sup> Harmonic vs.  $V_{DS}$  and  $P_{IN}$**



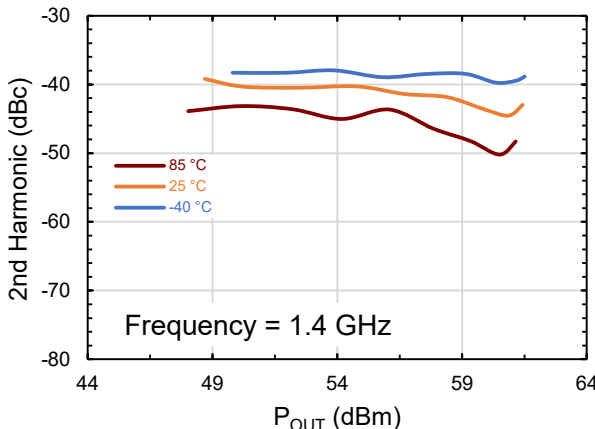
**2<sup>nd</sup> Harmonic vs. Temperature and  $P_{IN}$**



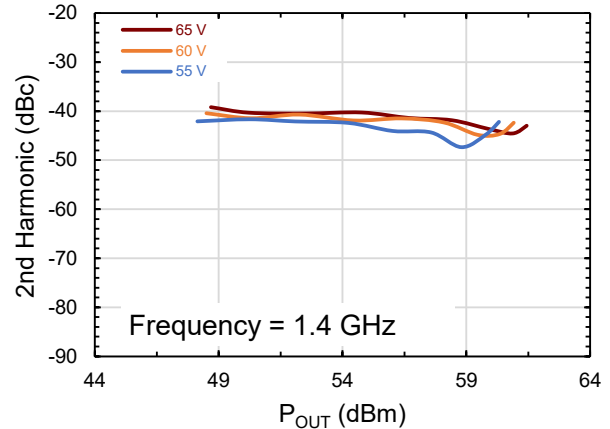
**2<sup>nd</sup> Harmonic vs.  $V_{DS}$  and  $P_{IN}$**



**2<sup>nd</sup> Harmonic vs. Temperature and  $P_{IN}$**



**2<sup>nd</sup> Harmonic vs.  $V_{DS}$  and  $P_{IN}$**

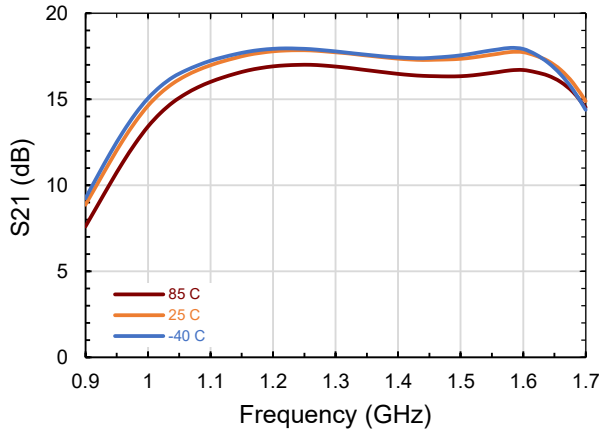


**Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture:**

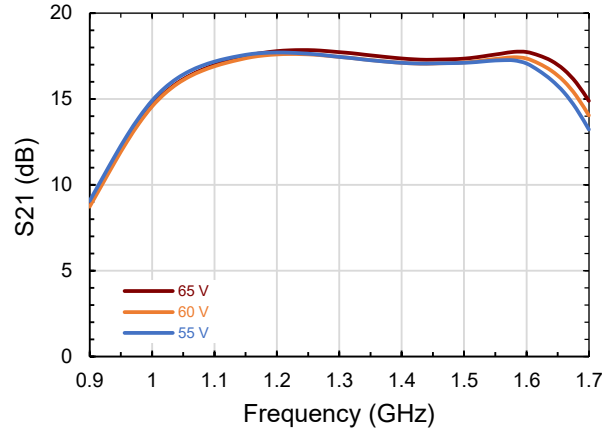
CW,  $V_{DS} = 65\text{ V}$ ,  $I_{DQ} = 800\text{ mA}$ ,  $P_{IN} = -20\text{ dBm}$  (unless Otherwise Noted)

For Engineering Evaluation Only—This data does not Modify MACOM's Datasheet Limits.

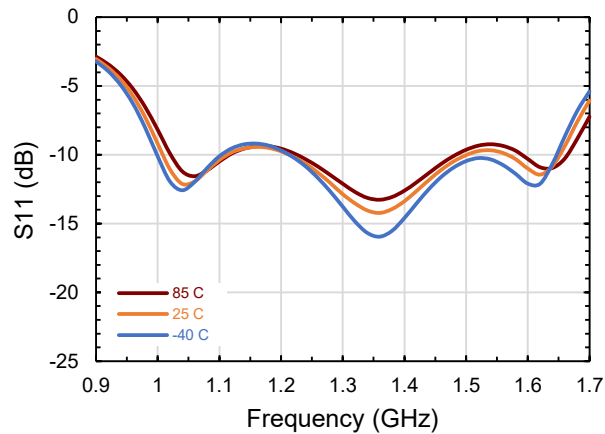
**S21 vs Frequency and Temperature**



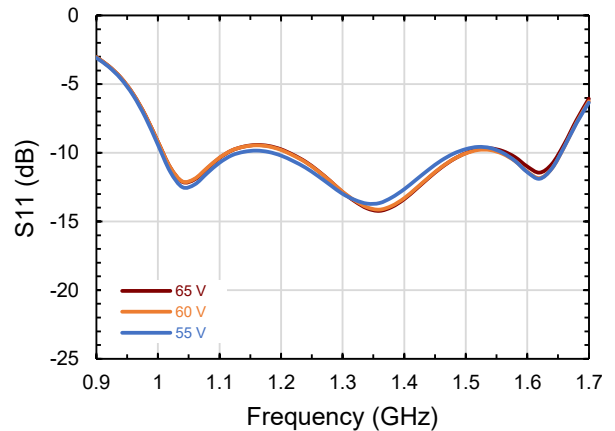
**S21 vs Frequency and  $V_{DS}$**



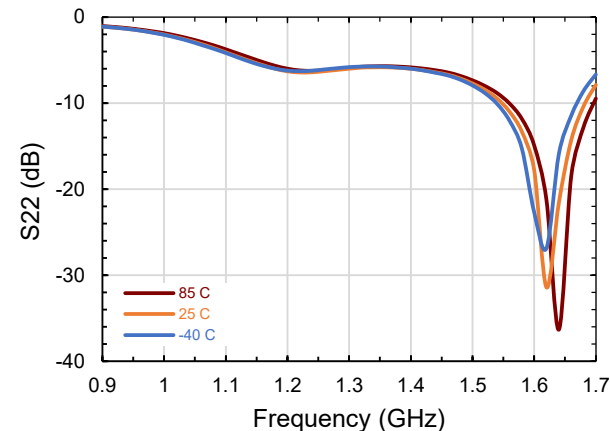
**S11 vs Frequency and Temperature**



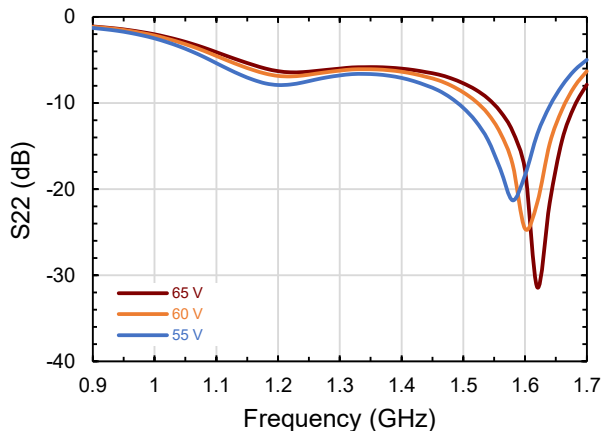
**S11 vs Frequency and  $V_{DS}$**



**S22 vs Frequency and Temperature**



**S22 vs Frequency and  $V_{DS}$**



# GaN on SiC Transistor, 1200 W, 65 V 1.2 - 1.4 GHz



**MACOM PURE CARBIDE™**

MAPC-A1558-AB

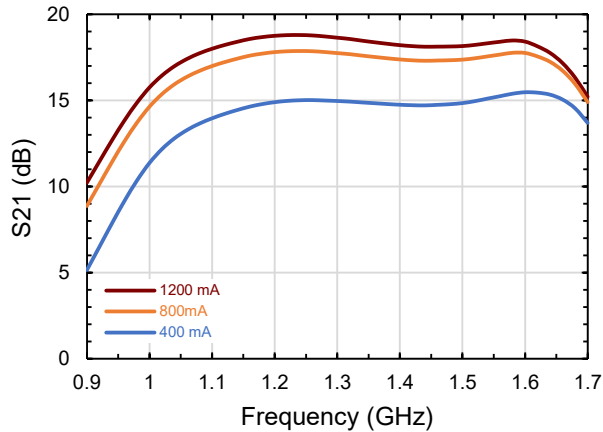
Rev. V1

## Typical Performance Curves as Measured in the 1.2 - 1.4 GHz Evaluation Test Fixture:

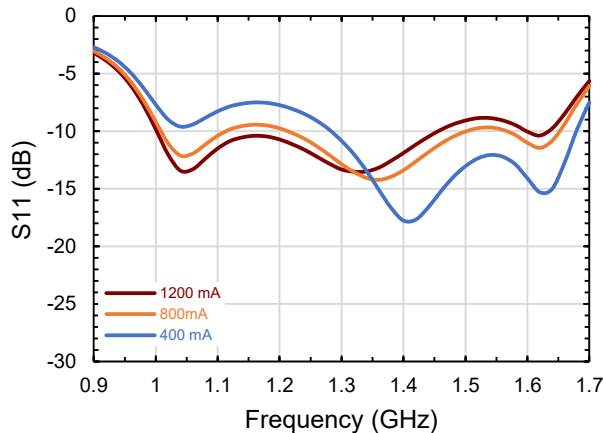
CW,  $V_{DS} = 65$  V,  $I_{DQ} = 800$  mA,  $P_{IN} = -20$  dBm (unless Otherwise Noted)

For Engineering Evaluation Only—This data does not Modify MACOM's Datasheet Limits.

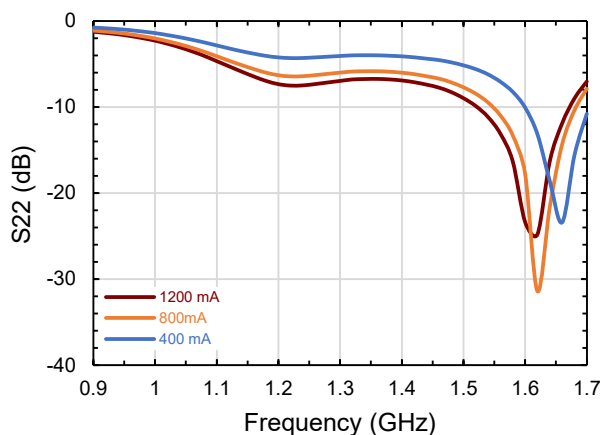
**S21 vs Frequency and  $I_{DQ}$**



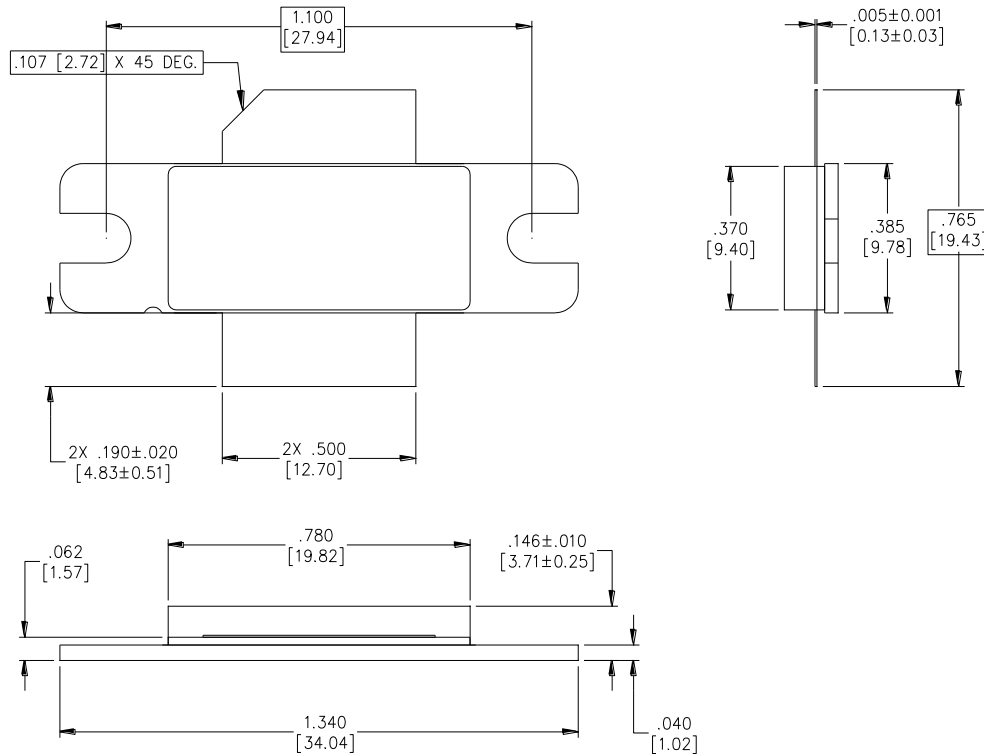
**S11 vs Frequency and  $I_{DQ}$**



**S22 vs Frequency and  $I_{DQ}$**



**AC-780B-2 Package Dimensions**



NOTES:

1. ALL DIMENSIONS SHOWN AS in[mm]. CONTROLLING DIMENSIONS ARE IN in AND CONVERTED mm DIMENSIONS ARE NOT NECESSARILY EXACT.
2. ALL TOLERANCES ARE ±.005 [0.13] UNLESS OTHERWISE NOTED
3. LEAD FINISH: AU  
FLANGE FINISH: AU
4. LID SEAL EPOXY MAY FLOW OUT A MAXIMUM OF .020 [0.51] FROM EDGE OF LID
5. LID MAY BE MIS-ALIGNED UP TO .010 [0.25] FROM PACKAGE IN ANY DIRECTION

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