

Features

- MACOM PURE CARBIDE® Amplifier Series
- Suitable for Linear & Saturated Applications
- Pulsed Operation: 1250 W Output Power
- Internally Pre-Matched
- 260°C Reflow Compatible
- 50 V Operation
- 100% RF Tested
- RoHS* Compliant

Applications

- UHF Radar, Public Safety Radio

Description

The MAPC-A1542 is a high power GaN on Silicon Carbide HEMT D-mode amplifier suitable for 400 - 460 MHz frequency operation. The device supports pulsed operation with output power levels of 1250 W (61 dBm) at 50 V in an air cavity ceramic package.

Typical Performance:

Measured in MACOM evaluation circuit at 3 dB compression, 15 mS pulse width, 25% duty cycle.

- $V_{DS} = 50 \text{ V}$, $I_{DQ} = 600 \text{ mA}$, $T_C = 25^\circ\text{C}$

Frequency (MHz)	Output Power (dBm)	Gain (dB)	η_D (%)
400	61.1	14.7	82.1
410	61.2	15.7	85.1
420	61.1	16.2	86.4
430	61.0	16.2	86.3
440	61.0	15.9	86.2
450	61.1	15.2	85.2
460	61.2	14.4	83.9

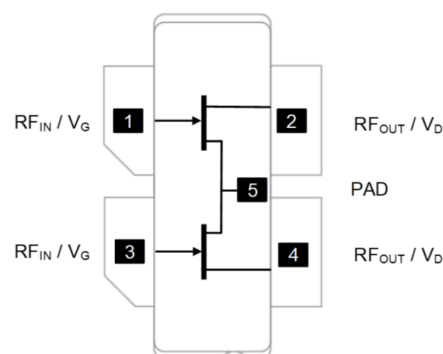


AC-1230B-4



AC-1230S-4

Functional Schematic



Pin Configuration

Pin #	Pin Name	Function
1, 3	RF _{IN} / V _G	RF Input / Gate
2, 4	RF _{OUT} / V _D	RF Output / Drain
5	Flange ¹	Ground / Source

1. The flange on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information

Part Number	Package
MAPC-A1542-AS000	Bulk Quantity : Earless
MAPC-A1542-ASTR1	Tape and Reel : Earless
MAPC-A1542-ASSB1	Sample Board : Earless
MAPC-A1542-AB000	Bulk Quantity : Boltdown
MAPC-A1542-ABTR1	Tape and Reel : Boltdown
MAPC-A1542-ABSB1	Sample Board : Boltdown

RF Electrical Characteristics: $T_C = 25^\circ\text{C}$, $V_{DS} = 50\text{ V}$, $I_{DQ} = 600\text{ mA}$

Note: Performance in MACOM 400 - 460 MHz Evaluation Test Fixture, 50 Ω system

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Small Signal Gain	Pulsed ² , 430 MHz	G_{SS}	-	19.2	-	dB
Saturated Output Power	Pulsed ² , 430 MHz, 3 dB Gain Compression	P_{SAT}	-	61.0	-	dBm
Power Gain	Pulsed ² , 430 MHz, 3 dB Gain Compression	G_{SAT}	-	16.2	-	dB
Saturated Drain Efficiency	Pulsed ² , 430 MHz, 3 dB Gain Compression	η_{SAT}	-	86	-	%
Gain Variation (-40°C to +85°C)	Pulsed ² , 430 MHz	ΔG	-	0.009	-	dB/°C
Power Variation (-40°C to +85°C)	Pulsed ² , 430 MHz	ΔP_{3dB}	-	0.004	-	dB/°C
Power Gain	Pulsed ² , 430 MHz, $P_{IN} = 43.1\text{ dBm}$	G_P	-	60.9	-	dB
Drain Efficiency	Pulsed ² , 430 MHz, $P_{IN} = 43.1\text{ dBm}$	η	-	84	-	%
Input Return Loss	Pulsed ² , 430 MHz, $P_{IN} = 43.1\text{ dBm}$	IRL	-	-17	-	dB
Ruggedness: Output Mismatch	Pulsed ³ , All phase angles	Ψ	VSWR = 50:1, No Damage			

2. Pulse Details: 15 ms pulse width, 25% duty cycle.

RF Electrical Specifications: $T_A = 25^\circ\text{C}$, $V_{DS} = 50\text{ V}$, $I_{DQ} = 600\text{ mA}$

Note: Performance in MACOM 400 MHz Production Test Fixture, 50 Ω system

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Power Gain	Pulsed ³ , 400 MHz, 2.5 dB Gain Compression	G_{SAT}	15.0	15.6	-	dB
Saturated Drain Efficiency	Pulsed ³ , 400 MHz, 2.5 dB Gain Compression	η_{SAT}	72.2	78	-	%
Saturated Output Power	Pulsed ³ , 400 MHz, 2.5 dB Gain Compression	P_{SAT}	60.4	61.2	-	dBm

3. Pulse Details: 100 μs pulse width, 10% duty cycle.

DC Electrical Characteristics $T_A = 25^\circ\text{C}$

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Units
Drain-Source Leakage Current	$V_{GS} = -8\text{ V}$, $V_{DS} = 130\text{ V}$	I_{DLK}	-	-	215	mA
Gate-Source Leakage Current	$V_{GS} = -8\text{ V}$, $V_{DS} = 0\text{ V}$	I_{GLK}	-	-	215	mA
Gate Threshold Voltage	$V_{DS} = 50\text{ V}$, $I_D = 215\text{ mA}$	V_T	-	-2.9	-	V
Gate Quiescent Voltage	$V_{DS} = 50\text{ V}$, $I_D = 600\text{ mA}$	V_{GSQ}	-	-2.71	-	V
Maximum Drain Current	$V_{DS} = 7\text{ V}$ pulsed, pulse width 300 μs	$I_{D, MAX}$	-	255	-	A

Absolute Maximum Ratings^{4,5,6,7,8}

Parameter	Absolute Maximum
Drain Source Voltage, V_{DS}	195 V
Gate Source Voltage, V_{GS}	-10 to 2 V
Gate Current, I_G	215 mA
Storage Temperature Range	-65°C to +150°C
Case Operating Temperature Range	-40°C to +85°C
Channel Operating Temperature Range, T_{CH}	-40°C to +225°C
Absolute Maximum Channel Temperature	+225°C

4. Exceeding any one or combination of these limits may cause permanent damage to this device.
5. MACOM does not recommend sustained operation near these survivability limits.
6. Operating at drain source voltage $V_{DS} < 55$ V will ensure $MTTF > 2 \times 10^6$ hours.
7. Operating at nominal conditions with $T_{CH} \leq 200^\circ\text{C}$ will ensure $MTTF > 2 \times 10^6$ hours.
8. MTTF may be estimated by the expression $MTTF \text{ (hours)} = A e^{\frac{B}{T+273}}$ where T is the channel temperature in degrees Celsius, $A = 1$, $B = -38.215$, and $C = 26,343$.

Thermal Characteristics⁹

Parameter	Test Conditions	Symbol	Typical	Units
Thermal Resistance using Finite Element Analysis	$V_{DS} = 50$ V $T_C = 85^\circ\text{C}, T_{CH} = 225^\circ\text{C}$	$R_{\theta}(\text{FEA})$	0.19	°C/W
Thermal Resistance using Infrared Measurement of Die Surface Temperature	$V_{DS} = 50$ V $T_C = 85^\circ\text{C}, T_{CH} = 225^\circ\text{C}$	$R_{\theta}(\text{IR})$	0.152	°C/W

9. Case temperature measured using thermocouple embedded in heat-sink. Contact local applications support team for more details on this measurement.

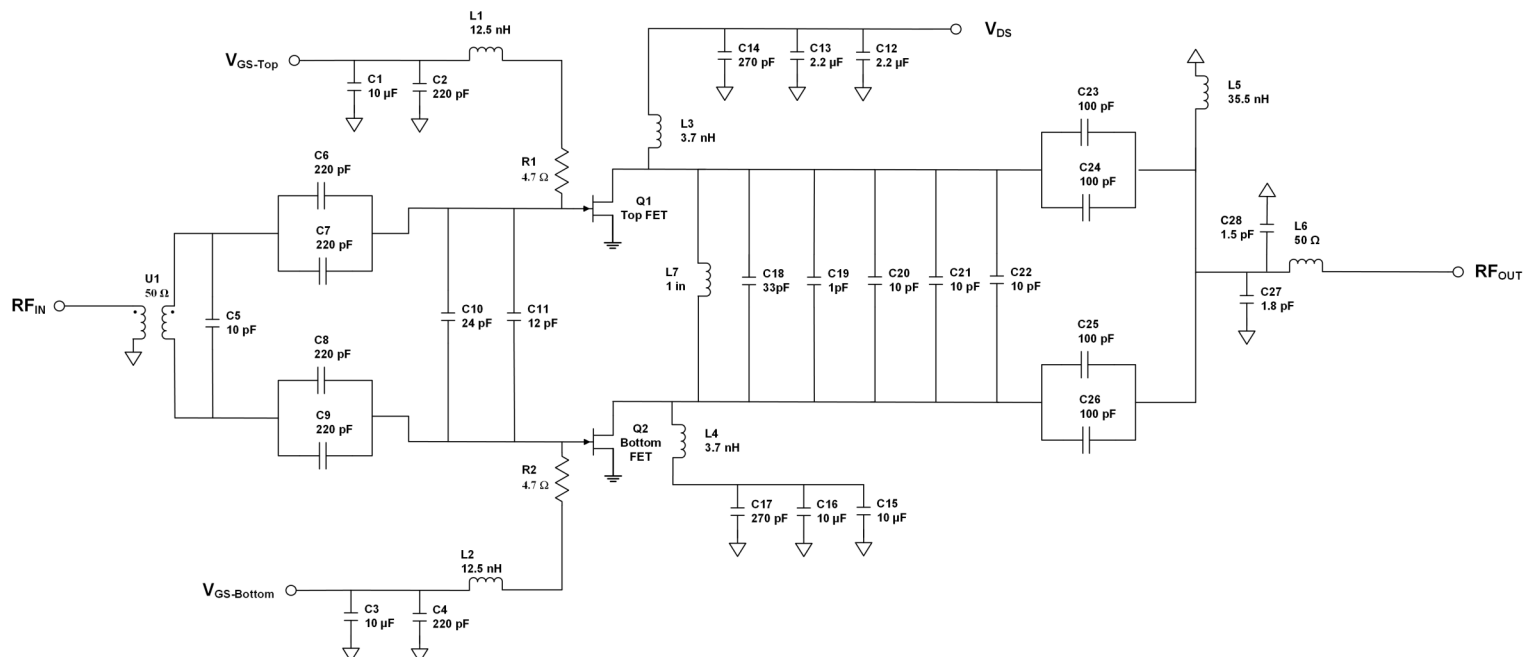
Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Nitride Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Evaluation Test Fixture and Recommended Tuning Solution 400 - 460 MHz



Description

Parts measured on evaluation board (50-mil thick RO3006). Matching is provided using a combination of lumped elements and transmission lines as shown in the simplified schematic above. Recommended tuning solution component placement, transmission lines, and details are shown on the next page.

Bias Sequencing

Turning the device ON

1. Set V_{GS} to pinch-off (V_P).
2. Turn on V_{DS} to nominal voltage (50 V).
3. Increase V_{GS} until I_{DS} current is reached.
4. Apply RF power to desired level.

Turning the device OFF

1. Turn the RF power OFF.
2. Decrease V_{GS} down to V_P pinch-off.
3. Decrease V_{DS} down to 0 V.
4. Turn off V_{GS} .

GaN Amplifier 50 V, 1250 W 400 - 460 MHz

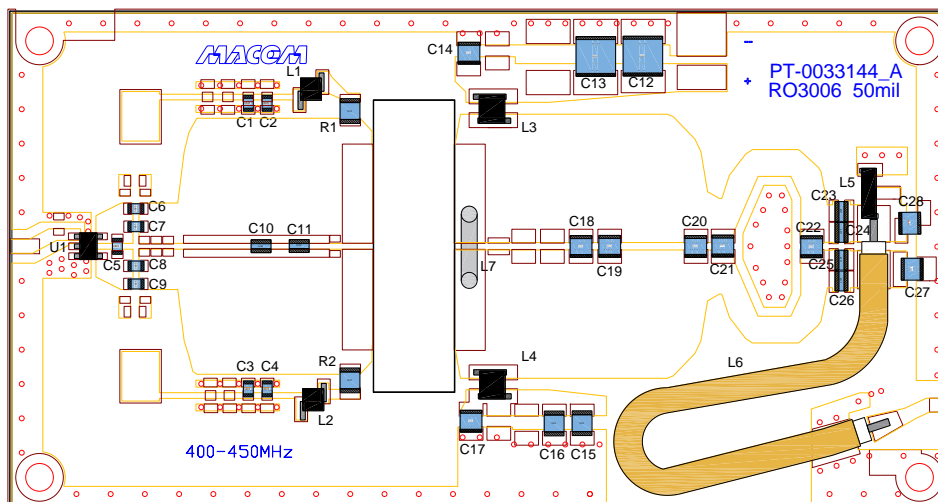


MACOM PURE CARBIDE

MAPC-A1542

Rev. V2

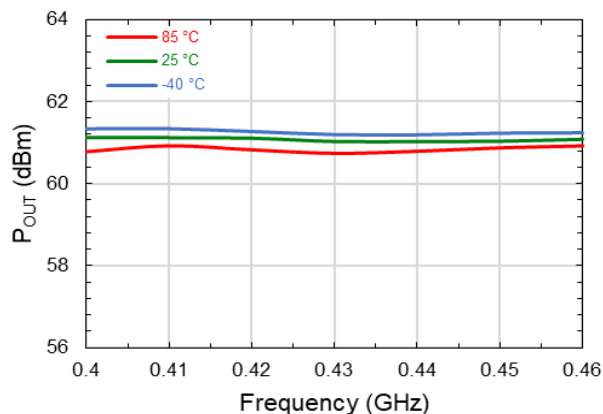
Evaluation Test Fixture and Recommended Tuning Solution 400 - 460 MHz



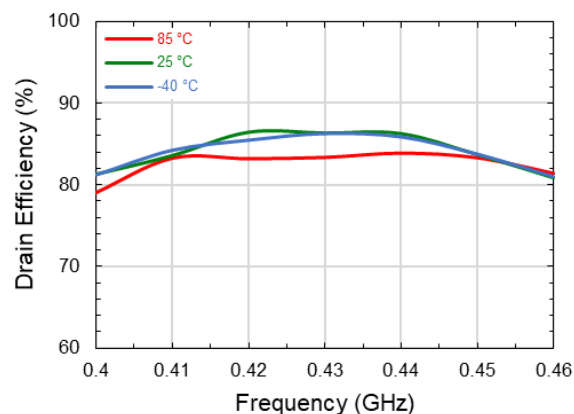
Reference Designator	Value	Tolerance	Manufacturer	Part Number
C1, C3	10 μ f	+/- 10 %	Murata	GRM21BR6YA106KE43L
C2, C4, C6-C9	220 pF	+/- 5 %	PPI	0805N221JW251X
C5	10 pF	+/- 5 %	PPI	0805N100JW251X
C10	24 pF	+/- 5 %	PPI	0708N240JW501
C11	12 pF	+/- 5 %	PPI	0708N120JW501
C12, C13	2.2 μ F	+/- 20 %	Murata	KRM55TR72E225MH01L
C14, C17	270 pF	+/- 5 %	PPI	1111N271JW201X
C15, C16	10 μ F	+/- 10 %	Murata	GRM32EC72A106KE05L
C18	1.0 pF	+/- 0.1 pF	PPI	1111N1R0BW501X
C19	33 pF	+/- 5 %	PPI	1111N330JW501X
C20-C22	10 pF	+/- 5 %	PPI	1111N100JW501X
C23-C26	100 pF	+/- 5 %	PPI	0708N101JW501
C27	1.8 pF	+/- 0.1 pF	PPI	1111N1R8BW501X
C28	1.5 pF	+/- 0.1 pF	PPI	1111N1R5BW501X
R1, R2	4.7 Ω	+/- 5 %	KOA Speer	SG73P2ETTD4R7J
L1, L2	12.5 nH	+/- 5 %	Coilcraft	A04TJLC
L3, L4	3.7 nH	+/- 5 %	Coilcraft	GA3092-ALC
L5	35.5 nH	+/- 2 %	Coilcraft	B09TGLB
L6	50 Ω	-	AMAWAVE	UT-141C
L7	1 in	-	-	18 AWG
U1	50 Ω	-	Macom	MABA-009602-ES2922
Q1	MACOM GaN Power Amplifier			MAPC-A1542
PCB	RO3006, 50 mil, 1.0 oz. Cu, Au Finish			

**Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² $V_{DS} = 50$ V, $I_{DQ} = 600$ mA, $P_{out} = p3dB$ (Unless Otherwise Noted)**

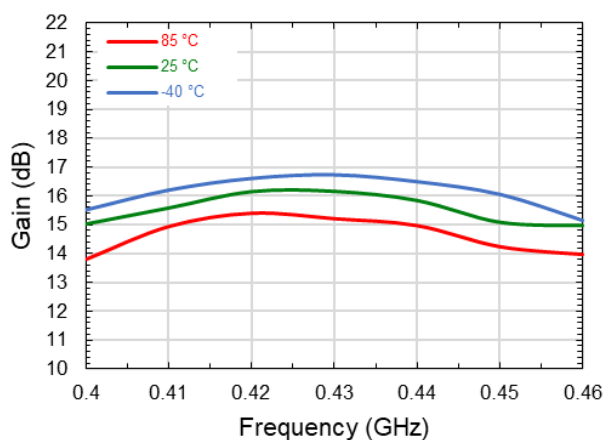
P_{OUT} (dBm) vs Frequency (GHz) vs Temp



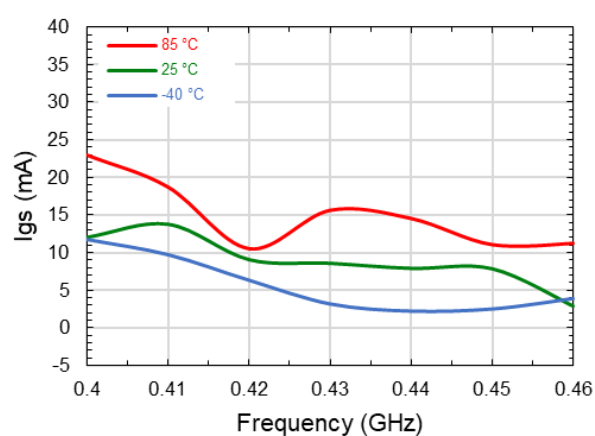
Drain Efficiency (%) vs Frequency (GHz) vs Temp



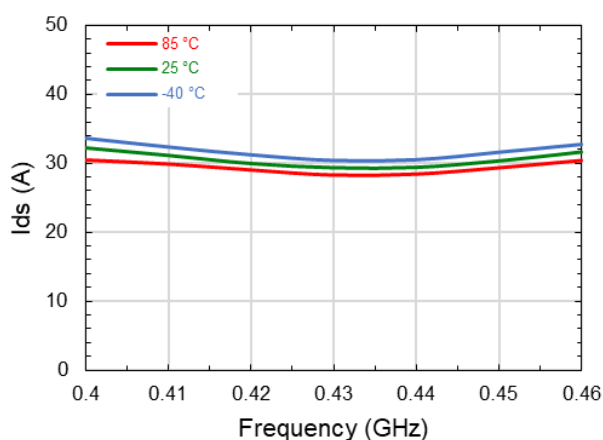
Gain (dB) vs Frequency (GHz) vs Temp



Gate Current (mA) vs Frequency (GHz) vs Temp

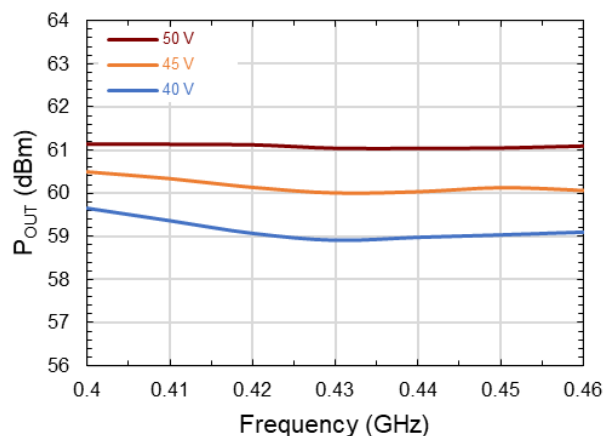


Drain Current (A) vs Frequency (GHz) vs Temp

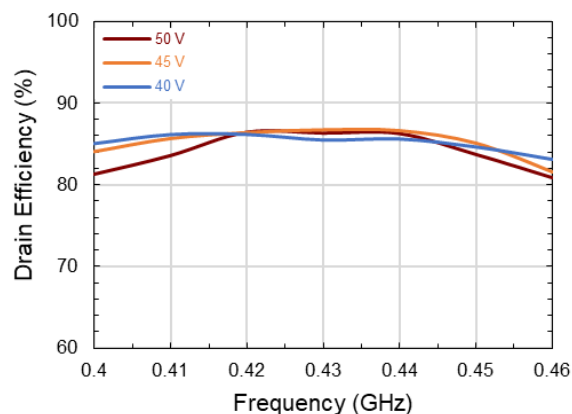


**Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² $I_{DQ} = 600$ mA, $P_{out} = p3dB$, $T_C = 25^\circ C$ (Unless Otherwise Noted)**

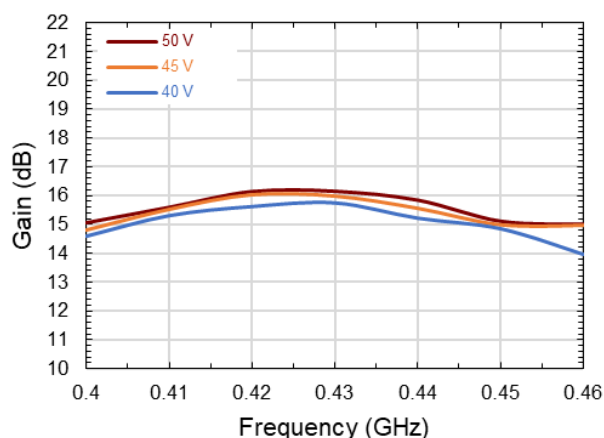
P_{out} (dBm) vs Frequency (GHz) vs V_{ds} (V)



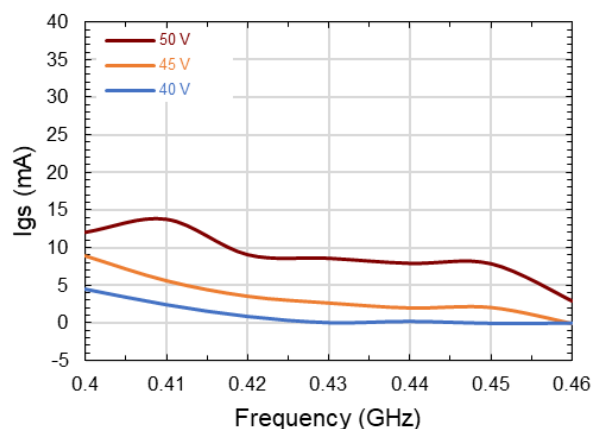
Drain Efficiency (%) vs Frequency (GHz) vs V_{ds} (V)



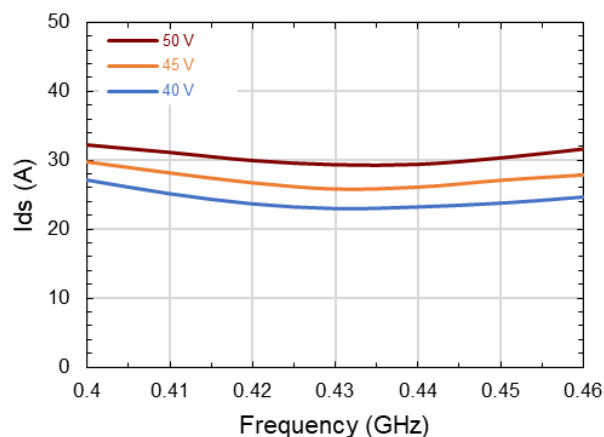
Gain (dB) vs Frequency (GHz) vs V_{ds} (V)



Gate Current (mA) vs Frequency (GHz) vs V_{ds} (V)

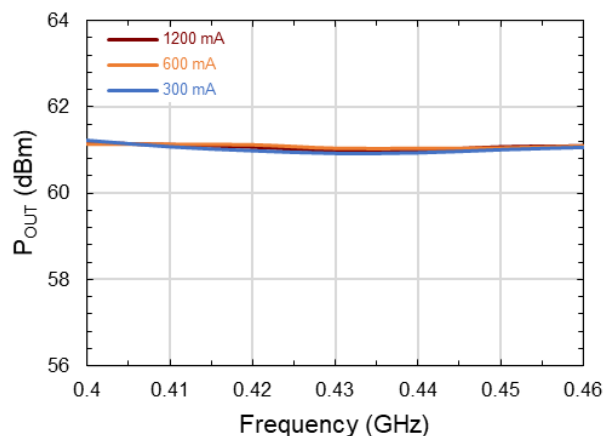


Drain Current (A) vs Frequency (GHz) vs V_{ds} (V)

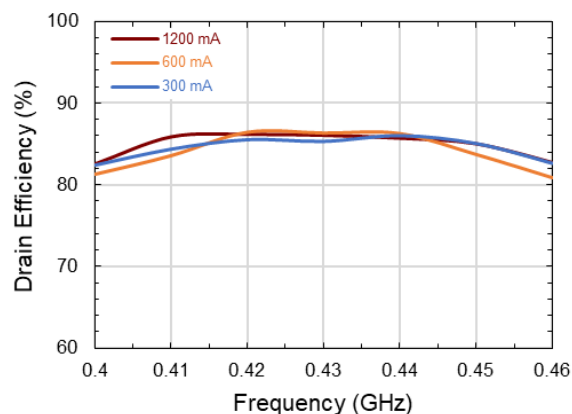


Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² $V_{DS} = 50$ V, $P_{in} = p3dB$, $T_C = 25^\circ C$ (Unless Otherwise Noted)

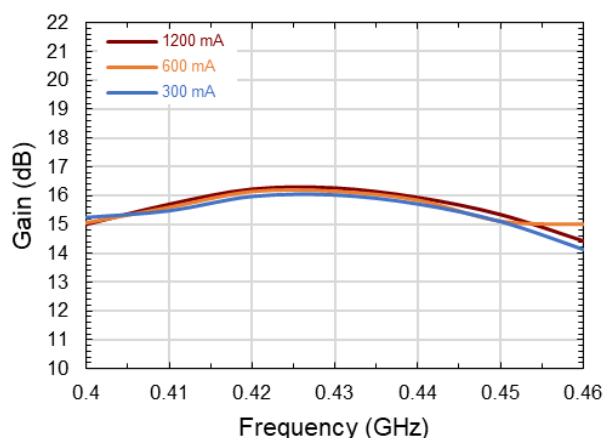
P_{OUT} (dBm) vs Frequency (GHz) vs I_{dq} (mA)



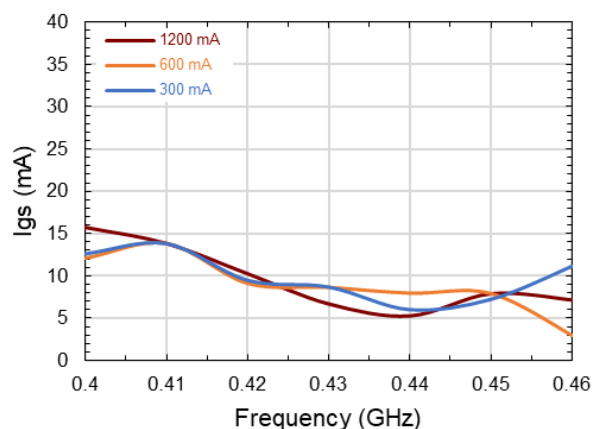
Drain Efficiency (%) vs Frequency (GHz) vs I_{dq} (mA)



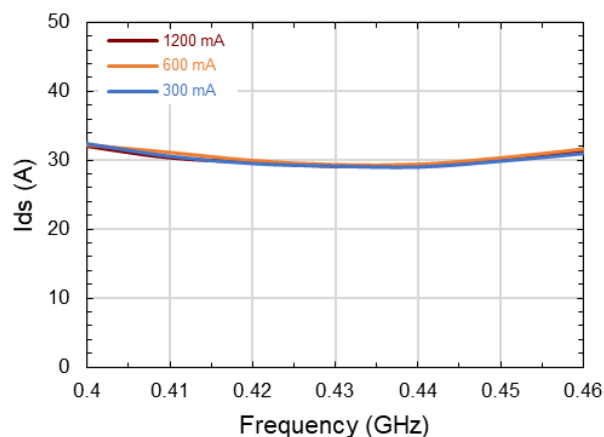
Gain (dB) vs Frequency (GHz) vs I_{dq} (mA)



Gate Current (mA) vs Frequency (GHz) vs I_{dq} (mA)

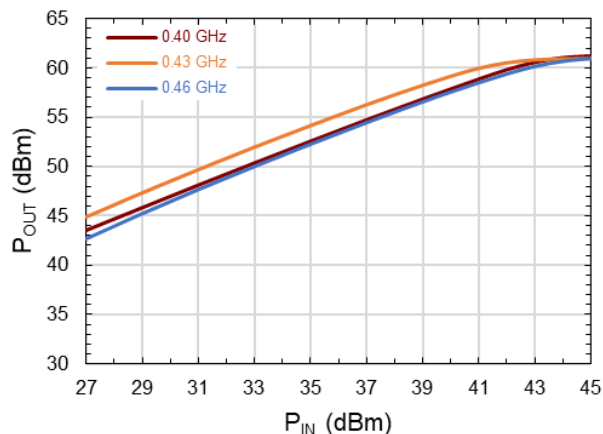


Drain Current (A) vs Frequency (GHz) vs I_{dq} (mA)

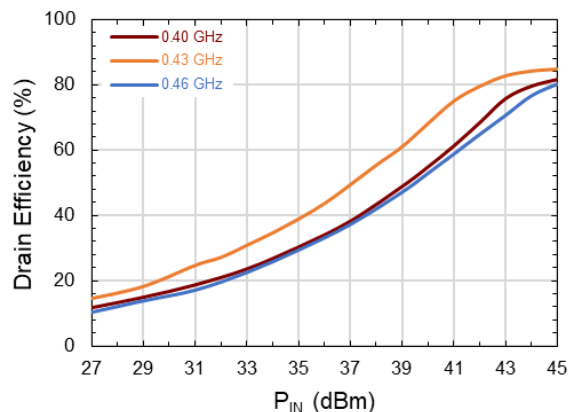


Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² $V_{DS} = 50$ V, $I_{DQ} = 600$ mA, $T_C = 25^\circ\text{C}$ (Unless Otherwise Noted)

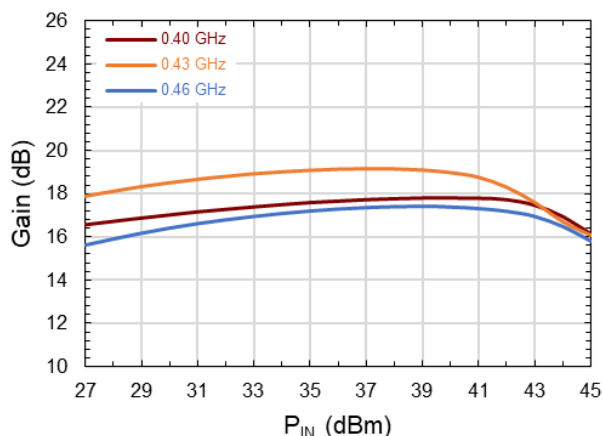
P_{OUT} (dBm) vs P_{IN} (dBm) vs Freq (GHz)



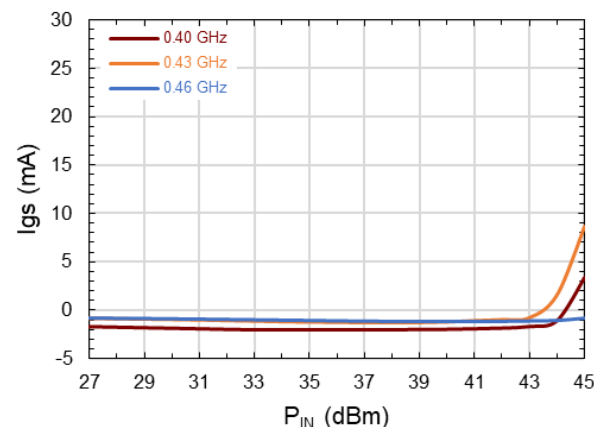
Drain Efficiency (%) vs P_{IN} (dBm) vs Freq (GHz)



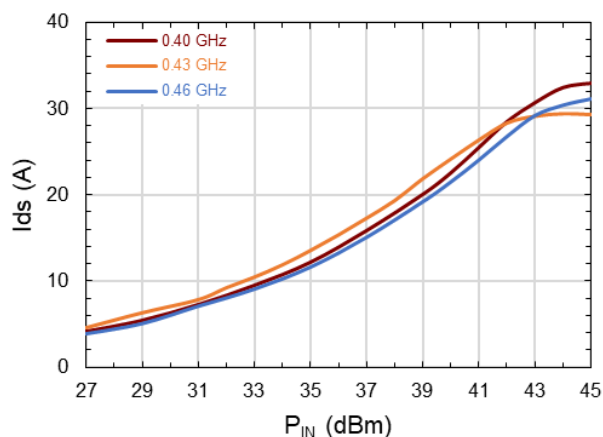
Gain (dB) vs P_{IN} (dBm) vs Freq (GHz)



Gate Current (mA) vs P_{IN} (dBm) vs Freq (GHz)

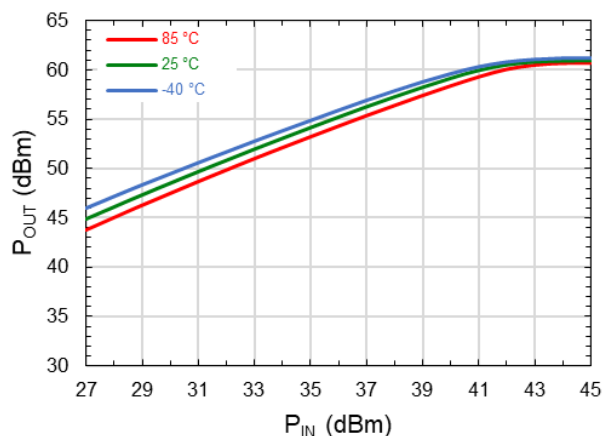


Drain Current (A) vs P_{IN} (dBm) vs Freq (GHz)

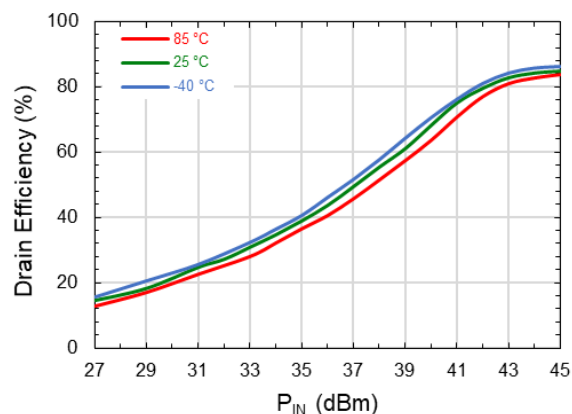


**Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² 430MHz, $V_{DS} = 50$ V, $I_{DQ} = 600$ mA (Unless Otherwise Noted)**

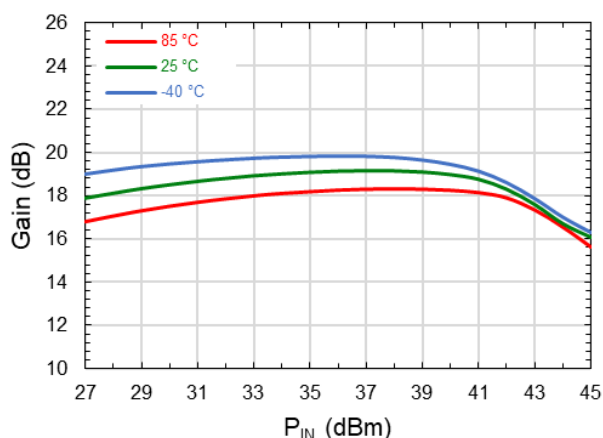
P_{OUT} (dBm) vs P_{IN} (dBm) vs Temp



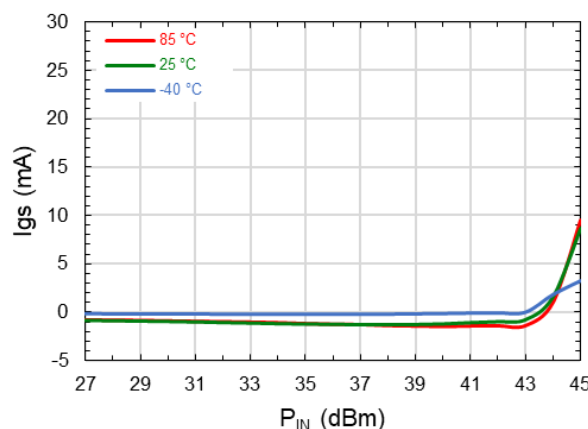
Drain Efficiency (%) vs P_{IN} (dBm) vs Temp



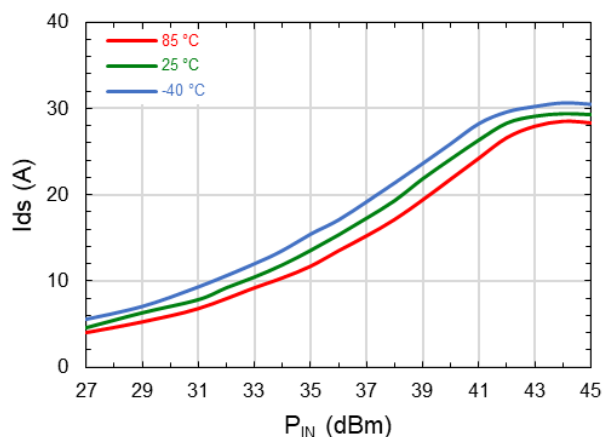
Gain (dB) vs P_{IN} (dBm) vs Temp



Gate Current (mA) vs P_{IN} (dBm) vs Temp

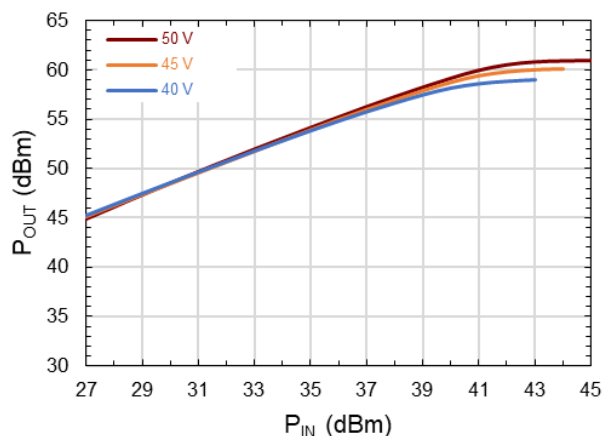


Drain Current (A) vs P_{IN} (dBm) vs Temp

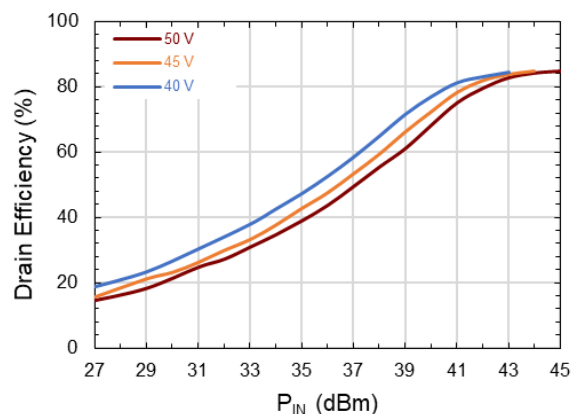


Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² 430MHz, $I_{DQ} = 600$ mA, $T_C = 25^\circ\text{C}$ (Unless Otherwise Noted)

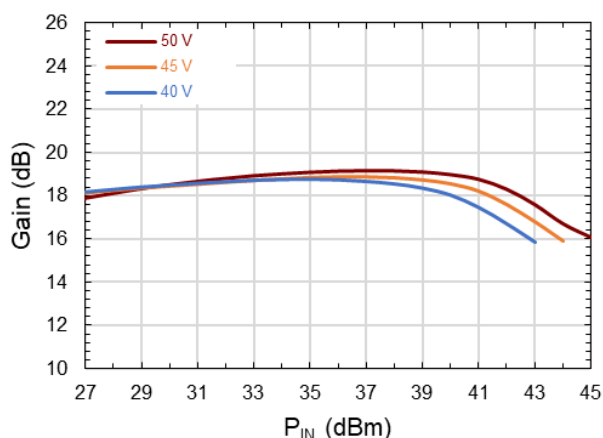
P_{OUT} (dBm) vs P_{IN} (dBm) vs V_{ds} (V)



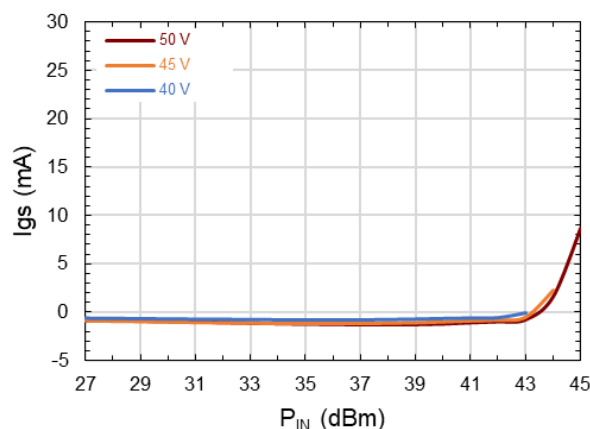
Drain Efficiency (%) vs P_{IN} (dBm) vs V_{ds} (V)



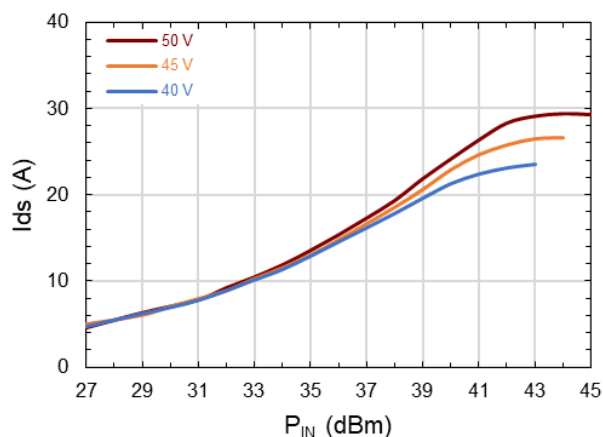
Gain (dB) vs P_{IN} (dBm) vs V_{ds} (V)



Gate Current (mA) vs P_{IN} (dBm) vs V_{ds} (V)

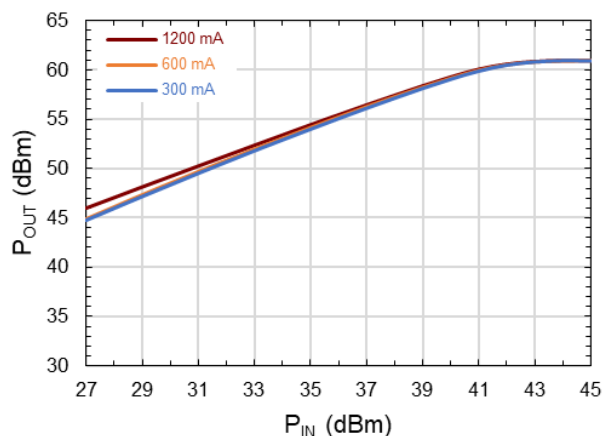


Drain Current (A) vs P_{IN} (dBm) vs V_{ds} (V)

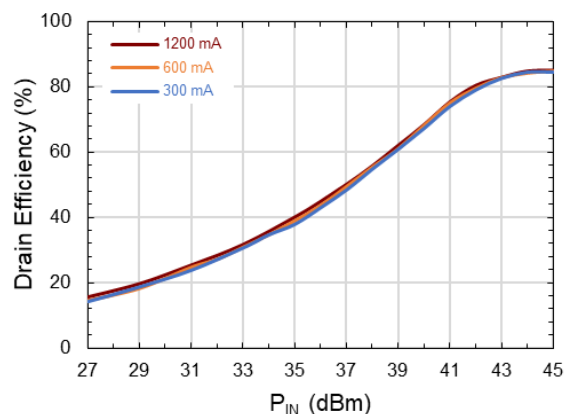


Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² 430MHz, $V_{DS} = 50$ V, $T_C = 25^\circ\text{C}$ (Unless Otherwise Noted)

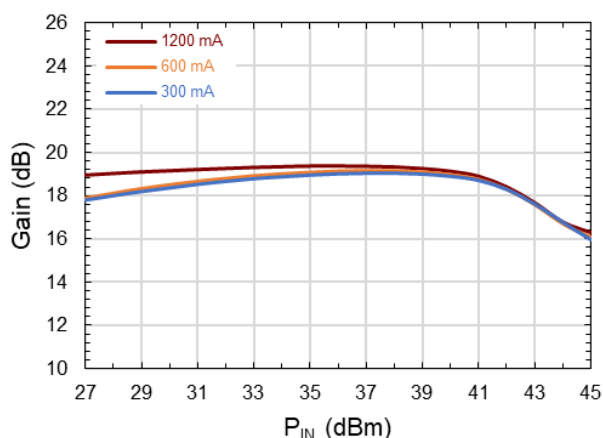
P_{OUT} (dBm) vs P_{IN} (dBm) vs I_{DQ} (mA)



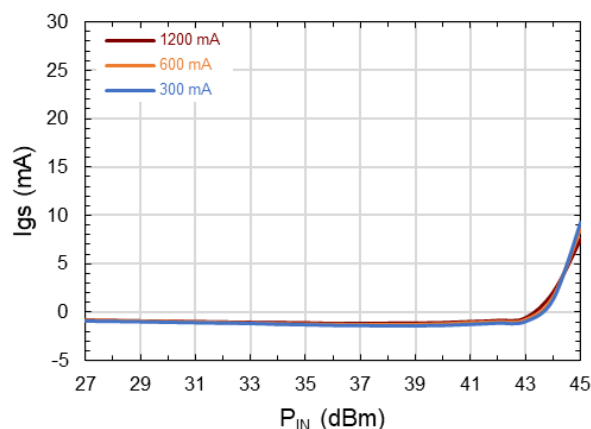
Drain Efficiency (%) vs P_{IN} (dBm) vs I_{DQ} (mA)



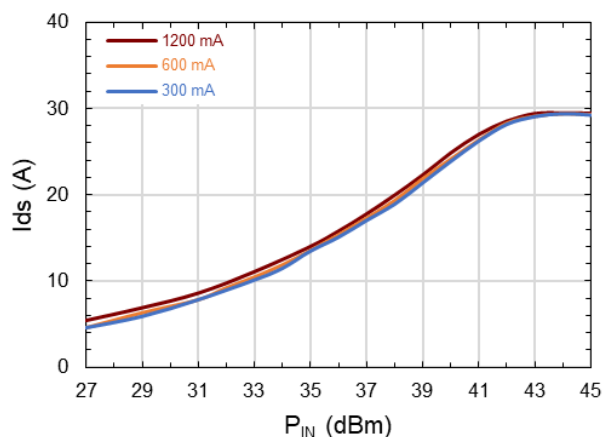
Gain (dB) vs P_{IN} (dBm) vs I_{DQ} (mA)



Gate Current (mA) vs P_{IN} (dBm) vs I_{DQ} (mA)

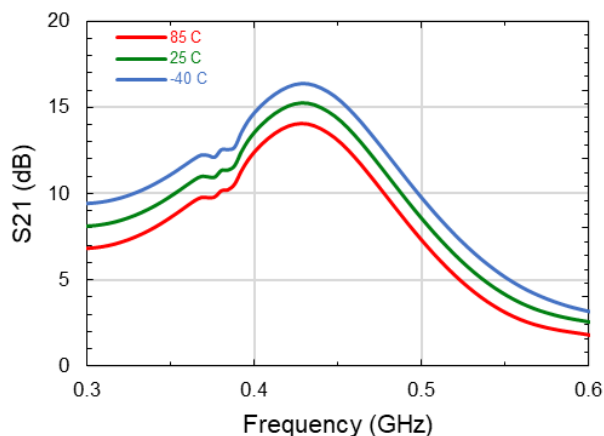


Drain Current (A) vs P_{IN} (dBm) vs I_{DQ} (mA)

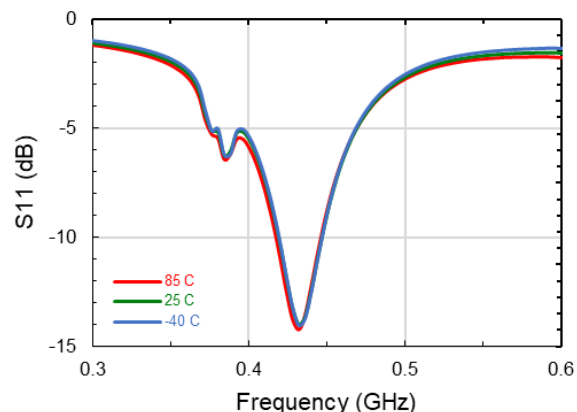


**Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² $V_{DS} = 50$ V, $I_{DQ} = 600$ mA, $P_{in} = -20$ dBm (Unless Otherwise Noted)**

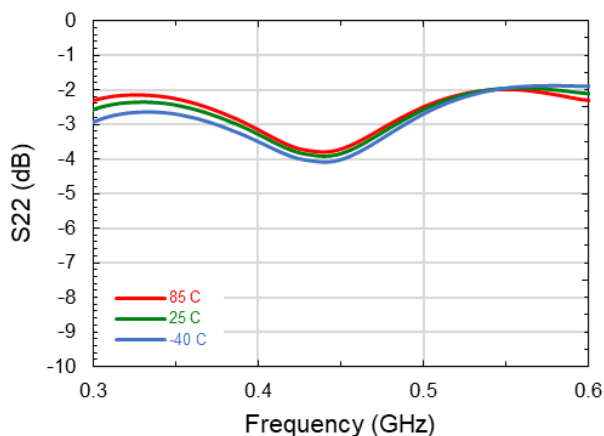
S₂₁ (dB) vs Freq (GHz) vs Temp



S₁₁ (dB) vs Freq (GHz) vs Temp

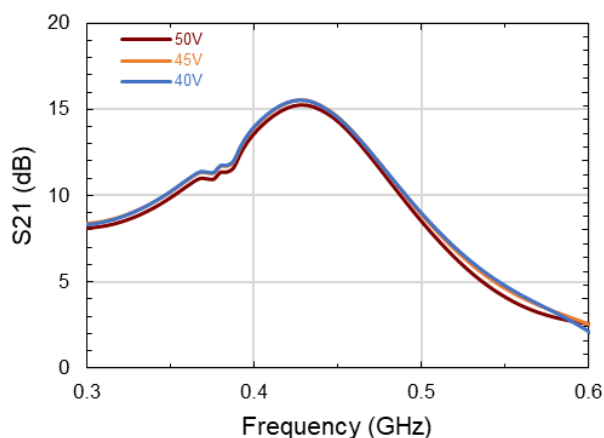


S₂₂ (dB) vs Freq (GHz) vs Temp

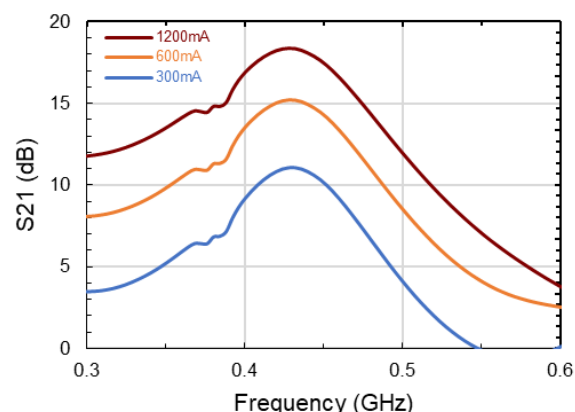


Typical Performance Curves as Measured in the 400 - 460 MHz Evaluation Test Fixture:
Pulsed² $V_{DS} = 50$ V, $I_{DQ} = 600$ mA, $P_{in} = -20$ dBm, $T_C = 25^\circ\text{C}$ (Unless Otherwise Noted)

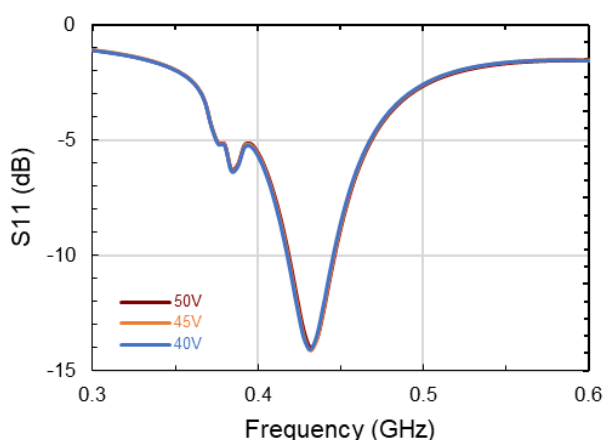
S_{21} (dB) vs Freq (GHz) vs V_{ds} (V)



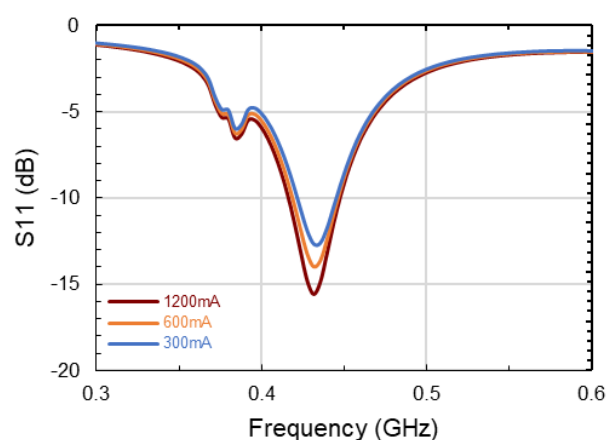
S_{21} (dB) vs Freq (GHz) vs I_{dq} (mA)



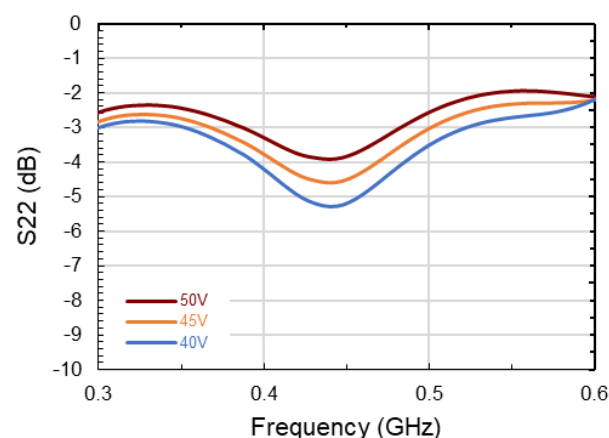
S_{11} (dB) vs Freq (GHz) vs V_{ds} (V)



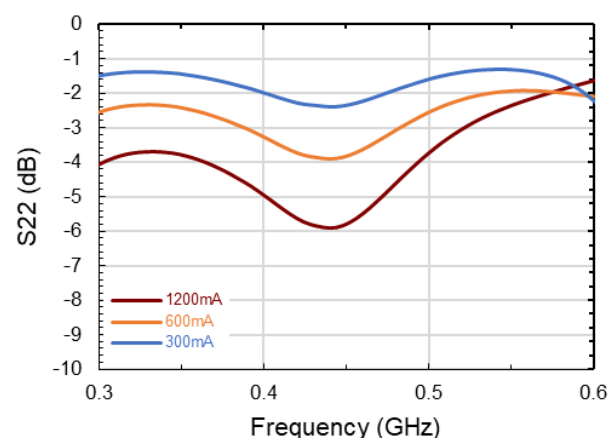
S_{11} (dB) vs Freq (GHz) vs I_{dq} (mA)



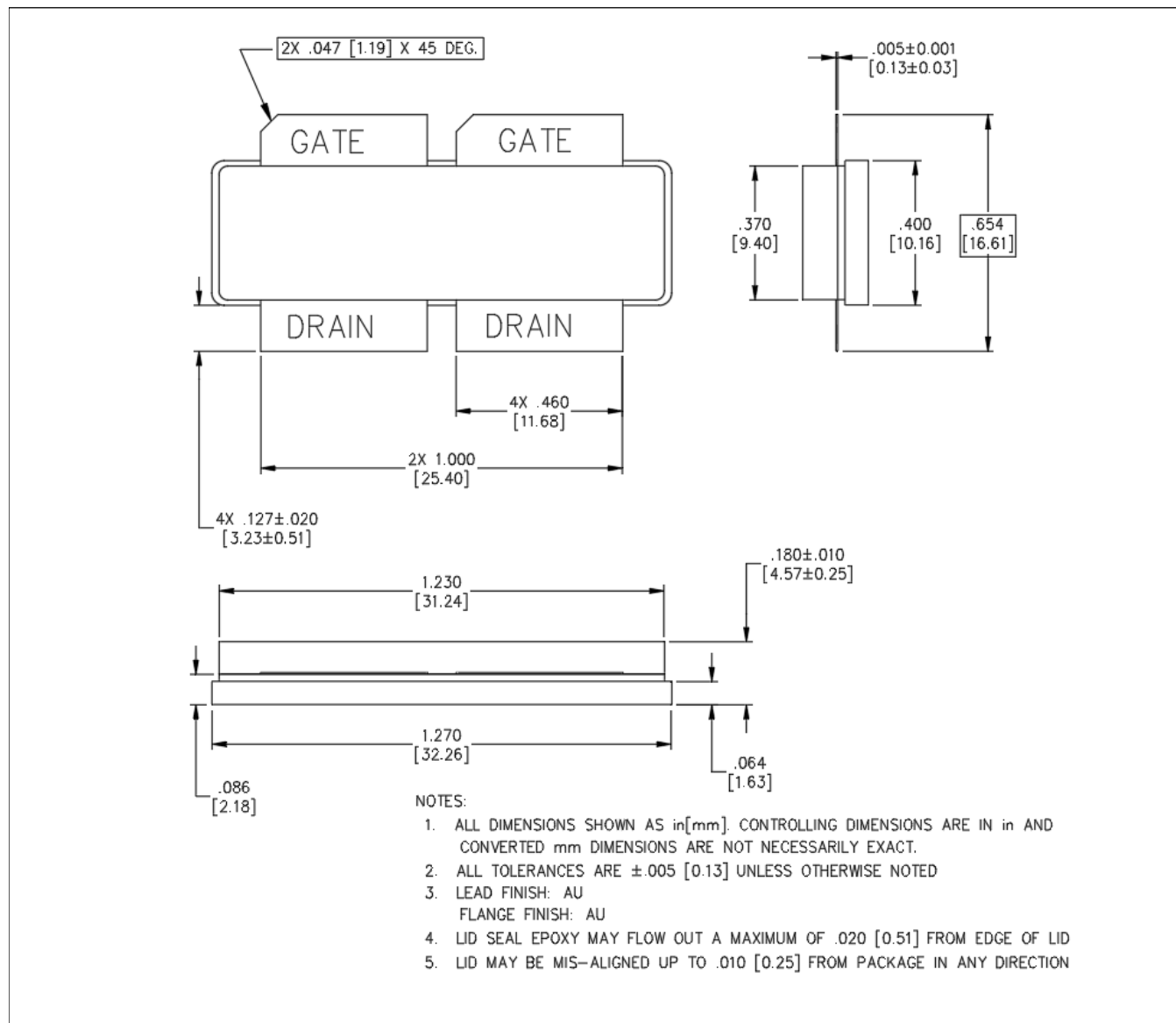
S_{22} (dB) vs Freq (GHz) vs V_{ds} (V)



S_{22} (dB) vs Freq (GHz) vs I_{dq} (mA)

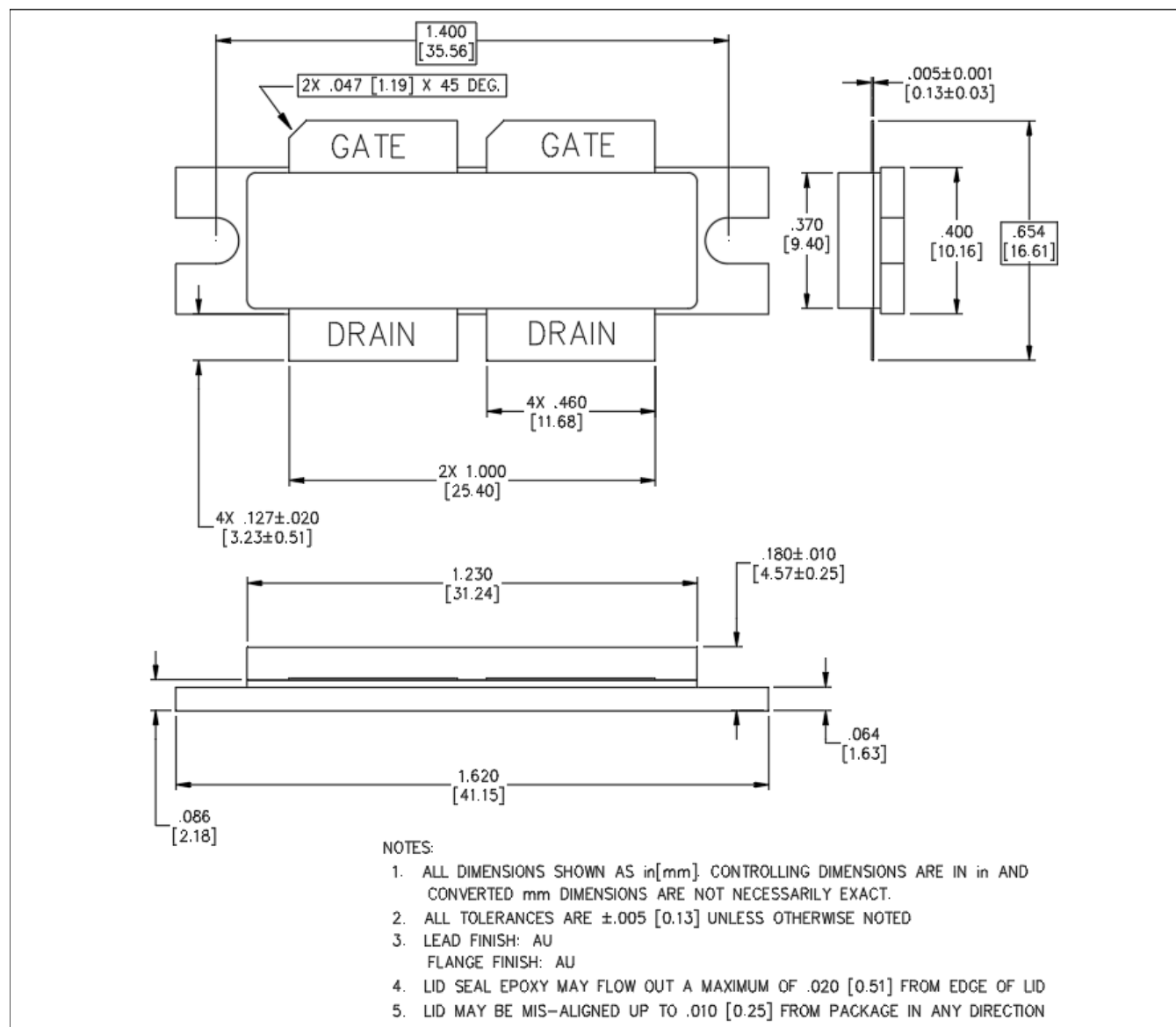


Lead-Free AC-1230S-4 Package Dimensions[†]



[†] Reference Application Note AN0004363 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is Au.

Lead-Free AC-1230B-4 Package Dimensions[†]



[†] Reference Application Note AN0004363 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is Au.

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