MADP-000504-10720T

Non Magnetic MELF PIN Diode

Features
- High Power Handling
- Low Loss / Low Distortion
- Leadless Low Inductance MELF Package
- Non-Magnetic
- Surface Mountable
- RoHS Compliant
- MSL 1

Description
M/A-COM Technology Solutions product line of MELF, PIN diodes, encompass a comprehensive range of electrical characteristics. The chip used in the MADP-000504-10720T is manufactured using a unique, CERMACHIP, passivation process which provides for a hard glass encapsulation that protects and hermetically seals the active area of the chip. This packaged, CERMACHIP, PIN diode is ideally suited for use in applications where high RF and DC voltages are present.

The chip is enclosed in a rugged, ceramic, Metal Electrode Leadless Faced (MELF), surface mount package that is full face bonded to refractory metal plugs on both the anode and cathode. The result is a low loss PIN diode with low thermal resistance due to its symmetrical thermal paths. MELF PIN diodes are designed specifically for high volume tape and reel assembly. Their user friendly design provides for extremely easy, automatic, pick and place, indexing and assembly. All solderable surfaces are tin plated and are compatible with all industry standard reflow and vapor phase soldering processes.

Applications
The MADP-000504-10720T MELF is well suited for use in low loss, low distortion, UHF and VHF high power switching circuits. It is specifically designed to operate in high magnetic fields and to tune or protect RF coils in MRI circuits. This device is designed to meet the most demanding electrical and mechanical environments.

Absolute Maximum Ratings @ 25°C

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Forward Voltage1,2</td>
<td>1.0 V</td>
</tr>
<tr>
<td>Reverse Voltage1,2</td>
<td>500V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-65°C to +175°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +200°C</td>
</tr>
<tr>
<td>Mounting Temperature</td>
<td>+260°C for 30 seconds</td>
</tr>
</tbody>
</table>

1. Exceeding these limits may cause permanent damage to the device.
2. Values will de-rate linearly over temperature.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Packaging</th>
<th>Quantity</th>
</tr>
</thead>
<tbody>
<tr>
<td>MADP-000504-10720T</td>
<td>Tape and reel</td>
<td>1500 pcs</td>
</tr>
</tbody>
</table>

Tape and reel information can be found in Application Note M513 on the M/A-COM website.
## Electrical Specifications @ T_A = +25°C

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Symbols</th>
<th>Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Total Capacitance (^1)</td>
<td>C_T100V</td>
<td>-100 V @ 1 MHz</td>
<td>pF</td>
<td>—</td>
<td>—</td>
<td>0.50</td>
</tr>
<tr>
<td>Package Capacitance</td>
<td>C_p</td>
<td>—</td>
<td>pF</td>
<td>0.16</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Series Resistance</td>
<td>R_S</td>
<td>+100 mA @ 100 MHz</td>
<td>Ω</td>
<td>—</td>
<td>—</td>
<td>0.60</td>
</tr>
<tr>
<td>Minority Carrier Lifetime</td>
<td>T_L</td>
<td>I_F = +10 mA / I_R = -6 mA</td>
<td>µS</td>
<td>—</td>
<td>1.0</td>
<td>—</td>
</tr>
<tr>
<td>Forward Voltage</td>
<td>V_F</td>
<td>+ 50 mA</td>
<td>V</td>
<td>—</td>
<td>—</td>
<td>1.0</td>
</tr>
<tr>
<td>Reverse Leakage Current</td>
<td>-I_R</td>
<td>-500 V</td>
<td>nA</td>
<td>—</td>
<td>—</td>
<td></td>
</tr>
<tr>
<td>Thermal Resistance (^2)</td>
<td>θ</td>
<td>—</td>
<td>°C/W</td>
<td>16</td>
<td>20</td>
<td>—</td>
</tr>
<tr>
<td>Power Dissipation (^2,3)</td>
<td>P_d</td>
<td>—</td>
<td>W</td>
<td>—</td>
<td>7.5</td>
<td>—</td>
</tr>
<tr>
<td>Thermal Resistance (^4)</td>
<td>θ</td>
<td>—</td>
<td>°C/W</td>
<td>48</td>
<td>51</td>
<td>—</td>
</tr>
<tr>
<td>Power Dissipation (^4,5)</td>
<td>P_d</td>
<td>—</td>
<td>W</td>
<td>—</td>
<td>—</td>
<td>2.9</td>
</tr>
<tr>
<td>I Region Thickness</td>
<td>—</td>
<td>—</td>
<td>µm</td>
<td>44</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>3rd Order Input Intermodulation Distortion</td>
<td>IIP3</td>
<td>Power In = +10dBm</td>
<td>dBm</td>
<td>—</td>
<td>&gt;54</td>
<td>—</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Freq. = 1000 MHz</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Spacing = 1 MHz</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

1. Total capacitance Ct = C_j (Chip Junction Capacitance) + C_p (Parasitic Package Capacitance)
2. Diode attached to an infinite heatsink.
3. De-rate linearly by -50 mW/°C to 0W @ +125 °C.
4. Diode in air.
5. De-rate linearly by -19.3 mW/°C to 0W @ +125 °C

### Typical DC Performance Curves

**Series Resistance vs. Forward Current**

![Series Resistance vs. Forward Current graph]

**Capacitance vs. Reverse Voltage**

![Capacitance vs. Reverse Voltage graph]
Non Magnetic MELF PIN Diode

Typical RF Performance Curves (50 – 4000 MHz)

MOUNTED IN A SERIES CONFIGURATION

Input Return Loss

Output Return Loss

Insertion Loss

Isolation

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PRELIMINARY: Data Sheets contain information regarding a product M/A-COM has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.
Typical RF Performance Curves (50 – 4000 MHz)

MOUNTED IN A SHUNT CONFIGURATION

**Input Return Loss**

**Output Return Loss**

**Insertion Loss**

**Isolation**
Non Magnetic MELF PIN Diode

MADP-000504-10720T

Package Outline (1072 MELF)

Cathode

Solderable Surfaces

Circuit Pad Layout

<table>
<thead>
<tr>
<th>Dimension</th>
<th>Package Style 1072</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>inches</td>
</tr>
<tr>
<td>A</td>
<td>0.093</td>
</tr>
<tr>
<td>B</td>
<td>0.050</td>
</tr>
<tr>
<td>C</td>
<td>0.060</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Dimension</th>
<th>INCHES</th>
<th>MM</th>
</tr>
</thead>
<tbody>
<tr>
<td>MIN.</td>
<td>MAX.</td>
<td>MIN.</td>
</tr>
<tr>
<td>A</td>
<td>0.080</td>
<td>0.095</td>
</tr>
<tr>
<td>B</td>
<td>0.115</td>
<td>0.125</td>
</tr>
<tr>
<td>C</td>
<td>0.008</td>
<td>0.023</td>
</tr>
</tbody>
</table>

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Visit www.macom.com for additional data sheets and product information.

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Assembly Recommendations

- Devices may be soldered using standard Sn63/Pb37 or any RoHS compliant solder. Leads are bright tin plated to a minimum thickness of 50μm to ensure an optimum connection.
- For recommended Sn/Pb and RoHS soldering time/temperature profiles. See Application Note M538 on the M/A-COM Technology Solutions website.

Handling Procedures

The following precautions should be observed to avoid damaging these devices.

Cleanliness and Storage

MELF devices should be handled and stored in a clean environment. The metalized ends of the device are tin plated for greater solderability and any continuous exposure to high humidity (>80%) for extended periods of time may cause the surface to oxidize. Caution should be taken when storing devices for extended intervals.

ESD

These devices are susceptible to ESD and are rated Class 1C.

RoHS

The MADP-000504-10720T is fully RoHS compliant meaning it contains less than the maximum allowable concentration of 0.1%, by weight, in homogenous materials for lead, hex chrome, mercury, PBB, PBDE, and 0.01% for cadmium.

Mounting Techniques

Solder Attach

Typical wave soldering or reflow techniques may be used to mount MELF packages to circuit boards. Alloys such as Sn63/Pb37 or any RoHS compliant solder may be used. For more information visit the M/A-COM Tech website and refer to application note M538.

General Handling

Device can be handled with tweezers or vacuum pickups and are suitable for use with automatic pick-and-place equipment.

Note: Click links below to view datasheets of other MELF and packaged PIN diodes.

PACKAGED_PIN_DIODES and/or MA4P MELF & HIPAX Series