

Rev. V2

Features

• Peak Power Handling: 4 W @ 40 GHz

CW Power Handling: 2 W

• Low Insertion Loss: 0.8 dB @ 40 GHz

Flat Leakage Power: 15 dBmDie size: 1.77 x 0.87 x 0.10 mm

Passive DeviceRoHS* Compliant

Applications

- Receiver Protection
- Radar Systems
- · Radio Frequency Front-End Modules

Description

MADL-011122-DIE is a fully integrated diode limiter. It is a passive device, DC decoupled at both input and output RF ports.

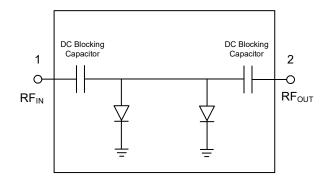
The limiter can handle 4 W peak power at 40 GHz with a low flat leakage of 15 dBm.

MADL-011122-DIE is available in die form. It is ideally suited for high frequency, high peak power receiver protection.

Ordering Information

Part Number	Package	
MADL-011122-DIE	Gel-Pak	
MADL-011122-SMB	Sample Board	

Functional Schematic



Pin Configuration

Pin#	Function		
1	RF Input		
2	RF Output		
Backside	Ground ¹		

^{1.} The entire exposed pad on the die bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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Electrical Specifications: $T_A = +25$ °C, $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	18 GHz 22 GHz 34 GHz 40 GHz	dB	_	0.3 0.4 0.5 0.8	_
Input & Output Return Loss	18 GHz 22 GHz 34 GHz 40 GHz	dB	l	20 18 10 18	_
Input IP3	15 dBm per Tone, 10 MHz Offset, 30 GHz	dBm	_	47	_
CW Power Handling	_	dBm	_	33	_
CW Flat Leakage	30 GHz	dBm	_	15	_
CW P1dB	30 GHz	dBm	_	17	_
Pulsed Peak Power Handling	1 μs PW, 10% Duty Cycle	dBm		36	_
Spike Leakage Power	1 μs PW, 10% DC, 33 dBm Input 18 GHz 27 - 40 GHz	dBm	_	14 10	_
Spike Leakage Energy	1 μs PW, 10% DC, 33 dBm Input 18 GHz 27 - 40 GHz	ergs	_	2.3e-3 0.6e-3	_
1 dB Recovery Time	1 μs PW, 10% DC, 33 dBm Input	ns	_	40	_
3 dB Recovery Time	1 μs PW, 10% DC, 33 dBm Input	ns	_	30	_

Absolute Maximum Ratings^{2,3}

	_		
Parameter	Absolute Maximum		
CW Incident Power	34 dBm @ +85°C		
Peak Incident Power	37 dBm @ +85°C		
Junction Temperature ⁴	+150°C		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-55°C to +150°C		

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- Operating at nominal conditions with T_J ≤ +150°C will ensure MTTF > 1 x 10⁶ hours.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

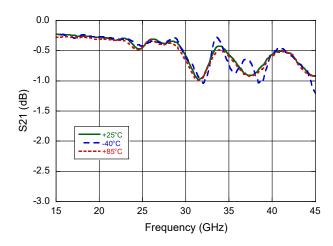
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



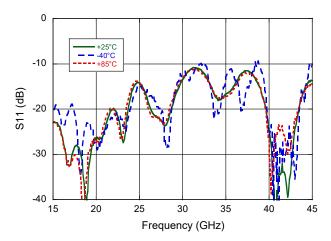
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Typical Small-Signal Performance, De-embedded Die On-Board: $Z_0 = 50 \Omega$

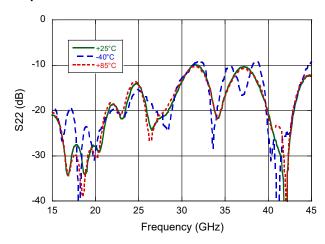
Insertion Loss



Input Return Loss



Output Return Loss

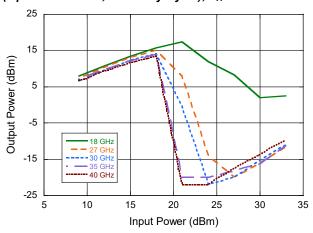




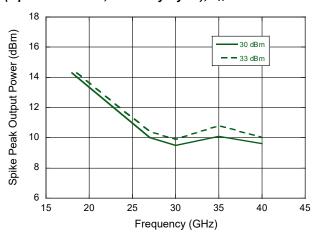
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Typical RF Power Performance, De-embedded Die On-Board: $Z_0 = 50 \Omega$

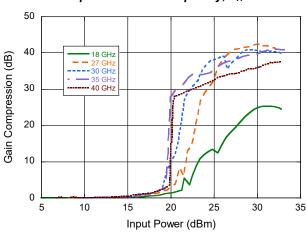
Pulsed Flat Leakage Power over Frequency (1 μ s Pulse Width, 10% Duty Cycle), $T_A = 25^{\circ}$ C



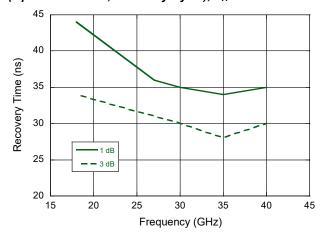
Pulsed Spike Peak Power over Input Power (1 μ s Pulse Width, 10% Duty Cycle), T_A = 25°C



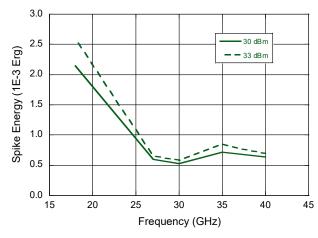
CW Gain Compression over Frequency, $T_A = 25$ °C



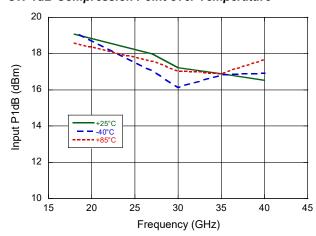
1dB and 3dB Recovery time at 33 dBm Input Power (1 μ s Pulse Width, 10% Duty Cycle), T_A = 25°C



Pulsed Spike Energy Power over Input Power (1 μ s Pulse Width, 10% Duty Cycle), T_A = 25°C



CW 1dB Compression Point over Temperature



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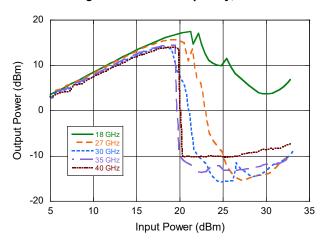
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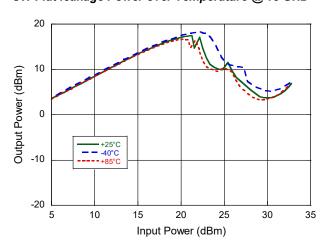
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Typical RF Power Performance, De-embedded Die On-Board: $Z_0 = 50 \Omega$

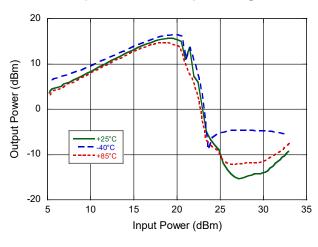
CW Flat leakage Power over Frequency, $T_A = 25$ °C



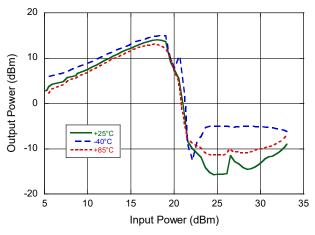
CW Flat leakage Power over Temperature @ 18 GHz



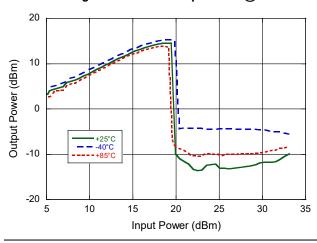
CW Flat leakage Power over Temperature @ 27 GHz



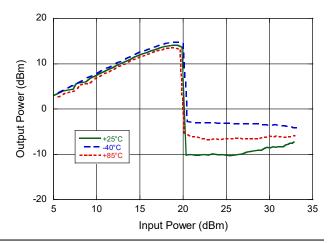
CW Flat leakage Power over Temperature @ 30 GHz



CW Flat leakage Power over Temperature @ 35 GHz



CW Flat leakage Power over Temperature @ 40 GHz



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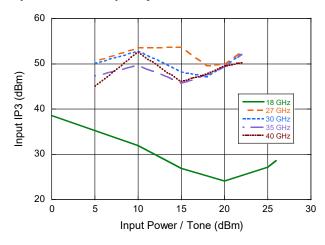
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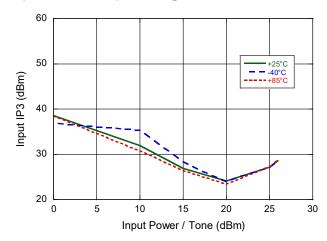
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Typical RF Power Performance, De-embedded Die On-Board: $Z_0 = 50 \Omega$

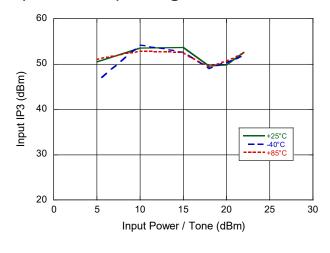
Input IP3 over Frequency, $T_A = 25^{\circ}C$



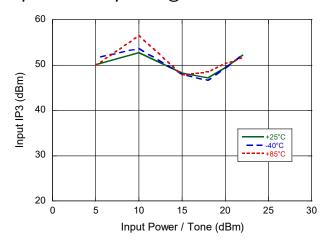
Input IP3 over temperature @ 18 GHz



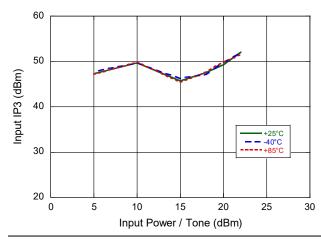
Input IP3 over temperature @ 27 GHz



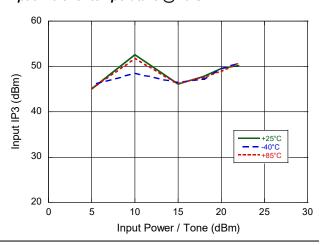
Input IP3 over temperature @ 30 GHz



Input IP3 over temperature @ 35 GHz



Input IP3 over temperature @ 40 GHz



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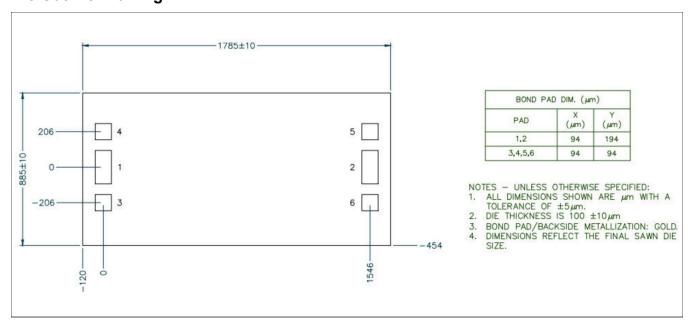
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Die Outline Drawing



Diode Limiter 18 - 40 GHz



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