Voltage Variable Attenuator
5 - 45 GHz

Features
- 5 - 45 GHz Frequency Range
- 1.5 dB Insertion Loss @ 20 GHz
- >30 dB Attenuation Range
- High Linearity, 30 dBm IIP3
- Lead-Free 3 mm, 16-Lead QFN Package
- RoHS* Compliant

Description
The MAAV-011013 is a voltage variable attenuator with analog control and greater than 30 dB of attenuation. Excellent linearity is maintained over the full attenuation range. The attenuation level is set by two control voltages of 0 to -2 V. This device is assembled in a lead free 3 mm 16 lead PQFN plastic package.

Applications include transceivers for cellular infrastructure.

Pin Configuration

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>No Connection</td>
</tr>
<tr>
<td>2</td>
<td>Ground</td>
</tr>
<tr>
<td>3</td>
<td>RF Input</td>
</tr>
<tr>
<td>4</td>
<td>Ground</td>
</tr>
<tr>
<td>5</td>
<td>No Connection</td>
</tr>
<tr>
<td>6</td>
<td>Vc1</td>
</tr>
<tr>
<td>7</td>
<td>Vc2</td>
</tr>
<tr>
<td>8</td>
<td>No Connection</td>
</tr>
<tr>
<td>9</td>
<td>Ground</td>
</tr>
<tr>
<td>10</td>
<td>RF Output</td>
</tr>
<tr>
<td>11</td>
<td>Ground</td>
</tr>
<tr>
<td>12 - 16</td>
<td>No Connection</td>
</tr>
</tbody>
</table>

3. It is recommended to connect unused pins to ground.
4. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAAV-011013-TR0500</td>
<td>500 Part Reel</td>
</tr>
<tr>
<td>MAAV-011013-TR1000</td>
<td>1000 Part Reel</td>
</tr>
<tr>
<td>MAAV-011013-001SMB</td>
<td>Sample Board</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

Functional Block Diagram

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Electrical Specifications: $T_A = +25^\circ C$, $Z_0 = 50 \ \Omega$, $P_{IN} = -10 \ \text{dBm}$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss ($V_{C1}$ and $V_{C2} = -2 \ \text{V}$)</td>
<td>5.9 -15.5 GHz, 17.6 - 20 GHz, 20 - 30 GHz, 30 - 34 GHz, 37 - 40 GHz</td>
<td>dB</td>
<td>1.5</td>
<td>1.5</td>
<td>4.0</td>
</tr>
<tr>
<td>Attenuation ($V_{C1}$ and $V_{C2} = 0 \ \text{V}$)</td>
<td>5.9 - 8.5 GHz, 10 - 11.7 GHz, 12.75-15.35 GHz, 17.6 - 20 GHz, 20 - 30 GHz, 30 - 34 GHz, 37 - 40 GHz</td>
<td>dB</td>
<td>22.5</td>
<td>25.0</td>
<td>—</td>
</tr>
<tr>
<td>Input P1dB</td>
<td>5 - 25 GHz, 25 - 40 GHz</td>
<td>dBm</td>
<td>24</td>
<td>22</td>
<td>—</td>
</tr>
<tr>
<td>IIP3 (any attenuation)</td>
<td>$P_{IN} = 12 \ \text{dBm/tone} @ 5.0 - 15.0 \ \text{GHz}$, $P_{IN} = 12 \ \text{dBm/tone} @ 15.0 - 26.5 \ \text{GHz}$, $P_{IN} = 12 \ \text{dBm/tone} @ 26.5 - 40.0 \ \text{GHz}$</td>
<td>dBm</td>
<td>29.0</td>
<td>31.0</td>
<td>—</td>
</tr>
<tr>
<td>IIP3 ($V_{C1}=V_{C2}=-2 \ \text{V}$)</td>
<td>$P_{IN} = 12 \ \text{dBm/tone} @ 5 - 40 \ \text{GHz}$</td>
<td>dBm</td>
<td>—</td>
<td>42</td>
<td>—</td>
</tr>
<tr>
<td>Input Return Loss (any attenuation)</td>
<td>—</td>
<td>dB</td>
<td>—</td>
<td>10</td>
<td>—</td>
</tr>
<tr>
<td>Output Return Loss (any attenuation)</td>
<td>—</td>
<td>dB</td>
<td>—</td>
<td>10</td>
<td>—</td>
</tr>
</tbody>
</table>

5. To increase attenuation from minimum attenuation state ($V_{C1} = -2 \ \text{V}$ and $V_{C2} = -2 \ \text{V}$) to max attenuation state ($V_{C1} = 0 \ \text{V}$ and $V_{C2} = 0 \ \text{V}$), $V_{C1}$ increases to full range prior to adjusting $V_{C2}$.
6. Guaranteed on MACOM Sample Board only

Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power</td>
<td>30 dBm</td>
</tr>
<tr>
<td>Voltage (RF pins)</td>
<td>30 V</td>
</tr>
<tr>
<td>Voltage (control pins)</td>
<td>+1 V to -6 V</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-55°C to +150°C</td>
</tr>
<tr>
<td>Case Temperature</td>
<td>-40°C to +85°C</td>
</tr>
</tbody>
</table>

Handling Procedures

The following precautions should be observed to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A devices.

7. Exceeding any one or combination of these limits may cause permanent damage to this device.
8. MACOM does not recommend sustained operation near these survivability limits.
Typical Performance Curves: @ +25°C

**Gain**

-50 -45 -40 -35 -30 -25 -20 -15 -10 -5 0 5 10 15 20 25 30 35 40 45 50

**Frequency (GHz)**

**Input Return Loss**

0 -5 -10 -15 -20 -25 -30 -35 -40 -45 -50

**Frequency (GHz)**

**Output Return Loss**

0 -5 -10 -15 -20 -25 -30 -35 -40

**Frequency (GHz)**

-40
-35
-30
-25
-20
-15
-10
-5
0
0 5 10 15 20 25 30 35 40 45 50

S21 (dB)

Frequency (GHz)

Gain (S21) vs. Freq (GHz)

-40
-35
-30
-25
-20
-15
-10
-5
0
0 5 10 15 20 25 30 35 40 45 50

S11 (dB)

Frequency (GHz)

Input Return Loss S11 (dB) vs. Freq (GHz)

-40
-35
-30
-25
-20
-15
-10
-5
0
0 5 10 15 20 25 30 35 40 45 50

S22 (dB)

Frequency (GHz)

Output Return Loss S22 (dB) vs. Freq (GHz)

-VC1=-2V, VC2=-2V
-VC1=-0.7V, VC2=-2V
-VC1=-0.5V, VC2=-2V
-VC1=-0.3V, VC2=-2V
-VC1=-0.1V, VC2=-2V
-VC1=0V, VC2=-0.8V
-VC1=0V, VC2=-0.6V
-VC1=0V, VC2=-0.4V
-VC1=0V, VC2=-0.2V
-VC1=0V, VC2=0V

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Typical Performance Curves: S-Parameters @ +25°C

S-Parameters $V_{C1} = -2.0 \, \text{V}$, $V_{C2} = -2.0 \, \text{V}$

S-Parameters $V_{C1} = -0.6 \, \text{V}$, $V_{C2} = -2.0 \, \text{V}$

S-Parameters $V_{C1} = -0.4 \, \text{V}$, $V_{C2} = -2.0 \, \text{V}$

S-Parameters $V_{C1} = -0.1 \, \text{V}$, $V_{C2} = -2.0 \, \text{V}$

S-Parameters $V_{C1} = 0 \, \text{V}$, $V_{C2} = -0.6 \, \text{V}$
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Typical Performance Curves: Power Gain, Freq. 16 GHz

Power Gain @ $V_{C1} = -2.0 \, \text{V}, \, V_{C2} = -2.0 \, \text{V}$

Power Gain @ $V_{C1} = -0.4 \, \text{V}, \, V_{C2} = -2.0 \, \text{V}$

Power Gain @ $V_{C1} = 0 \, \text{V}, \, V_{C2} = -2.0 \, \text{V}$

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Typical Performance Curves: Power Gain @ +25°C

Power Gain @ Vc1 = -2.0 V, Vc2 = -2.0 V

Power Gain @ Vc1 = -0.4 V, Vc2 = -2.0 V

Power Gain @ Vc1 = 0 V, Vc2 = -0.6 V

Power Gain @ Vc1 = 0 V, Vc2 = 0 V

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DC-0010352
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Typical Performance Curves: Input IP3

**Input IP3 vs. Frequency**
@ \( V_{C1} = -2.0 \text{ V}, \ V_{C2} = -2.0 \text{ V} \)

**Input IP3 vs. SCL Input Power**
@ \( V_{C1} = -2.0 \text{ V}, \ V_{C2} = -2.0 \text{ V} \)

**Input IP3 vs. Attenuation, SCL \( P_{IN} = 6 \text{ dBm} \)**

**Input IP3 vs. Attenuation, SCL \( P_{IN} = 12 \text{ dBm} \)**
**Lead-Free 3 mm 16-Lead PQFN†**

All DIMENSIONS SHOWN AS INCHES/MM.

† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Plating is NiPdAuAg.
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