### Features
- Fully Integrated Power Amplifier
- Wide Bandwidth 17.7 - 26.5 GHz
- 28.5 dB Small Signal Gain
- 37.0 dBm Third Order Intercept Point (OIP3)
- 28.5 dBm Output P1dB
- Integrated Power Detector
- Typical Bias 5 V, 650 mA
- Lead-Free 5 mm 24-lead QFN Package

### Description
The MAAP-118260 is a packaged linear power amplifier that operates over the frequency range 17.7 - 26.5 GHz. The device provides 28.5 dB of gain and 37.0 dBm Output Third Order Intercept Point (OIP3) with more than 28.5 dBm of Output P1dB.

This power amplifier is assembled in a lead free, fully molded 5 mm, 24 lead, QFN package and consists of a four stage power amplifier with integrated, on-chip power and envelope detectors. The device includes on-chip ESD protection structures to ease the implementation and volume assembly.

The device is well suited for use in the 18 GHz, 23 GHz, 26 GHz cellular backhaul applications.

### Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
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<tbody>
<tr>
<td>MAAP-118260</td>
<td>Bulk</td>
</tr>
<tr>
<td>MAAP-118260-TR0500</td>
<td>Tape and Reel</td>
</tr>
<tr>
<td>MAAP-118260-001SMB</td>
<td>Sample Board</td>
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</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

For further information and support please visit: [https://www.macom.com/support](https://www.macom.com/support)
Power Amplifier
18 - 26 GHz

Electrical Specifications: Freq. = 17.7 - 26.5 GHz, $T_A = 25^\circ$C, $V_D = +5$ V, $Z_0 = 50$ Ω

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
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<tbody>
<tr>
<td>Gain</td>
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<td>dB</td>
<td>25</td>
<td>28.5</td>
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<td></td>
<td>20.0 - 24.0 GHz</td>
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<td>24</td>
<td>25.5</td>
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<td></td>
<td>24.0 - 26.5 GHz</td>
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<td>23</td>
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<td>P1dB, @ 1 dB Compression</td>
<td>17.7 - 20.0 GHz</td>
<td>dBm</td>
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<td>28.0</td>
<td>—</td>
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<td></td>
<td>20.0 - 24.0 GHz</td>
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<td>29.0</td>
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<td>$P_{SAT}$</td>
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<td></td>
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<td></td>
<td>24.0 - 26.5 GHz</td>
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<td>29</td>
<td>30.5</td>
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<td>OIP3</td>
<td>17.7 - 20.0 GHz</td>
<td>dBm</td>
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<td>37.0</td>
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<td>20.0 - 24.0 GHz</td>
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<td>24.0 - 26.5 GHz</td>
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<td>34</td>
<td>36.5</td>
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<td>Input Return Loss</td>
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<td>—</td>
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<td>PAE, @ 1 dB Compression</td>
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<td>%</td>
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<td>—</td>
</tr>
<tr>
<td>Quiescent Current</td>
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<td>mA</td>
<td>590</td>
<td>662</td>
<td></td>
</tr>
</tbody>
</table>

Absolute Maximum Ratings

5. Exceeding any one or combination of these limits may cause permanent damage to this device.
6. MACOM does not recommend sustained operation near these survivability limits.
7. Operating at nominal conditions with $T_J \leq +150^\circ$C will ensure MTTF $> 1 \times 10^6$ hours.

Maximum Operating Ratings

8. Junction temperature directly affects device MTTF. Junction temperature should be kept as low as possible to maximize lifetime. Thermal resistance, $\Theta_{JC}$ is 18.4 °C/W.
9. For saturated performance, it is recommended that the sum of $(2V_{DD} + \text{abs}(V_{GG})) < 12$ V.
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**Biasing**

All gates should be pinched-off, \( V_G < -2 \, \text{V} \), before applying the drain voltage, \( V_D = 5 \, \text{V} \) (do not exceed maximum \( V_{DG} \) value for RF drive condition). Then the gate voltages can be increased until the desired quiescent drain current is reached in each stage. The recommended quiescent bias is \( V_D = 5 \, \text{V} \), \( I_{D1,2} + I_{D3} + I_{D4} = 650 \, \text{mA} \) (total). The performance in this datasheet has been measured with a fixed gate voltage and no drain current regulation under large signal operation. It is also possible to regulate the drain current dynamically, to limit the DC power dissipation under RF drive. To turn off the device, the turn on bias sequence should be followed in reverse.

**Detector Operation**

MAAP-118260 includes dual power and envelope detectors. These are included on both sides of the device to ease integration onto larger radio boards. As per the application schematic, the power detector requires an external 5 V supply and the envelope detector requires -5 V. The output from the resistive voltage divider can be fed into a ADC or multimeter for the result.

**Bias Arrangement**

Each DC pin (\( V_{D1,2}, V_{D3}, V_{D4} \) and \( V_{G1,2}, V_{G3,4} \)) needs to have bypass capacitance of 100 nF mounted as close to the packaged device as possible.

**Handling Procedures**

Please observe the following precautions to avoid damage:

**Static Sensitivity**

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these CDM class C1, HBM Class 0A devices.
Typical Performance Curves: $V_D = 5\, \text{V}$, $I_{DQ} = 0.65\, \text{A}$, $V_G = -1.05 \sim -0.85\, \text{V}$, $T_A = +25^\circ\text{C}$

Broadband S-Parameters vs. Freq (GHz), $V_d = 5\, \text{V}$, $I_d = 0.65\, \text{A}$

Gain ($S_{21}$) vs. Freq (GHz), $V_d = 5\, \text{V}$, $I_d = 0.65\, \text{A}$

Return Loss ($S_{11}$/S22) vs. Freq (GHz), $V_d = 5\, \text{V}$, $I_d = 0.65\, \text{A}$

P1dB/P3dB/Psat (dBm) vs. Freq (GHz), $V_d = 5\, \text{V}$, $I_d = 650\, \text{mA}$

Output IP3 (dBm) vs. SCL Output Power, $V_d = 5\, \text{V}$, $I_d = 650\, \text{mA}$

Output IP3 (dBm) vs. Freq (GHz), $V_d = 5\, \text{V}$, $I_d = 650\, \text{mA}$
Typical Performance Curves: $V_D = 5\,V$, $I_{DQ} = 0.65\,A$, $V_G = -1.05$ ~ $-0.85\,V$, $T_A = +25^\circ C$

Output Power (dBm), Power Gain (dB), Drain Current (mA) vs. Input Power (dBm) @ 17.70GHz, $V_d = 5V$, $I_d = 650mA$

Power Gain (dB) and Power Added Efficiency (%) vs. Output Power (dBm) @ 17.70GHz, $V_d = 5V$, $I_d = 650mA$

Output Power (dBm), Power Gain (dB), Drain Current (mA) vs. Input Power (dBm) @ 19.16GHz, $V_d = 5V$, $I_d = 650mA$

Power Gain (dB) and Power Added Efficiency (%) vs. Output Power (dBm) @ 19.16GHz, $V_d = 5V$, $I_d = 650mA$

Output Power (dBm), Power Gain (dB), Drain Current (mA) vs. Input Power (dBm) @ 20.62GHz, $V_d = 5V$, $I_d = 650mA$

Power Gain (dB) and Power Added Efficiency (%) vs. Output Power (dBm) @ 20.62GHz, $V_d = 5V$, $I_d = 650mA
Typical Performance Curves: $V_D = 5 \text{ V}$, $I_{DQ} = 0.65 \text{ A}$, $V_G = -1.05 \sim -0.85 \text{ V}$, $T_A = +25^\circ \text{C}$

Output Power (dBm), Power Gain (dB), Drain Current (mA) vs. Input Power (dBm) @ 22.08GHz, $V_d = 5\text{ V}$, $I_d = 650\text{ mA}$

Power Gain (dB) and Power Added Efficiency (%) vs. Output Power (dBm) @ 22.08GHz, $V_d = 5\text{ V}$, $I_d = 650\text{ mA}$

Output Power (dBm), Power Gain (dB), Drain Current (mA) vs. Input Power (dBm) @ 23.54GHz, $V_d = 5\text{ V}$, $I_d = 650\text{ mA}$

Power Gain (dB) and Power Added Efficiency (%) vs. Output Power (dBm) @ 23.54GHz, $V_d = 5\text{ V}$, $I_d = 650\text{ mA}$

Output Power (dBm), Power Gain (dB), Drain Current (mA) vs. Input Power (dBm) @ 25.00GHz, $V_d = 5\text{ V}$, $I_d = 650\text{ mA}$

Power Gain (dB) and Power Added Efficiency (%) vs. Output Power (dBm) @ 25.00GHz, $V_d = 5\text{ V}$, $I_d = 650\text{ mA}$
Typical Performance Curves: \( V_D = 5 \text{ V}, \ I_{DQ} = 0.65 \text{ A}, \ V_G = -1.05 \sim -0.85 \text{ V}, \ T_A = +25\degree \text{C} \)

### Output Power (dBm), Power Gain (dB), Drain Current (mA)

- **Output Power (dBm)** vs. **Input Power (dBm)** @ 26.50GHz, \( V_d = 5\text{ V}, \ I_d = 650\text{mA} \)

### Power Gain (dB) and Power Added Efficiency (%)

- **Power Gain (dB)** vs. **Output Power (dBm)** @ 26.50GHz, \( V_d = 5\text{ V}, \ I_d = 650\text{mA} \)

### Envelope Detector Voltage (V) vs. Output Power

- **Envelope Detected Voltage (V)** vs. **Output Power (dBm)**,
  \( V_d = 5\text{ V}, \ I_d = 650\text{mA} \)

### Peak Detector Voltage (V) vs. Output Power

- **Peak Detected Voltage (V)** vs. **Output Power (dBm)**,
  \( V_d = 5\text{ V}, \ I_d = 650\text{mA} \)

### 3rd Order Intermodulation (dBm) vs. SCL Output Power

- **IMD3 (dBm)** vs. **SCL Output Power**,
  \( V_d = 5\text{ V}, \ I_d = 650\text{mA} \)
Typical Performance Curves: \( V_D = 5 \, V \), \( I_{DQ} = 0.65A \), \( V_G = -1.05 \sim -0.85 \, V \), \( T_A = -40^\circ C \sim +85^\circ C \)

- **S21 (dB) vs. Freq (GHz)**, \( V_d = 5V \), \( I_d = 0.65A \)
- **S11 (dB) vs. Freq (GHz)**, \( V_d = 5V \), \( I_d = 0.65A \)
- **S22 (dB) vs. Freq (GHz)**, \( V_d = 5V \), \( I_d = 0.65A \)
- **P1dB (dBm) vs. Frequency (GHz)**, \( V_d = 5V \), \( I_d = 650mA \)
- **P3dB (dBm) vs. Frequency (GHz)**, \( V_d = 5V \), \( I_d = 650mA \)
- **Psat (dBm) vs. Frequency (GHz)**, \( V_d = 5V \), \( I_d = 650mA \)
Power Amplifier
18 - 26 GHz

Typical Performance Curves: $V_D = 5\, \text{V}$, $I_{DQ} = 0.65\, \text{A}$, $V_G = -1.05 \sim -0.85\, \text{V}$, $T_A = -40^\circ\text{C} \sim +85^\circ\text{C}$

Output IP3 (dBm) @ 19dBm SCL O/P Power vs. Freq (GHz), $V_d = 5\, \text{V}, \, I_d = 650\, \text{mA}$

Output IP3 (dBm) @ 21dBm SCL O/P Power vs. Freq (GHz), $V_d = 5\, \text{V}, \, I_d = 650\, \text{mA}$

Output IP3 (dBm) @ 24dBm SCL O/P Power vs. Freq (GHz), $V_d = 5\, \text{V}, \, I_d = 650\, \text{mA}$

Typical Performance Curves: $V_D = 5\, \text{V}$, $I_{DQ} = \text{Various}$, $V_G = -0.85 \sim -1.65\, \text{V}$, $T_A = +25^\circ\text{C}$

Gain ($S_{21}$) vs. Freq (GHz), Various Bias Points

Input Return Loss ($S_{11}$) vs. Freq (GHz), Various Bias Points
Typical Performance Curves: $V_D = 5\, V$, $I_{DQ} = \text{Various}$, $V_G = -0.85 \sim -1.65\, V$, $T_A = +25^\circ\text{C}$

Output Return Loss ($S_{22}$) vs. Freq (GHz), Various Bias Points

Output IP3 (dBm) vs. Drain Current (mA), $V_d = 5\, V$, $I_d = \text{Various}$

Gain (dB) vs. Drain Current (mA), $V_d = 5\, V$, $I_d = \text{Various}$

Output IP3 (dBm) vs. Gain (dB), $V_d = 5\, V$, $I_d = \text{Various}$
Lead-Free 5 mm 24-Lead PQFN†

† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is NiPdAu
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