4 W Power Amplifier
7.1 - 7.9 GHz

Features
- 22.0 dB Small Signal Gain
- 46.5 dBm Third Order Intercept Point (OIP3)
- >36.5 dBm Saturated Output Power (P_{SAT})
- Bias 2000 mA at 8 V
- Lead-Free 7mm Copper Coin Air Cavity Package
- RoHS* Compliant

Description
The MAAP-011161 is a packaged linear power amplifier that operates from 7.1 - 7.9 GHz. The device provides 22 dB gain and 46.5 dBm Output Third Order Intercept Point (OIP3) with >35.5 dBm saturated output power (P_{SAT}).

The packaged amplifier comes in an air cavity 7 mm surface mount package with a copper coin paddle and is comprised of a two stage power amplifier MMIC. The device includes on-chip ESD protection structures and DC by-pass capacitors to ease the implementation and volume assembly of the packaged part.

The device is specifically designed for use in 7 GHz point-to-point radios for cellular backhaul applications.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAAP-011161</td>
<td>Bulk Quantity</td>
</tr>
<tr>
<td>MAAP-011161-TR0500</td>
<td>500 Piece Reel</td>
</tr>
<tr>
<td>MAAP-011161-001SMB</td>
<td>Sample Board</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.

4 W Power Amplifier
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Electrical Specifications:  Freq. = 7.1 - 7.9 GHz, $V_D = 8$ V, $I_{DQ}^4 = 2000$ mA, $T_A = +25^\circ C$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Small Signal Gain</td>
<td>dB</td>
<td>18.5</td>
<td>22</td>
<td>23.5</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>dB</td>
<td>—</td>
<td>12</td>
<td>—</td>
</tr>
<tr>
<td>Output Return Loss</td>
<td>dB</td>
<td>7</td>
<td>12</td>
<td>—</td>
</tr>
<tr>
<td>Power at 1dB Gain Compression, P1dB</td>
<td>dB</td>
<td>—</td>
<td>35.5</td>
<td>—</td>
</tr>
<tr>
<td>Power at 3dB Gain Compression, P3dB</td>
<td>dBm</td>
<td>—</td>
<td>36</td>
<td>—</td>
</tr>
<tr>
<td>Saturated Output Power, $P_{SAT}$</td>
<td>dBm</td>
<td>35.5</td>
<td>36.5</td>
<td>—</td>
</tr>
<tr>
<td>Output IP3, 25.5 dBm SCL @ Freq = 7.5 GHz</td>
<td>dBm</td>
<td>44.5</td>
<td>46.5</td>
<td>—</td>
</tr>
<tr>
<td>Drain Bias voltage</td>
<td>V</td>
<td>—</td>
<td>8.0</td>
<td>—</td>
</tr>
<tr>
<td>Drain Current</td>
<td>mA</td>
<td>—</td>
<td>2000</td>
<td>—</td>
</tr>
<tr>
<td>Gate Voltage</td>
<td>V</td>
<td>-1.5</td>
<td>—</td>
<td>-0.5</td>
</tr>
</tbody>
</table>

4. Adjust $V_{G1}$ and $V_{G2}$ between $–1.2$ and $–0.7$V to achieve specified $I_{DQ}$ ($I_{D1} + I_{D2}$). $V_{G1}$ and $V_{G2}$ should be the same voltage.

Absolute Maximum Ratings$^5,6,7$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power</td>
<td>18 dBm</td>
</tr>
<tr>
<td>Drain Voltage ($V_{D1,2}$)</td>
<td>+9 V</td>
</tr>
<tr>
<td>Gate Voltage ($V_{G1,2}$)</td>
<td>-3 V</td>
</tr>
<tr>
<td>Continuous Power Dissipation @ 85ºC</td>
<td>33.3 W</td>
</tr>
<tr>
<td>Junction Temperature (max.)</td>
<td>+175ºC</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+150ºC</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40ºC to +85ºC</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65ºC to +150ºC</td>
</tr>
</tbody>
</table>

Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1A HBM devices.

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Recommended PCB Layout

Schematic

Parts List

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C4, C7, C9, C12</td>
<td>2.2 µF</td>
<td>0603</td>
</tr>
<tr>
<td>C3, C10</td>
<td>0.47 µF</td>
<td>0603</td>
</tr>
<tr>
<td>C2, C5, C6, C8, C11</td>
<td>1.0 nF</td>
<td>0603</td>
</tr>
</tbody>
</table>
Typical Performance Curves: $V_D = 8\, V$, $I_{DQ} = 2\, A$, $V_G = -1.05 \sim -0.95\, V$, $T_A = +25^\circ$C

Broadband Gain ($S_{21}$) vs. Freq (GHz), $V_D = 8V$, $I_d = 2A$

Gain ($S_{21}$) vs. Freq (GHz), $V_D = 8V$, $I_d = 2A$

IP/OP Return Loss ($S_{11}/S_{22}$) vs. Freq (GHz), $V_D = 8V$, $I_d = 2A$

P1dB/P3dB/Psat (dBm) vs. Freq (GHz), $V_D = 8V$, $I_d = 2A$

Output IP3 (dBm) vs. SCL O/P Pwr (dBm), $V_D = 8V$, $I_d = 2A$

Output IP3 (dBm) vs. SCL O/P Pwr (dBm), $V_D = 8V$, $I_d = 2A$
Typical Performance Curves: $V_D = 8 \, V$, $I_{DQ} = 2 \, A$, $V_G = -1.05 \sim -0.95 \, V$, $T_A = +25^\circ C$
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Typical Performance Curves: \( V_D = 8 \, \text{V}, \, I_{DQ} = 2 \, \text{A}, \, V_G = -1.05 \sim -0.95 \, \text{V}, \, T_A = -40^\circ \text{C} \sim +85^\circ \text{C} \)

- **P3dB Power (dBm) over Temp vs. Freq (GHz)**
  - \( V_D = 8\, \text{V}, \, I_{DQ} = 2\, \text{A} \)
  - Graph showing power levels at different temperatures across a frequency range of 7.1 GHz to 7.9 GHz.

- **P1dB Power (dBm) over Temp vs. Freq (GHz)**
  - \( V_D = 8\, \text{V}, \, I_{DQ} = 2\, \text{A} \)
  - Graph showing power levels at different temperatures across a frequency range of 7.1 GHz to 7.9 GHz.

- **Power Gain (dB) over Temp vs. Output Power (dBm)**
  - \( V_D = 8\, \text{V}, \, I_{DQ} = 2\, \text{A} \)
  - Graph showing gain levels at different temperatures across a range of output powers.

- **Output IP3 at 17.5dBm SCL O/P Pwr (dBm) over Temp vs. Freq (GHz)**
  - \( V_D = 8\, \text{V}, \, I_{DQ} = 2\, \text{A} \)
  - Graph showing output power levels at different frequencies and temperatures.

- **Output IP3 at 25.5dBm SCL O/P Pwr (dBm) over Temp vs. Freq (GHz)**
  - \( V_D = 8\, \text{V}, \, I_{DQ} = 2\, \text{A} \)
  - Graph showing output power levels at different frequencies and temperatures.

- **Output IP3 at 28.5dBm SCL O/P Pwr (dBm) over Temp vs. Freq (GHz)**
  - \( V_D = 8\, \text{V}, \, I_{DQ} = 2\, \text{A} \)
  - Graph showing output power levels at different frequencies and temperatures.
Typical Performance Curves: \( V_D = \text{Various}, \ I_{DQ} = 2 \ \text{A}, \ V_G = -1.05 \sim -0.95 \ \text{V}, \ T_A = +25^\circ\text{C} \)
Typical Performance Curves:

\( V_D = \text{Various}, \ I_{DO} = 1.5 \ A, 1.65 \ A, 2.0 \ A, \ V_G = -1.15 \sim -0.95 \ V, \ T_A = +25^\circ C \)

![Graphs showing various performance curves for the 4 W Power Amplifier from 7.1 to 7.9 GHz.]
Typical Performance Curves: $V_D = 8\,V$, $I_{DQ} = \text{Various}$, $V_G = -0.9\,\sim\,1.65\,V$, $T_A = +25^\circ C$
4 W Power Amplifier
7.1 - 7.9 GHz

Lead Free 7 mm Laminate Package (16 pin)

† Meets JEDEC moisture sensitivity level 3 requirements.

1. All Dimensions as mm.
2. Dimension Tolerance ±0.05mm.
3. Plating is Nickel/Palladium/Gold over Copper
4. Reference Application Note S2083 for lead-free solder reflow recommendations.

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