Low Noise Amplifier 22 - 48 GHz



MAAL-011246-DIE Rev. V1

Features

Noise Figure: 1.5 dB

Gain: 27 dB

Output P1dB: 5 dBm

DC Current Regulation Integrated

Bias Current: 50 mA DC

Bias Voltage:

Drain = +1.5 VGate = -1.5 V

Die Size: 1500 x 1000 x 100 μm

RoHS* Compliant

Applications

Radar

SATCOM

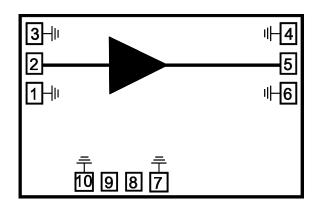
Description

The MAAL-011246-DIE is a very low noise 3 stage LNA designed to operate from 22 to 48 GHz with 1.5 dB of noise figure and 27 dB of gain. This amplifier is a single positive and single negative voltage bias which include a DC current regulation.

This LNA is matched to 50 Ω at both input and output ports.

The MAAL-011246-DIE is manufactured using a high performance 70 nm gate length GaAs HEMT technology (D007IH). The MMIC uses gold bonding pads and backside metallization and is fully protected with silicon nitride passivation to obtain the highest level of reliability.

Block Diagram



Pad Configuration

Pad #	Function	Function		
1,3,4,6,7,10	GND	Ground		
2	RF _{IN}	RF Input		
5	RF _{OUT}	RF Output		
8	V _D	Voltage Drain		
9	V_{G}	Voltage Gate		

Ordering Information

Part Number	Package
MAAL-011246-DIE	DIE
MAAL-011246-SB2	Sample Board

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



Electrical Specifications: Freq. = 22 - 48 GHz, V_D = +1.5 V, V_G = -1.5 V

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Noise Figure	@ T _A and nominal conditions	dB	_	1.5	2
Gain	@ T _A and nominal conditions	dB	24	27	30
Output P1dB	@ T _A and nominal conditions	dBm	_	5	_
Input Return Loss	@ T _A and nominal conditions	dB	_	-12	-5
Output Return Loss	@ T _A and nominal conditions	dB	_	-10	-5
Current	Total DC current included DC current regulation	mA	_	50	75

Recommended Operating Conditions

Parameter	Typical
Input RF ports	-35 dBm
DC Supply V _D	+1.5 V to +2.0 V
DC Supply V _G	-2.0 V to -1.5 V

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 0 devices.

Absolute Maximum Ratings^{1,2}

Parameter	Absolute Maximum
Input RF ports	15 dBm
DC Voltage Drain Supply	2 V
DC Voltage Gate Supply	-2 V
Junction Temperature ^{3,4}	+150°C
Operating Temperature ³	-40°C to +85°C
Storage Temperature	-40°C to +150°C

- 1. Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- 3. Operating at nominal conditions with $T_J \le +150^{\circ} C$ will ensure MTTF > 1 x 10^{11} hours.
- Junction Temperature (T_J) = T_C + Θjc * (V * I)
 Typical thermal resistance (Θjc) = 283 °C/W.
 - a) For $T_C = +25^{\circ}C$,

 $T_J = 55.8$ °C @ 1.5 V, 55 mA

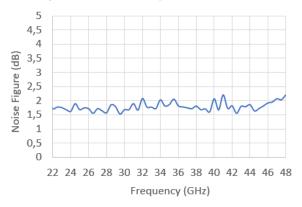
b) For $T_C = +85^{\circ}C$,

T_J = 121.5°C @ 1.5 V, 55 mA

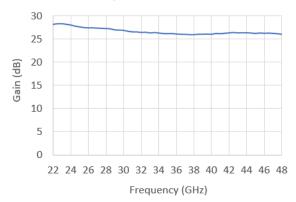


Typical performance on Wafer curves at T_C = 25°C

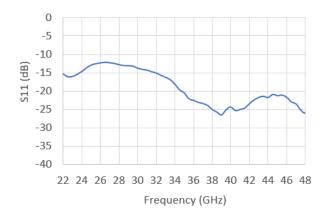
Noise Figure over Frequency



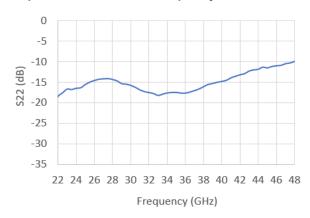
Gain over Frequency



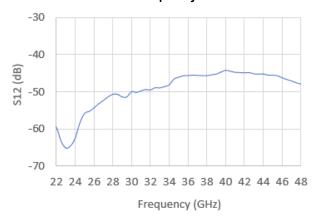
Input Return Loss over Frequency



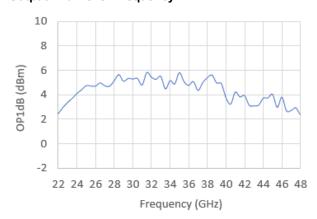
Output Return Loss over Frequency



Reverse Isolation over Frequency



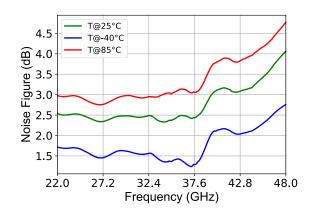
Output P1dB over Frequency



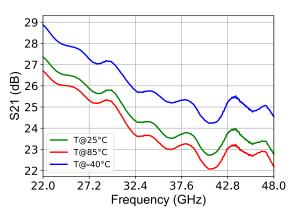


Typical Performance Curves In board with De-Embedding at Different Temperatures: Power measurements in CW at PCB level with De-Embedding

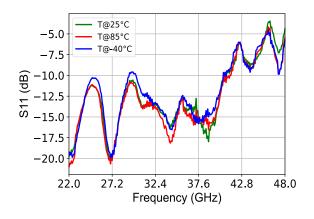
Noise Figure over Frequency



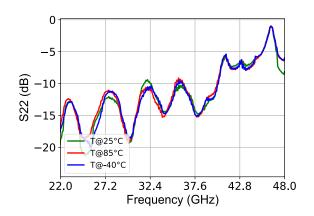
Gain over Frequency



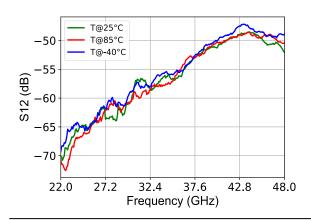
Input Return Loss over Frequency



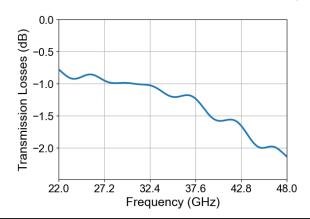
Output Return Loss over Frequency



Reverse Isolation over Frequency



RF access line & connector Losses over Frequency



4

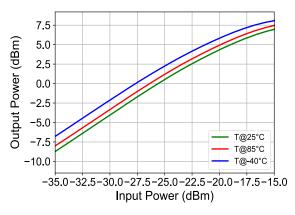
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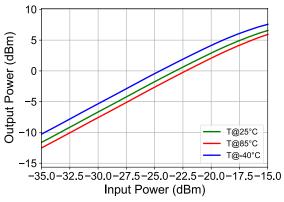


Typical Performance Curves In board with De-Embedding at Different Temperatures: Power measurements in CW at PCB level with De-Embedding

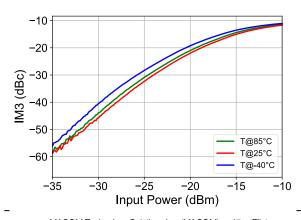
Output Power over Input Power @ 25 GHz



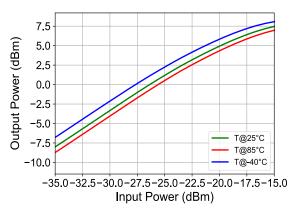
Output Power over Input Power @ 45 GHz



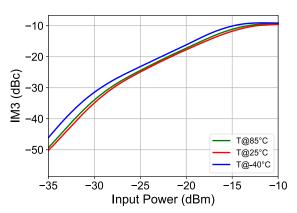
Third-order Intermodulation over Input Power @ 35 GHz



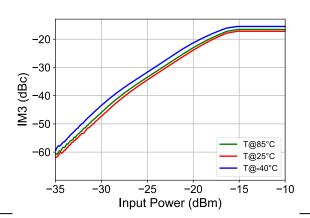
Output Power over Input Power @ 35 GHz



Third-order Intermodulation over Input Power @ 25 GHz



Third-order Intermodulation over Input Power @ 45 GHz



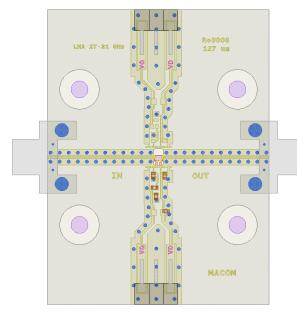
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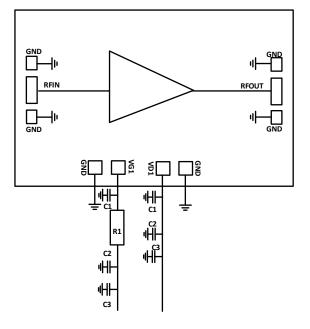
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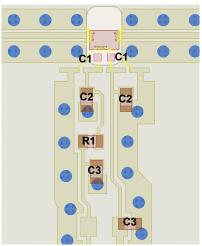
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PCB Layout





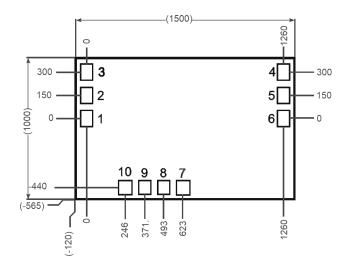


Parts List

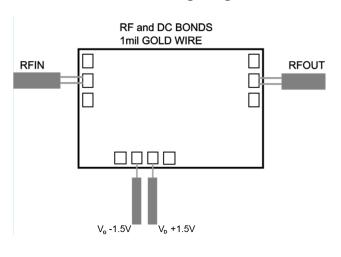
Part	Value	Case Style	Manufacturer	type	Manufacturer's Part number
C1	47 pF	0.015 mils	PRESIDIO	Single layer capacitor	LSA1515B470K1U5C
C2	10 nF	0402 INCH	Murata	SMD Multi Layer Capacitor	GRM155R61E103KA01D
C3	100 nF	0402 INCH	Murata	SMD Multi Layer Capacitor	GRM155R61E225ME11D
R1	0 Ω	0402 INCH	Panasonic	SMD Resistor	ERJ-S020R00X



Die outline



Recommended bonding diagram



Pads Dimensions (µm)

Pad #	x	Y
1,2,3, 4,5,6	80	100
7,10	84	90
8,9	80	90

Revision History

Rev	Date	Change description
V1	4/5/24	Production Release

Low Noise Amplifier 22 - 48 GHz



MAAL-011246-DIE Rev. V1

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