

MAAL-011227 Rev. V1

Features

Non-Magnetic Laminate Module

• Noise Figure: 0.5 dB

Gain: 28 dB

Input Impedance (Real): 3 Ω
 Output Impedance: 50 Ω
 Single Voltage Bias: 3 V
 Integrated Active Bias Circuit

RoHS* Compliant

Applications

MRI Applications

Description

The MAAL-011227 is a high dynamic range, single stage MMIC LNA. The module includes external matching networks to provide excellent low noise performance and high gain characteristics suitable for 1.5T MRI applications.

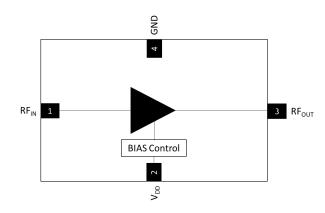
This low noise amplifier has an integrated active bias circuit allowing direct connection to a 3 V bias and minimizing variations over temperature and process.

Ordering Information¹

Part Number	Package
MAAL-011227-TR0100	100 piece reel
MAAL-011227-TR0500	500 piece reel
MAAL-011227-SMB	Sample Board

1. Reference Application Note M513 for reel size information.

Functional Block Diagram



Pin Configuration^{2,3}

Pin#	Pin Name	Description
1	RF _{IN}	RF Input, DC Blocked
2	V_{DD}	Bias Voltage
3	RF _{OUT}	RF Output, DC Blocked
4	GND	Ground

- MACOM recommends connecting unused package pins to ground
- The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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Electrical Specifications: Freq. = 63.87 MHz, $T_A = 25^{\circ}$ C, $V_{DD} = +3$ V, $Z_0 = 50$ Ω

1.5T (Fo = 63.87 MHz)					
Parameter	Test Conditions	Units	Min.	Тур.	Max.
Bandwidth	Centered @ 63.87 MHz	MHz	_	1	_
Noise Figure	_	dB	_	0.5	_
Gain	_	dB	24.0	28.0	_
Input Reflection Coefficient	_	_	_	0.900	_
Real Input Impedance	_	Ω	_	3.0	_
Imaginary Input Impedance ⁴	_	Ω	-2.0	0.2	2.0
Output Return Loss	_	dB	_	28	_
Reverse Isolation	_	dB	_	67	_
Output IP3	P _{IN} = -32 dBm per tone, 100 kHz spacing	dBm	_	23	_
Output P1dB	_	dBm	_	6.8	_
Total Current	I _{DD}	mA	_	29	42

^{4.} With test fixture at 17.6 mm "Delay Dist." of RF input port and 1 Velocity Factor Port Extensions added.

Maximum Operating Limits^{5,6}

Parameter	Maximum
RF Input Power CW	-14 dBm
V _{DD}	5.5 V
Operating Temperature	-10°C to +60°C
Junction Temperature	+150°C

- 5. Operating at nominal conditions with $T_J \le 150$ °C will ensure MTTF > 1 x 10^6 hours.
- 6. Junction Temperature (T_J) = T_C + Θ_{JC} * ((V * I) (P_{OUT} P_{IN})) Typical thermal resistance (Θ_{JC}) = 83°C/W
 - a) For $T_C = +25^{\circ}C$,

 $T_J = 32$ °C @ 3V, 29 mA, $P_{OUT} = 3$ dBm

b) For $T_C = +60$ °C,

 T_J = 67°C @ 3V, 29 mA, P_{OUT} = 3 dBm

Absolute Maximum Ratings^{7,8}

Parameter	Absolute Maximum
RF Input Power CW	-15 dBm
V_{DD}	6.0 V
Storage Temperature	-55°C to +150°C

- 7. Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.

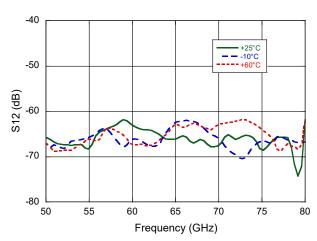


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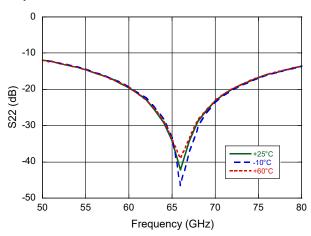
Typical Performance Curves

Gain 30 25 20 10 10 50 55 60 65 70 75 80 Frequency (GHz)

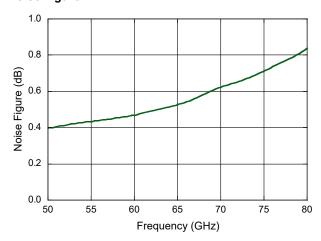
Reverse Isolation



Output Return Loss

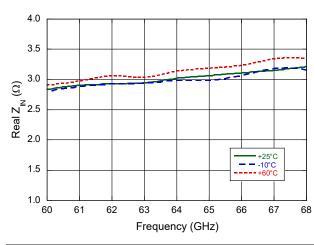


Noise Figure

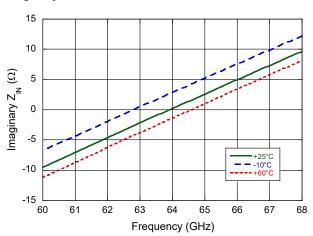


Real Zin

3



Imaginary Zin



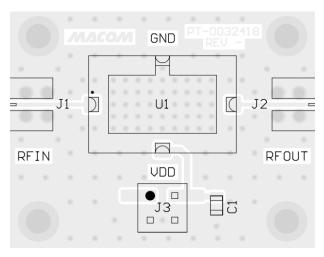
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PCB Layout



Parts List

Part	Value	Case Style
C1	1 µF	0805

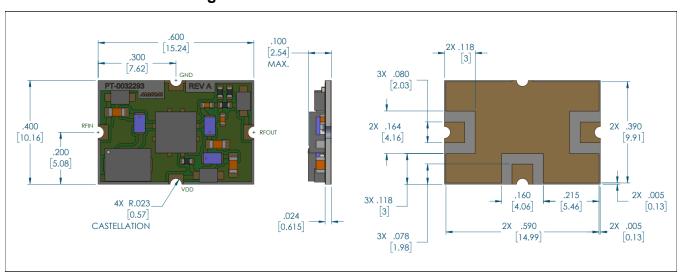
Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 2 HBM and Class C2A CDM devices.

Lead-Free Laminate Package





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