Ultra Low Phase Noise Amplifier
2 - 18 GHz

Features
- Wideband Performance
- Noise Figure: 4 dB @ 8 GHz
- Phase Noise: -154 dBc/Hz @ 1 kHz
- Bias Voltage: 5 V
- Bias Current: 60 mA
- 50 Ω Matched Input / Output
- Positive Voltage Only
- Die Size: 2.8 x 1.73 x 0.1 mm
- RoHS* Compliant

Description
The MAAL-011151-DIE is an easy to use, wideband low noise distributed amplifier die. It operates from 2 to 18 GHz and provides 17 dB of linear gain, 16 dBm of P1dB and 4 dB of noise figure at 8 GHz. The input and output are fully matched to 50 Ω with typical return loss >15 dB.

The RF input and RF output ports are DC blocked. Amplifier control is available through the use of a control circuit or by direct bias injection.

This product is fabricated using a low phase noise HBT process which features full passivation for enhanced reliability.

The MAAL-011151-DIE can be used as a low noise amplifier stage for signal generation applications. This device is ideally suited for Test and Measurement, EW, ECM, and Radar applications where ultra low phase noise and drive power is required.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAAL-011151-DIE</td>
<td>gel pack</td>
</tr>
</tbody>
</table>

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.
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2 - 18 GHz

Electrical Specifications: \( T_A = +25^\circ C, \ VC = VCT^3 = 5 \ V, \ Z_0 = 50 \ \Omega \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
</table>
| Gain               | \( P_{IN} = -15 \ dBm \)  
2 GHz  
10 GHz  
18 GHz | dB | 15.0 | 14.0 | 13.5 | 16.5 | 16.0 | 15.5 | — |
| Output P3dB⁴       | 2 GHz  
10 GHz  
18 GHz | dBm | — | 23 | 19 | 14 | — |
| Output Power       | \( P_{IN} = +4.5 \ dBm, \ 2 \ GHz \)  
\( P_{IN} = +2.8 \ dBm, \ 10 \ GHz \)  
\( P_{IN} = -3.0 \ dBm, \ 18 \ GHz \) | dBm | 18.0 | 15.0 | 9.0 | 20.0 | 17.5 | 11.5 | — |
| Input Return Loss  | \( P_{IN} = -15 \ dBm \) | dB | — | 10 | — |
| Output Return Loss | \( P_{IN} = -15 \ dBm \) | dB | — | 10 | — |
| Noise Figure       | 2 GHz  
10 GHz  
18 GHz | dB | — | 8 | 5 | 8 | — |
| Isolation          | \( P_{IN} = -15 \ dBm \)  
2 GHz  
10 GHz  
18 GHz | dB | — | 50 | 42 | 30 | — |
| Phase Noise        | \( P_{IN} = +3 \ dBm, \ 12 \ GHz \)  
100 Hz  
1 kHz  
10 kHz  
1 MHz | dBC/Hz | — | -144 | -150 | -156 | -162 | — |
| ICQ                | -15 dBm \( P_{IN}, \ VC = 5 \ V \) | mA | — | 60 | — |
| ICT³               | Total current into R1, R2 | mA | — | 2 | — |

3. Reference detailed bias conditions on pages 3-4.
4. MACOM does not recommend sustained operation at power levels above 3 dB compression.
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Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A devices.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power</td>
<td>( P_{\text{IN}} \leq 3 \text{ dB compression level} )</td>
</tr>
<tr>
<td>ICQ</td>
<td>90 mA</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+130°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
</tbody>
</table>

5. Operating at nominal conditions with junction temperature \( \leq 130°C \) will ensure MTTF > \( 1 \times 10^6 \) hours.
6. Junction Temperature \( (T_J) = T_C + \Theta_{JC} \cdot (V \cdot I - (P_{\text{OUT}} - P_{\text{IN}})) \).
   Typical thermal resistance \( (\Theta_{JC}) = 120°C/W \).
   a) For \( T_C = +25°C \)
      \( T_J = +72°C @ 5 \text{ V}, 98 \text{ mA}, P_{\text{OUT}} = 20 \text{ dBm}, P_{\text{IN}} = 4.5 \text{ dBm} \)
   b) For \( T_C = +85°C \)
      \( T_J = 129°C @ 5 \text{ V}, 88 \text{ mA}, P_{\text{OUT}} = 19 \text{ dBm}, P_{\text{IN}} = 4.5 \text{ dBm} \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power</td>
<td>12 dBm</td>
</tr>
<tr>
<td>ICQ</td>
<td>120 mA</td>
</tr>
<tr>
<td>VCC</td>
<td>6 V</td>
</tr>
<tr>
<td>VB1, VB2, VB3, VB4, VB5</td>
<td>6 V</td>
</tr>
<tr>
<td>VB1, VB2, VB3, VB4, VB5 Current</td>
<td>5 mA</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+150°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +125°C</td>
</tr>
</tbody>
</table>

7. Exceeding any one or combination of these limits may cause permanent damage to this device.
8. MACOM does not recommend sustained operation near these survivability limits.
9. Junction temperature directly effects device MTTF. Junction temperature should be kept as low as possible to maximize lifetime.
Operating Conditions
Recommended biasing conditions are VC = 5 V applied to the VB4 and VCC pads. Apply 5 V to the amplifier control VCT node through the offset resistors to VB2 and VB3 pads according to the application schematic as shown. Applying VCT = 5 V will turn the LNA on, which should draw 60 mA from VC. Applying VCT = 0 V will turn off the LNA. The VCT will draw <2 mA at 5 V. All DC supplies need to be low noise to prevent degradation of the amplifier phase noise.
Recommended Bonding Diagram & PCB Layout
RF input and output port matching circuit patterns are designed to compensate for bonding wires. Input and output bonding configuration are identical.

Parts List

<table>
<thead>
<tr>
<th>Part #</th>
<th>Value</th>
<th>Case Style</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1 - C4</td>
<td>330 pF</td>
<td>Single Layer</td>
</tr>
<tr>
<td>C5, C6</td>
<td>1 µF</td>
<td>0402</td>
</tr>
<tr>
<td>R1</td>
<td>1.56 kΩ</td>
<td>Thin film</td>
</tr>
<tr>
<td>R2</td>
<td>3.2 kΩ</td>
<td>Thin film</td>
</tr>
</tbody>
</table>
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Typical Performance Curves: 5 V, ICQ = 60 mA

Gain

Noise Figure

Input Return loss

Output Return Loss

Phase Noise @ +25°C

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MAAL-011151-DIE

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Typical Performance Curves: P1dB @ ICQ = 60 mA

Typical Performance Curves: P3dB @ ICQ = 60 mA

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DC-0017291
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**MMIC Die Outline**

![Diagram of MMIC Die Outline]

**Bond Pad Detail**

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Size (x)</th>
<th>Size (y)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 - 2</td>
<td>100</td>
<td>200</td>
</tr>
<tr>
<td>3</td>
<td>300</td>
<td>100</td>
</tr>
<tr>
<td>4 - 8</td>
<td>100</td>
<td>100</td>
</tr>
</tbody>
</table>

10. All dimensions shown as microns (µm) with a tolerance of +/-5 µm, unless otherwise noted.
11. Die thickness is 100 µm +/-10 µm.

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