MAAL-011130

Broadband Low Noise Amplifier
2 - 18 GHz

Features
- Gain: 21 dB @ 10 GHz
- Noise Figure: 1.4 dB @ 10 GHz
- Output P1dB: 14 dBm
- Single Voltage Bias: 3.3 V to 5 V
- Power Down Capability
- Lead-Free 2 mm 8-lead PDFN Package
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant

Description
The MAAL-011130 is an easy-to-use, broadband, low noise amplifier with 19 dB typical gain from 2 to 18 GHz. The input and output are fully matched to 50 Ω with typical return loss >10 dB. Third order linearity (OIP3) is typically 23 dBm and reverse isolation is >35 dB.

Single voltage (VDD from +3.3 V to +5 V) operation is achieved using an external resistor, RB, between pin 4 and VDD. The value of RB will set the drain current. Alternatively, the application of a bias voltage (VB) to pin 4 allows for the adjustment of drain current from 5 mA to 80 mA and provides power down capability, achieved by applying VB <0.2 V. See biasing information on pages 3 and 4.

The MAAL-011130 is housed in a lead-free 2 mm 8-lead PDFN package compatible with standard pick and place assembly equipment.

The MAAL-011130 is well suited to multiple applications such as X-Band satellite communication receivers and wideband A&D systems.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAAL-011130</td>
<td>Bulk</td>
</tr>
<tr>
<td>MAAL-011130-TR3000</td>
<td>3000 piece reel</td>
</tr>
<tr>
<td>MAAL-011130-SMB</td>
<td>Sample Board</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 3 loose parts.


For further information and support please visit: https://www.macom.com/support

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## Broadband Low Noise Amplifier

### Electrical Specifications: $T_A = +25^\circ C$, $V_{DD} = 5$ V, $V_B = 0.9$ V$^2$, $Z_0 = 50$ Ω

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gain</td>
<td>2 GHz 6 GHz 10 GHz 14 GHz 18 GHz</td>
<td>dB</td>
<td>19</td>
<td>27</td>
<td>23</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>19</td>
<td>21</td>
<td>14</td>
</tr>
<tr>
<td>Output P1dB</td>
<td>2 GHz 6 GHz 10 GHz 14 GHz 18 GHz</td>
<td>dBm</td>
<td>—</td>
<td>12</td>
<td>13</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>14</td>
<td>15</td>
<td>15</td>
</tr>
<tr>
<td>Noise Figure</td>
<td>2 GHz 6 GHz 10 GHz 14 GHz 18 GHz</td>
<td>dB</td>
<td>—</td>
<td>2.1</td>
<td>1.2</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>1.4</td>
<td>1.8</td>
<td>2.9</td>
</tr>
<tr>
<td>Output IP3</td>
<td>$P_{IN} = -22$ dBm/tone (10 MHz Tone Spacing) 2 GHz 6 GHz 10 GHz 14 GHz 18 GHz</td>
<td>dBm</td>
<td>—</td>
<td>18</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>24</td>
<td>25</td>
<td>24</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>$P_{IN} = -20$ dBm</td>
<td>dB</td>
<td>—</td>
<td>10</td>
<td>—</td>
</tr>
<tr>
<td>Output Return Loss</td>
<td>$P_{IN} = -20$ dBm</td>
<td>dB</td>
<td>—</td>
<td>10</td>
<td>—</td>
</tr>
<tr>
<td>Isolation</td>
<td>$P_{IN} = -20$ dBm</td>
<td>dB</td>
<td>—</td>
<td>35</td>
<td>—</td>
</tr>
<tr>
<td>Bias Current</td>
<td>—</td>
<td>mA</td>
<td>—</td>
<td>76</td>
<td>90</td>
</tr>
</tbody>
</table>

5. For single voltage operation, refer to typical $R_E$ values and biasing information on pages 3 and 4.
Application Information

The MAAL-011130 is designed for simple implementation with high performance. The ultra small size, fully matched, and simple bias application allows easy placement on system boards. It has a shunt inductor connected to ground on the input for ESD protection. For this reason, an input DC blocking capacitor is required if DC voltage is present on the input.

Bias Adjust Using \( V_B \)

Pin 4 can be connected to a separate voltage source to achieve the desired \( I_{DD} \). The amplifier will be powered down by applying a \( V_B \) of 0.2 V or less.

The following tables show typical total drain current (\( I_{D\_TOTAL} = I_{D\_BIAS} + I_{DD} \), where \( I_{D\_BIAS} \) is the current drawn by the \( V_B \) pin and \( I_{DD} \) is the drain current) versus bias voltage (\( V_B \)) values for \( V_{DD} \) voltages of 5.0 V and 3.3 V. Also shown in each case is a typical value of \( R_B \) required to set \( I_{DD} \) if using a single supply (see Single Bias Operation information on page 4).

**Handling Procedures**

Please observe the following precautions to avoid damage:

**Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1A devices.

### Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power</td>
<td>10 dBm</td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>7 V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40 °C to +85 °C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65 °C to +150 °C</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+150 °C</td>
</tr>
</tbody>
</table>

6. Exceeding any one or combination of these limits may cause permanent damage to this device.
7. MACOM does not recommend sustained operation near these survivability limits.
8. Operating at nominal conditions with \( T_J \leq +150°C \) will ensure MTTF > \( 1 \times 10^6 \) hours.
9. Junction Temperature (\( T_J \)) = \( T_C + \Theta_{jc} \times (V \times I) \).
   a) \( T_C = +25°C \), \( T_J = 76°C @ 5 \text{ V}, 80 \text{ mA} \)
   b) \( T_C = +85°C \), \( T_J = 136°C @ 5 \text{ V}, 80 \text{ mA} \)

### VDD = 3.3 V

<table>
<thead>
<tr>
<th>( V_B ) (V)</th>
<th>( I_{D_TOTAL} ) (mA)</th>
<th>( I_{D_BIAS} ) (mA)</th>
<th>( I_{DD} ) (mA)</th>
<th>( R_B ) (kΩ)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.6</td>
<td>32.9</td>
<td>1.2</td>
<td>31.7</td>
<td>2.2</td>
</tr>
<tr>
<td>0.7</td>
<td>44.9</td>
<td>2.3</td>
<td>42.6</td>
<td>1.1</td>
</tr>
<tr>
<td>0.8</td>
<td>56.7</td>
<td>3.5</td>
<td>53.3</td>
<td>0.7</td>
</tr>
<tr>
<td>0.9</td>
<td>67.4</td>
<td>4.6</td>
<td>62.8</td>
<td>0.5</td>
</tr>
<tr>
<td>1.0</td>
<td>76.4</td>
<td>5.8</td>
<td>70.5</td>
<td>0.4</td>
</tr>
</tbody>
</table>

### VDD = 5 V

<table>
<thead>
<tr>
<th>( V_B ) (V)</th>
<th>( I_{D_TOTAL} ) (mA)</th>
<th>( I_{D_BIAS} ) (mA)</th>
<th>( I_{DD} ) (mA)</th>
<th>( R_B ) (kΩ)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.6</td>
<td>40</td>
<td>1.1</td>
<td>39</td>
<td>4.0</td>
</tr>
<tr>
<td>0.7</td>
<td>53</td>
<td>2.3</td>
<td>50</td>
<td>1.9</td>
</tr>
<tr>
<td>0.8</td>
<td>65</td>
<td>3.4</td>
<td>62</td>
<td>1.3</td>
</tr>
<tr>
<td>0.9</td>
<td>77</td>
<td>4.5</td>
<td>73</td>
<td>0.9</td>
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<tr>
<td>1.0</td>
<td>89</td>
<td>5.8</td>
<td>83</td>
<td>0.7</td>
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</tbody>
</table>

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Sample PCB

Application Schematic

Single Bias Operation
Connecting $V_{DD}$ to pin 4 using an external resistor $R_B$ enables single bias operation of the amplifier, where the value of external resistor $R_B$ can be used to set the desired $I_{DD}$.

In this configuration, power down mode cannot be used unless a switch is included to connect $V_B$ to ground.

Grounding
It is recommended that the total ground (common mode) inductance not exceed 0.03 nH (30 pH). This is equivalent to placing at least four 8-mil (200-μm) diameter vias under the device, assuming an 8-mil (200-μm) thick RF layer to ground.

Parts List

<table>
<thead>
<tr>
<th>Des</th>
<th>Value</th>
<th>Size</th>
<th>Part Number</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1</td>
<td>0.01 μF</td>
<td>0201</td>
<td>Murata GRM033R70J103KA01D</td>
<td>Bypass</td>
</tr>
<tr>
<td>U1</td>
<td>—</td>
<td>2 mm</td>
<td>MACOM MAAL-011130</td>
<td>LNA</td>
</tr>
</tbody>
</table>
Typical Performance Curves $V_{DD} = 5\, \text{V}$, $V_B = 0.9\, \text{V}$

**Gain**

- S21 (dB) vs. Frequency (GHz)
- $+25\, ^\circ\text{C}$
- $-40\, ^\circ\text{C}$
- $+85\, ^\circ\text{C}$

**Isolation**

- S12 (dB) vs. Frequency (GHz)
- $+25\, ^\circ\text{C}$
- $-40\, ^\circ\text{C}$
- $+85\, ^\circ\text{C}$

**Input Return Loss**

- S11 (dB) vs. Frequency (GHz)
- $+25\, ^\circ\text{C}$
- $-40\, ^\circ\text{C}$
- $+85\, ^\circ\text{C}$

**Output Return Loss**

- S22 (dB) vs. Frequency (GHz)
- $+25\, ^\circ\text{C}$
- $-40\, ^\circ\text{C}$
- $+85\, ^\circ\text{C}$

**Noise Figure**

- Noise Figure (dB) vs. Frequency (GHz)
- $+25\, ^\circ\text{C}$
- $-40\, ^\circ\text{C}$
- $+85\, ^\circ\text{C}$
Typical Performance Curves $T_A = 25^\circ C$, $V_{DD} = 3.3\, V$ & $5\, V$

**Gain**

![Gain Graph](image)

**Isolation**

![Isolation Graph](image)

**Input Return Loss**

![Input Return Loss Graph](image)

**Output Return Loss**

![Output Return Loss Graph](image)

**Noise Figure**

![Noise Figure Graph](image)
Typical Performance Curves $T_A = 25^\circ\text{C}$, $V_{DD} = 5\text{ V} \& 3.3\text{ V}$

**Gain vs. $V_B$ for $V_{DD} = 5\text{ V}$**

![Graph showing gain vs. frequency for $V_{DD} = 5\text{ V}$ with different $V_B$ values.]

**Gain vs. $V_B$ for $V_{DD} = 3.3\text{ V}$**

![Graph showing gain vs. frequency for $V_{DD} = 3.3\text{ V}$ with different $V_B$ values.]

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Typical Output IP3 Curves $V_{DD} = 5\, V$ and $3.3\, V$

**Output IP3 @ $T_A = 25^\circ C$, $V_{DD} = 5\, V$**

**Output IP3 @ $T_A = -40^\circ C$, $V_{DD} = 5\, V$**

**Output IP3 @ $T_A = 85^\circ C$, $V_{DD} = 5\, V$**

**Output IP3 @ $T_A = 25^\circ C$, $V_{DD} = 3.3\, V$**

**Output IP3 @ $T_A = -40^\circ C$, $V_{DD} = 3.3\, V$**

**Output IP3 @ $T_A = 85^\circ C$, $V_{DD} = 3.3\, V$**
Typical P1dB and P3dB Curves $V_{DD} = 5$ V

$P1\text{dB} @ V_{DD} = 5$ V, $V_B = 0.9$ V

$P3\text{dB} @ V_{DD} = 5$, $V_B = 0.9$ V

$P1\text{dB} @ T_A = 25^\circ$C, $V_{DD} = 5$ V

$P3\text{dB} @ T_A = 25^\circ$C, $V_{DD} = 5$ V
Typical P1dB and P3dB Curves $V_{DD} = 3.3$ V

**P1dB @ $V_{DD} = 3.3$ V, $V_B = 0.8$ V**

![Typical P1dB Curve at $V_{DD} = 3.3$ V, $V_B = 0.8$ V](image)

**P3dB @ $V_{DD} = 3.3$ V, $V_B = 0.8$ V**

![Typical P3dB Curve at $V_{DD} = 3.3$ V, $V_B = 0.8$ V](image)

**P1dB @ $T_A = 25^\circ$C, $V_{DD} = 3.3$ V**

![Typical P1dB Curve at $T_A = 25^\circ$C, $V_{DD} = 3.3$ V](image)

**P3dB @ $T_A = 25^\circ$C, $V_{DD} = 3.3$ V**

![Typical P3dB Curve at $T_A = 25^\circ$C, $V_{DD} = 3.3$ V](image)
Lead-Free 2 mm 8-Lead PDFN†

†Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Plating is 100% matte tin over copper.
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