MAAL-011129

Low Noise Amplifier
18 - 31.5 GHz

Features
- Noise Figure: 2.5 dB @ 24 GHz
- High Gain: 23 dB @ 24 GHz
- 50 Ω match on input and output
- Single Voltage Bias: 3 V to 5 V range
- Integrated Active Bias Circuit
- Current adjustable from 1 mA - 80 mA
- Lead-Free 2 mm 8-lead PDFN Package
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant

Description

The MAAL-011129 is an easy-to-use three stage low noise amplifier with high gain and broadband 50 Ω match. It is designed for operation from 18 to 31.5 GHz and housed in a lead-free 2 mm 8-lead PDFN plastic package.

The MAAL-011129 has an integrated active bias circuit and bias tee to allow direct connection to V<sub>DD</sub> without external chokes or DC blocks. The bias current is set by a simple external resistor, R<sub>B</sub>, so the user can customize the power consumption. When V<sub>BIAS</sub> = 0 V, the device is placed in power down mode.

The MAAL-011129 offers a surface-mount, easy-to-use, low noise amplifier solution that is well suited to diverse receiver applications such as VSAT, Point-to-Point and 24 GHz ISM.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAAL-011129-TR3000</td>
<td>3000 piece reel</td>
</tr>
<tr>
<td>MAAL-011129-SMB</td>
<td>Sample Board</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

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Electrical Specifications: Freq. = 24 GHz, T\textsubscript{A} = 25°C, V\textsubscript{DD} = 5 V, R\textsubscript{B} = 1 kΩ, Z\textsubscript{0} = 50 Ω

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Noise Figure</td>
<td></td>
<td>dB</td>
<td>2.5</td>
<td>3.3</td>
<td></td>
</tr>
<tr>
<td>Gain</td>
<td>P\textsubscript{IN} = -20 dBm</td>
<td>dB</td>
<td>20</td>
<td>23</td>
<td></td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>P\textsubscript{IN} = -20 dBm</td>
<td>dB</td>
<td></td>
<td>-13</td>
<td></td>
</tr>
<tr>
<td>Output Return Loss</td>
<td>P\textsubscript{IN} = -20 dBm</td>
<td>dB</td>
<td></td>
<td>-13</td>
<td></td>
</tr>
<tr>
<td>Output IP3</td>
<td>P\textsubscript{IN} = -22 dBm/tone</td>
<td>dBm</td>
<td></td>
<td>25</td>
<td></td>
</tr>
<tr>
<td>Output P1dB</td>
<td></td>
<td>dBm</td>
<td></td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>Isolation</td>
<td>P\textsubscript{IN} = -20 dBm</td>
<td>dB</td>
<td></td>
<td>45</td>
<td></td>
</tr>
<tr>
<td>Bias Current</td>
<td></td>
<td>mA</td>
<td>50</td>
<td>65</td>
<td></td>
</tr>
</tbody>
</table>

Absolute Maximum Ratings\textsuperscript{4,5}

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power</td>
<td>10 dBm</td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>6 V</td>
</tr>
<tr>
<td>Junction Temperature\textsuperscript{6,7}</td>
<td>+150°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
</tbody>
</table>

Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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\textsuperscript{4} Exceeding any one or combination of these limits may cause permanent damage to this device.
\textsuperscript{5} MACOM does not recommend sustained operation near these survivability limits.
\textsuperscript{6} Operating at nominal conditions with T\textsubscript{J} ≤ +150°C will ensure MTTF > 1 x 10\textsuperscript{6} hours.
\textsuperscript{7} Junction Temperature (T\textsubscript{J}) = T\textsubscript{C} + θ\textsubscript{jc} * (V * I)

Typical thermal resistance (θ\textsubscript{jc}) = 102°C/W.

a) T\textsubscript{C} = +25°C,
   T\textsubscript{J} = 51°C @ 5 V, 50 mA
b) T\textsubscript{C} = +85°C,
   T\textsubscript{J} = 111°C @ 5 V, 50 mA
Sample PCB

![Sample PCB Diagram]

Sample PCB Layout

![Sample PCB Layout Diagram]

Application Information

The MAAL-011129 is designed to be easy to use yet provide high performance. The ultra small size, with no matching, and simple bias application allows easy placement on system boards.

Single Bias Operation

Connecting $V_{DD}$ to $V_{BIAS}$ using an external resistor $R_B$ enables single bias operation of the amplifier, and the value of external resistor $R_B$ sets the desired current $I_{DD}$. The following table shows drain current ($I_{DD}$) versus external resistor ($R_B$) values for $V_{DD}$ voltages of 5 V and 3.3 V:

<table>
<thead>
<tr>
<th>$V_{DD}$</th>
<th>$R_B$ (Ω)</th>
<th>$I_{DD}$ (mA)</th>
<th>$R_B$ (Ω)</th>
<th>$I_{DD}$ (mA)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.3 V</td>
<td>Open</td>
<td>15</td>
<td>Open</td>
<td>25</td>
</tr>
<tr>
<td></td>
<td>200</td>
<td>50</td>
<td>200</td>
<td>80</td>
</tr>
<tr>
<td></td>
<td>400</td>
<td>40</td>
<td>400</td>
<td>70</td>
</tr>
<tr>
<td></td>
<td>1k</td>
<td>30</td>
<td>1k</td>
<td>50</td>
</tr>
<tr>
<td></td>
<td>2k</td>
<td>25</td>
<td>2k</td>
<td>40</td>
</tr>
</tbody>
</table>

With pin 4 ($V_{BIAS}$) left open the amplifier will default to low power mode. When pin 4 ($V_{BIAS}$) is set to 0 V through RB, the device enters power down mode. In order to use power down mode a second supply is required that directly drives the RB resistor.

Grounding

It is recommended that the total ground (common mode) inductance not exceed 0.03 nH (30 pH). This is equivalent to placing at least four 8-mil (200-μm) diameter vias under the device, assuming an 8-mil (200-μm) thick RF layer to ground.
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Typical Performance Curves $V_{DD} = 5\, \text{V}$, $R_B = 1\, \text{k} \Omega$

- **Gain**
  - $S_{21}$ (dB)
  - Frequency (GHz)

- **Noise Figure**
  - Noise Figure (dB)
  - Frequency (GHz)

- **Input Return Loss**
  - $S_{11}$ (dB)
  - Frequency (GHz)

- **Output Return Loss**
  - $S_{22}$ (dB)
  - Frequency (GHz)

- **$P_{1dB}$**
  - $P_{1dB}$ (dBm)
  - Frequency (GHz)

- **OIP3**
  - OIP3 (dBm)
  - Frequency (GHz)

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Typical Performance Curves $V_{DD} = 3.3$ V & 5 V

**Gain**

![Gain Graph](image)

**Noise Figure**

![Noise Figure Graph](image)

**Input Return Loss**

![Input Return Loss Graph](image)

**Output Return Loss**

![Output Return Loss Graph](image)

**$P_{1dB}$**

![$P_{1dB}$ Graph](image)

**OIP3**

![OIP3 Graph](image)

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Typical Performance Curves VDD = 5 V, I_{DD} varied by R_B

Gain

Noise Figure

Input Return Loss

Output Return Loss

P1dB

OIP3

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**Lead Free 2 mm 8 Lead PDFN Package†**

† Reference Application Note S2083 for lead-free solder reflow recommendations.

Meets JEDEC moisture sensitivity level 1 requirements.

Plating is 100% Matte Tin over Copper
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