Features

- Low Noise Figure:
  - 0.35 dB @ 1.9 GHz
  - 0.50 dB @ 2.6 GHz
- High Gain:
  - 22 dB @ 2.6 GHz
  - 15 dB @ 6.0 GHz
- High Linearity: 33 dBm OIP3
- Single Voltage Bias: 3 - 5 V
- Integrated Active Bias Circuit
- Current Adjustable 30 - 80 mA
- Lead-Free 2 mm 8-LD PDFN Package
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MAAL-011078 is a high dynamic range, single stage MMIC LNA with ultra low noise figure, high gain and excellent linearity. This amplifier is designed for operation from 700 MHz to 6 GHz and is housed in a lead-free 2 mm 8-lead PDFN plastic package.

This low noise amplifier has an integrated active bias circuit allowing direct connection to 3 V or 5 V bias and minimizing variations over temperature and process. The bias current is set by an external resistor, so the user can customize the power consumption to fit the application. $V_{\text{BIAS}}$ can be utilized as an enable pin to power the device up and down during operation.

In the 50 $\Omega$ environment and at 3 V, the MAAL-011078 offers 0.5 dB noise figure at 2.6 GHz, with 22 dB of gain and over 33 dBm output third order intercept point (OIP3). It is ideal for 5G & 4G cellular infrastructure and Wi-Fi applications.

Functional Block Diagram

Pin Configuration

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Pin Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 3, 4, 6, 8</td>
<td>N/C</td>
<td>No Connection</td>
</tr>
<tr>
<td>2</td>
<td>RF$_{\text{IN}}$</td>
<td>RF Input</td>
</tr>
<tr>
<td>5</td>
<td>$V_{\text{BIAS}}$</td>
<td>Bias Voltage</td>
</tr>
<tr>
<td>7</td>
<td>RF$<em>{\text{OUT}}$ / $V</em>{\text{DD}}$</td>
<td>RF Output / Drain Voltage</td>
</tr>
<tr>
<td>9</td>
<td>Pad$^4$</td>
<td>Ground</td>
</tr>
</tbody>
</table>

3. MACOM recommends connecting unused package pins to ground.
4. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MAAL-011078-TR1000</td>
<td>1000 piece reel</td>
</tr>
<tr>
<td>MAAL-011078-TR3000</td>
<td>3000 piece reel</td>
</tr>
<tr>
<td>MAAL-011078-001SMB</td>
<td>Sample Board 2.3 - 2.7 GHz</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.
2. All sample boards include 3 loose parts.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.
Low Noise Amplifier
700 MHz - 6 GHz

Electrical Specifications: Freq = 1.9 GHz, $V_{\text{DD}} = 3$ V, +25°C, $Z_0 = 50$ Ω, $V_{\text{BIAS}} = 2.3$ V

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Noise Figure</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>0.35</td>
<td>0.7</td>
</tr>
<tr>
<td>Gain</td>
<td>-</td>
<td>dB</td>
<td>21</td>
<td>23</td>
<td>25</td>
</tr>
<tr>
<td>Input Return Loss$^6$</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>5</td>
<td>—</td>
</tr>
<tr>
<td>Output Return Loss$^6$</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>5</td>
<td>—</td>
</tr>
<tr>
<td>Output IP3</td>
<td>$P_{\text{IN}}$ = -22 dBm, tones 11 MHz apart</td>
<td>dBm</td>
<td>—</td>
<td>33</td>
<td>—</td>
</tr>
<tr>
<td>Output P1dB</td>
<td>-</td>
<td>dBm</td>
<td>—</td>
<td>17.5</td>
<td>—</td>
</tr>
<tr>
<td>Total Current</td>
<td>$I_{\text{DQ}} = I_{\text{DD}} + I_{\text{BIAS}}$</td>
<td>mA</td>
<td>39</td>
<td>50</td>
<td>68</td>
</tr>
</tbody>
</table>

5. Refer to biasing options on page 3.
6. Return Loss can be improved with external matching components. Refer to application section.

Absolute Maximum Ratings$^7,8,9$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>RF Input Power CW</td>
<td>19 dBM</td>
</tr>
<tr>
<td>$V_{\text{DD}}$</td>
<td>6 V</td>
</tr>
<tr>
<td>$V_{\text{BIAS}}$</td>
<td>5 V</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-55°C to +150°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Junction Temperature$^{10}$</td>
<td>+150°C</td>
</tr>
</tbody>
</table>

7. Exceeding any one or combination of these limits may cause permanent damage to this device.
8. MACOM does not recommend sustained operation near these survivability limits.
9. Operating at nominal conditions with $T_J \leq 150^\circ C$ will ensure MTTF $\geq 1 \times 10^6$ hours.
10. Junction Temperature ($T_J$) = $T_C + \Theta_{JC} * ((V * I) - (P_{\text{OUT}} - P_{\text{IN}}))$
    Typical thermal resistance ($\Theta_{JC}$) = 83°C/W
    a) For $T_C = +25^\circ C$.
    $T_J = 33^\circ C @ 3$ V, 0.05 A, $P_{\text{OUT}} = 17.5$ dBm, $P_{\text{IN}} = -4.5$ dBm
    b) For $T_C = +85^\circ C$.
    $T_J = 93^\circ C @ 3$ V, 0.05 A, $P_{\text{OUT}} = 17.5$ dBm, $P_{\text{IN}} = -4.5$ dBm

Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.
Biasing Options

The MAAL-011078 bias can be set in 2 different ways: using only $V_{DD}$ or using separate $V_{DD}$ and $V_{BIAS}$ voltages. A separate $V_{BIAS}$ voltage allows pin 5 ($V_{BIAS}$) to be used as an enable pin to power the device up and down during operation.

For both bias methods select the value of $R_{BIAS}$ to achieve the desired current based on the tables on page 4, and use DC blocks at pin 2 ($RF_{IN}$) and pin 7 ($RF_{OUT} / V_{DD}$).

Biasing Option - $V_{DD}$ only

To use only $V_{DD}$, connect pin 7 ($RF_{OUT} / V_{DD}$) to $V_{DD}$ through an RF choke inductor and connect pin 5 ($V_{BIAS}$) to $V_{DD}$ through bias resistor $R_{BIAS}$ as shown in Figure 1.

Biasing Option - Separate $V_{DD}$ and $V_{BIAS}$ Voltages ($V_{BIAS} \leq V_{DD}$)

To use separate $V_{DD}$ and $V_{BIAS}$ voltages, connect pin 7 ($RF_{OUT} / V_{DD}$) to $V_{DD}$ through an RF choke inductor and connect pin 5 ($V_{BIAS}$) to $V_{BIAS}$ through bias resistor $R_{BIAS}$ as shown in Figure 2. Typical current ($I_{BIAS}$) draw for pin 5 ($V_{BIAS}$) is $1.4 \, mA \, @ \, V_{BIAS} = 3 \, V$ and $1 \, \mu A \, @ \, V_{BIAS} = 0 \, V$. Typical current ($I_{DD}$) draw for pin 7 ($RF_{OUT} / V_{DD}$) is $< 1 \, \mu A \, @ \, V_{BIAS} = 0 \, V$.  

For further information and support please visit:  
https://www.macom.com/support
Low Noise Amplifier
700 MHz - 6 GHz

Typical Performance Curves of the Active Bias Circuit

Current, $V_{DD} = 3$ V

Current, $V_{DD} = 4$ V

Current, $V_{DD} = 5$ V

Bias Table

<table>
<thead>
<tr>
<th>Bias Resistance (Ω)</th>
<th>Total Current (mA)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>$V_{DD} = 3$ V</td>
</tr>
<tr>
<td></td>
<td>$+25^\circ C$</td>
</tr>
<tr>
<td>2000</td>
<td>29</td>
</tr>
<tr>
<td>1200</td>
<td>38</td>
</tr>
<tr>
<td>1000</td>
<td>41</td>
</tr>
<tr>
<td>800</td>
<td>45</td>
</tr>
<tr>
<td>600</td>
<td>50</td>
</tr>
<tr>
<td>400</td>
<td>56</td>
</tr>
<tr>
<td>200</td>
<td>64</td>
</tr>
<tr>
<td>0</td>
<td>75</td>
</tr>
</tbody>
</table>
Low Noise Amplifier
700 MHz - 6 GHz

Typical Performance Curves @ 3 V / 50 mA, $Z_0 = 50 \, \Omega$

**Gain**

![Gain Graph](image)

**Noise Figure**

![Noise Figure Graph](image)

**Input Return Loss**

![Input Return Loss Graph](image)

**Output Return Loss**

![Output Return Loss Graph](image)

**OIP3**

![OIP3 Graph](image)

**P1dB**

![P1dB Graph](image)

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Typical Performance Curves @ 5 V / 70 mA, $Z_0 = 50 \, \Omega$

**Gain**

$$S_{21} \text{ (dB)}$$

- Frequency (GHz)
- $+25^\circ C$
- $+40^\circ C$
- $+85^\circ C$

**Noise Figure**

$$\text{Noise Figure (dB)}$$

- Frequency (GHz)
- $+25^\circ C$
- $+40^\circ C$
- $+85^\circ C$

**Input Return Loss**

$$S_{11} \text{ (dB)}$$

- Frequency (GHz)
- $+25^\circ C$
- $+40^\circ C$
- $+85^\circ C$

**Output Return Loss**

$$S_{22} \text{ (dB)}$$

- Frequency (GHz)
- $+25^\circ C$
- $+40^\circ C$
- $+85^\circ C$

**OIP3**

$$\text{OIP3 (dBm)}$$

- Frequency (GHz)
- $+25^\circ C$
- $+40^\circ C$
- $+85^\circ C$

**P1dB**

$$\text{P1dB (dBm)}$$

- Frequency (GHz)
- $+25^\circ C$
- $+40^\circ C$
- $+85^\circ C$
Typical Performance Curves @ 3 V / 30 mA, 3 V / 50 mA, 5V / 70 mA, $Z_0 = 50 \Omega$

- **Gain**
  - $S_{21}$ in dB vs. Frequency (GHz)
  - Lines for different input currents and voltages

- **Noise Figure**
  - Noise Figure (dB) vs. Frequency (GHz)
  - Lines for different input currents and voltages

- **Input Return Loss**
  - $S_{11}$ in dB vs. Frequency (GHz)
  - Lines for different input currents and voltages

- **Output Return Loss**
  - $S_{22}$ in dB vs. Frequency (GHz)
  - Lines for different input currents and voltages

- **OIP3**
  - OIP3 (dBm) vs. Frequency (GHz)
  - Lines for different input currents and voltages

- **P1dB**
  - P1dB (dBm) vs. Frequency (GHz)
  - Lines for different input currents and voltages

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Low Noise Amplifier
700 MHz - 6 GHz

Rev. V4

MAAL-011078

Lead-Free 2 mm 8-Lead PDFN†

† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Plating is 100% matte tin over copper.

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DC-0005748
2.3 - 2.7 GHz Application Section

The MAAL-011078 is designed to work as a low noise gain block over a wide range of frequencies in a 50 Ω environment.

Input and output can be tuned to improve return loss over a specific frequency band.

The evaluation board shown has been designed for tuning flexibility. The parts list on page 10 details the components needed to tune the MAAL-011078 for operation from 2.3 – 2.7 GHz. R1 or R2 may be used as $R_{\text{BIAS}}$ according to the biasing option chosen.

Evaluation Board, 2.3 - 2.7 GHz

Schematic, 2.3 - 2.7 GHz
### 2.3 - 2.7 GHz Application Section

#### Parts List, 2.3 - 2.7 GHz

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
<th>Size</th>
<th>Manufacturer</th>
<th>Manufacturer Part #</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1</td>
<td>—</td>
<td>0201</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>C2</td>
<td>10 pF</td>
<td>0201</td>
<td>Murata</td>
<td>GJM0336C1E100JB01</td>
</tr>
<tr>
<td>C3</td>
<td>0.7 pF</td>
<td>0201</td>
<td>Murata</td>
<td>GJM0335C1ER70WB0</td>
</tr>
<tr>
<td>C4</td>
<td>1.8 pF</td>
<td>0201</td>
<td>Murata</td>
<td>GJM0335C1E1R8BB01</td>
</tr>
<tr>
<td>C5</td>
<td>0.4 pF</td>
<td>0201</td>
<td>Murata</td>
<td>GJM0335C1ER40WB01</td>
</tr>
<tr>
<td>C6</td>
<td>—</td>
<td>0201</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>C7</td>
<td>—</td>
<td>0402</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>C8</td>
<td>0.1 µF</td>
<td>0402</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>C9, C10</td>
<td>49 pF</td>
<td>0201</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>L1</td>
<td>2.5 nH</td>
<td>0201</td>
<td>Coilcraft</td>
<td>0201DS-2N5XJL</td>
</tr>
<tr>
<td>L2</td>
<td>2.7 nH</td>
<td>0201</td>
<td>Murata</td>
<td>LQP03TN2N7C02</td>
</tr>
<tr>
<td>R1</td>
<td>470 Ω</td>
<td>0402</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>R2</td>
<td>—</td>
<td>0402</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>R3</td>
<td>0 Ω</td>
<td>0201</td>
<td>—</td>
<td>—</td>
</tr>
</tbody>
</table>
Low Noise Amplifier
700 MHz - 6 GHz

Electrical Specifications: Freq = 2.6 GHz\textsuperscript{11,12}, V\textsubscript{DD} = 3 V, +25\degree C, Z\textsubscript{0} = 50 \, \Omega

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Noise Figure</td>
<td>-</td>
<td>dB</td>
<td>-</td>
<td>0.7</td>
<td>-</td>
</tr>
<tr>
<td>Gain</td>
<td>-</td>
<td>dB</td>
<td>-</td>
<td>23</td>
<td>-</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>-</td>
<td>dB</td>
<td>-</td>
<td>16</td>
<td>-</td>
</tr>
<tr>
<td>Output Return Loss</td>
<td>-</td>
<td>dB</td>
<td>-</td>
<td>10</td>
<td>-</td>
</tr>
<tr>
<td>Output IP3</td>
<td>P\textsubscript{IN} = -22 dBm, tones 11 MHz apart</td>
<td>dBm</td>
<td>-</td>
<td>33.4</td>
<td>-</td>
</tr>
<tr>
<td>Total Current</td>
<td>I\textsubscript{DQ} = I\textsubscript{DD} + I\textsubscript{BIAS}</td>
<td>mA</td>
<td>-</td>
<td>50</td>
<td>-</td>
</tr>
</tbody>
</table>

\textsuperscript{11} Typical performance of the evaluation module with exact components shown on the 2.3 - 2.7 GHz parts list.
\textsuperscript{12} Typical measured data includes evaluation board and connector losses.

Typical Performance Curves: Broadband performance (2.3 - 2.7 GHz evaluation board)

Gain

\begin{align*}
\text{S}_{21} & (\text{dB}) \\
0 & \text{Frequency (GHz)}
\end{align*}

\begin{align*}
\text{S}_{12} & (\text{dB}) \\
0 & \text{Frequency (GHz)}
\end{align*}

Input Return Loss

\begin{align*}
\text{S}_{11} & (\text{dB}) \\
0 & \text{Frequency (GHz)}
\end{align*}

Output Return Loss

\begin{align*}
\text{S}_{22} & (\text{dB}) \\
0 & \text{Frequency (GHz)}
\end{align*}
Typical Performance Curves: Freq = 2.3 - 2.7 GHz, Z₀ = 50 Ω

**Gain**

- **S21 (dB)**
  - Frequency (GHz): 2.1, 2.3, 2.5, 2.7, 2.9
  - Values: 21.5, 22.0, 22.5, 23.0, 23.5, 24.0, 24.5, 25.0

- **Noise Figure**
  - Frequency (GHz): 2.1, 2.3, 2.5, 2.7, 2.9
  - Values: 0.0, 0.1, 0.2, 0.3, 0.4, 0.5, 0.6, 0.7, 0.8, 0.9

**Input Return Loss**

- **S11 (dB)**
  - Frequency (GHz): 2.1, 2.3, 2.5, 2.7, 2.9
  - Values: -25, -20, -15, -10, -5, 0, 5

**Output Return Loss**

- **S22 (dB)**
  - Frequency (GHz): 2.1, 2.3, 2.5, 2.7, 2.9
  - Values: -39.5, -38.5, -37.5, -36.5, -35.5, -34.5

**Reverse Isolation**

- **S12 (dB)**
  - Frequency (GHz): 2.1, 2.3, 2.5, 2.7, 2.9
  - Values: -39.5, -38.5, -37.5, -36.5, -35.5, -34.5

**OIP3**

- **OIP3 (dBm)**
  - Frequency (GHz): 2.1, 2.3, 2.5, 2.7, 2.9
  - Values: 30, 32, 34, 36, 38, 40

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3.6 - 4.2 GHz Application Section

The MAAL-011078 is designed to work as a low noise gain block over a wide range of frequencies in a 50 Ω environment.

Input and output can be tuned to improve return loss over a specific frequency band.

The evaluation board shown has been designed for tuning flexibility. The parts list on page 14 details the components needed to tune the MAAL-011078 for operation from 3.6 – 4.2 GHz. R1 or R2 may be used as $R_{\text{BIAS}}$ according to the biasing option chosen.

Evaluation Board, 3.6 - 4.2 GHz

Schematic, 3.6 - 4.2 GHz

Parts List

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
<th>Size</th>
<th>Component</th>
<th>Value</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C12</td>
<td>0.1 µF</td>
<td>0402</td>
<td>C7, C10, C11</td>
<td>DNF</td>
<td>0402</td>
</tr>
<tr>
<td>C2, C3</td>
<td>47 pF</td>
<td>0402</td>
<td>L1</td>
<td>2.4 nH</td>
<td>0402</td>
</tr>
<tr>
<td>C4</td>
<td>0.75 pF</td>
<td>0402</td>
<td>L2</td>
<td>1.5 nH</td>
<td>0402</td>
</tr>
<tr>
<td>C5</td>
<td>10 pF</td>
<td>0402</td>
<td>L3</td>
<td>1.0 nH</td>
<td>0402</td>
</tr>
<tr>
<td>C6</td>
<td>0.3 pF</td>
<td>0402</td>
<td>R1</td>
<td>470 Ω</td>
<td>0402</td>
</tr>
</tbody>
</table>
Electrical Specifications: Freq = 3.6 - 4.2 GHz\textsuperscript{13}, $V_{DD} = 3$ V, +25\degree C, $Z_0 = 50$ $\Omega$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Noise Figure</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>0.7</td>
<td>—</td>
</tr>
<tr>
<td>Gain</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>19.5</td>
<td>—</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>17</td>
<td>—</td>
</tr>
<tr>
<td>Output Return Loss</td>
<td>-</td>
<td>dB</td>
<td>—</td>
<td>15</td>
<td>—</td>
</tr>
<tr>
<td>Total Current</td>
<td>$I_{DQ} = I_{DD} + I_{BIAS}$</td>
<td>mA</td>
<td>—</td>
<td>56</td>
<td>—</td>
</tr>
</tbody>
</table>

\textsuperscript{13} Typical performance of the evaluation module with exact components shown on the 3.6 - 4.2 GHz parts list.

Typical Performance Curves: Broadband performance (3.6 - 4.2 GHz evaluation board)
4.4 - 4.9 GHz Application Section

The MAAL-011078 is designed to work as a low noise gain block over a wide range of frequencies in a 50 Ω environment.

Input and output can be tuned to improve return loss over a specific frequency band.

The evaluation board shown has been designed for tuning flexibility. The parts list on page 14 details the components needed to tune the MAAL-011078 for operation from 4.4 – 4.9 GHz. R1 or R2 may be used as $R_{BIAS}$ according to the biasing option chosen.

Evaluation Board, 4.4 - 4.9 GHz

Schematic, 4.4 - 4.9 GHz

Parts List

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
<th>Size</th>
<th>Component</th>
<th>Value</th>
<th>Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C12</td>
<td>0.1 µF</td>
<td>0402</td>
<td>C10</td>
<td>1.5 nH</td>
<td>0402</td>
</tr>
<tr>
<td>C2, C3</td>
<td>47 pF</td>
<td>0402</td>
<td>L1</td>
<td>1.0 nH</td>
<td>0402</td>
</tr>
<tr>
<td>C4</td>
<td>1.2 pF</td>
<td>0402</td>
<td>L2</td>
<td>0.6 nH</td>
<td>0402</td>
</tr>
<tr>
<td>C5</td>
<td>1.5 pF</td>
<td>0402</td>
<td>L3</td>
<td>1.0 pF</td>
<td>0402</td>
</tr>
<tr>
<td>C6, C11</td>
<td>DNF</td>
<td>—</td>
<td>R1</td>
<td>470 Ω</td>
<td>0402</td>
</tr>
<tr>
<td>C7</td>
<td>0.3 pF</td>
<td>0402</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Electrical Specifications: Freq = 4.4 - 4.9 GHz\textsuperscript{14}, V_{DD} = 3 V, +25°C, Z_0 = 50 Ω

\begin{tabular}{|c|c|c|c|c|}
\hline
Parameter & Test Conditions & Units & Min. & Typ. & Max. \\
\hline
Noise Figure & - & dB & — & 0.65 & — \\
Gain & - & dB & — & 18.5 & — \\
Input Return Loss & - & dB & — & 18 & — \\
Output Return Loss & - & dB & — & 15 & — \\
Total Current & I_{DQ} = I_{DD} + I_{BIAS} & mA & — & 56 & — \\
\hline
\end{tabular}

14. Typical performance of the evaluation module with exact components shown on the 4.4 - 4.9 GHz parts list.

Typical Performance Curves: Broadband performance (4.4 - 4.9 GHz evaluation board)

\begin{align*}
\text{Gain} & \quad S_{21} (\text{dB}) \\
\text{Frequency (GHz)} & \quad 4.4 \quad 4.5 \quad 4.6 \quad 4.7 \quad 4.8 \quad 4.9 \\
\text{Noise Figure} & \quad \text{Noise Figure (dB)} \\
\text{Frequency (GHz)} & \quad 4.4 \quad 4.5 \quad 4.6 \quad 4.7 \quad 4.8 \quad 4.9 \\
\text{Input Return Loss} & \quad S_{11} (\text{dB}) \\
\text{Frequency (GHz)} & \quad 4.4 \quad 4.5 \quad 4.6 \quad 4.7 \quad 4.8 \quad 4.9 \\
\text{Output Return Loss} & \quad S_{22} (\text{dB}) \\
\text{Frequency (GHz)} & \quad 4.4 \quad 4.5 \quad 4.6 \quad 4.7 \quad 4.8 \quad 4.9
\end{align*}