MA4SW110 Series

Features
- Broad Bandwidth
- Specified from 50 MHz to 20 GHz
- Usable from 50 MHz to 26.5 GHz
- Lower Insertion Loss / Higher Isolation
- Fully Monolithic, Glass Encapsulated Chip
- Up to +33 dBm CW Power Handling @ +25°C
- RoHS* Compliant

Description
The MA4SW110 (SPST), MA4SW210 (SPDT) and MA4SW310 (SP3T) are series-shunt, broadband, PIN diode switches made with MACOM’s HMIC™ (Heterolithic Microwave Integrated Circuit) process. This process allows the silicon pedestals which form the series-shunt diodes and vias to be embedded into low loss, low dispersion glass. By also incorporating small spacing between circuit elements, the result is an HMIC chip with low insertion loss and high isolation at frequencies up to 26.5 GHz.

They are designed for use as moderate power, high performance switches and provide superior performance when compared to similar designs that use discrete components.

The top side of the chip is protected by a polymer coating for manual or automatic handling and large gold bond pads help facilitate connection of low inductance ribbons. The gold metallization on the backside of the chip allows for attachment via 80/20, gold/tin solder or conductive silver epoxy.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MA4SW110</td>
<td>Gel Pack</td>
</tr>
<tr>
<td>MA4SW210</td>
<td>Gel Pack</td>
</tr>
<tr>
<td>MA4SW310</td>
<td>Gel Pack</td>
</tr>
</tbody>
</table>

## MA4SWx10 Series

### HMIC™ Silicon PIN Diode Switches

Rev. V11

**MA4SW110 (SPST)**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>0.4</td>
<td>0.5</td>
<td>0.7</td>
</tr>
<tr>
<td></td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>46</td>
<td>55</td>
<td>—</td>
</tr>
<tr>
<td>Isolation</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>40</td>
<td>50</td>
<td>—</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>18</td>
<td>25</td>
<td>—</td>
</tr>
<tr>
<td>Switching Speed</td>
<td>ns</td>
<td>ns</td>
<td>50</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Voltage Rating</td>
<td>V</td>
<td>V</td>
<td>50</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Signal Compression</td>
<td>500 mW, 1 GHz</td>
<td>dB</td>
<td>0.2</td>
<td>—</td>
<td>—</td>
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**MA4SW210 (SPDT)**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>0.4</td>
<td>0.5</td>
<td>0.7</td>
</tr>
<tr>
<td></td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>48</td>
<td>63</td>
<td>—</td>
</tr>
<tr>
<td>Isolation</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>40</td>
<td>50</td>
<td>—</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>18</td>
<td>25</td>
<td>—</td>
</tr>
<tr>
<td>Switching Speed</td>
<td>ns</td>
<td>ns</td>
<td>50</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Voltage Rating</td>
<td>V</td>
<td>V</td>
<td>50</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Signal Compression</td>
<td>500 mW, 1 GHz</td>
<td>dB</td>
<td>0.2</td>
<td>—</td>
<td>—</td>
</tr>
</tbody>
</table>

**MA4SW310 (SP3T)**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Insertion Loss</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>0.5</td>
<td>0.7</td>
<td>0.8</td>
</tr>
<tr>
<td></td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>49</td>
<td>57</td>
<td>—</td>
</tr>
<tr>
<td>Isolation</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>42</td>
<td>48</td>
<td>—</td>
</tr>
<tr>
<td>Input Return Loss</td>
<td>6 GHz, 13 GHz, 20 GHz</td>
<td>dB</td>
<td>14</td>
<td>22</td>
<td>—</td>
</tr>
<tr>
<td>Switching Speed</td>
<td>ns</td>
<td>ns</td>
<td>50</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Voltage Rating</td>
<td>V</td>
<td>V</td>
<td>50</td>
<td>—</td>
<td>—</td>
</tr>
<tr>
<td>Signal Compression</td>
<td>500 mW, 1 GHz</td>
<td>dB</td>
<td>0.2</td>
<td>—</td>
<td>—</td>
</tr>
</tbody>
</table>

1. Typical switching speed is measured from (10% to 90% and 90% to 10% of detected RF voltage), driven by TTL compatible drivers. In the modulating state, (the switching port is modulating, all other ports are in steady state isolation.) The switching speed is measured using an RC network using the following values: $R = 50 \cdot 200 \Omega$, $C = 390 \cdot 1000 \text{ pF}$. Driver spike current, $I_C = C \frac{dV}{dt}$, ratio of spike current to steady state current, is typically 10:1.

2. Maximum reverse leakage current in either the shunt or series PIN diodes shall be 10 µA maximum at -50 volts.

For further information and support please visit: [https://www.macom.com/support](https://www.macom.com/support)
Absolute Maximum Ratings\textsuperscript{3,4,5}

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>RF CW Incident Power</td>
<td>+33 dBm</td>
</tr>
<tr>
<td>Reverse Voltage</td>
<td>-50 V</td>
</tr>
<tr>
<td>Bias Current per Port</td>
<td>±50 mA @ +25°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-65°C to +25°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+175°C</td>
</tr>
</tbody>
</table>

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. MACOM does not recommend sustained operation near these survivability limits.
5. Maximum operating conditions for a combination of RF power, DC bias and temperature: +33 dBm CW @ 15 mA (per diode) +85°C.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (HBM) and Class C1 (CDM) devices.

Typical Performance Curves @ 10 GHz:

**Insertion Loss vs. Bias Current**

![Insertion Loss vs. Bias Current](image1)

**Isolation vs. Bias Current**

![Isolation vs. Bias Current](image2)

**Input Return Loss vs. Bias Current**

![Input Return Loss vs. Bias Current](image3)

**Output Return Loss vs. Bias Current**

![Output Return Loss vs. Bias Current](image4)
Typical Performance Curves @ $T_A = +25^\circ C$, 20 mA Bias Current

**MA4SW110**

- **Return Loss vs. Frequency**
  - Input Return Loss
  - Output Return Loss

- **Insertion Loss vs. Frequency**

**MA4SW210**

- **Return Loss vs. Frequency**
  - Input Return Loss
  - Output Return Loss

- **Insertion Loss vs. Frequency**

**MA4SW310**

- **Return Loss vs. Frequency**
  - Input Return Loss
  - Output Return Loss

- **Insertion Loss vs. Frequency**
MA4SWx10 Series

Typical Performance Curves @ T<sub>A</sub> = +25°C, 20 mA Bias Current

MA4SW110

Isolation vs. Frequency

![Isolation vs. Frequency graph for MA4SW110](graph1.png)

MA4SW210

Isolation vs. Frequency

![Isolation vs. Frequency graph for MA4SW210](graph2.png)

MA4SW310

Isolation vs. Frequency

![Isolation vs. Frequency graph for MA4SW310](graph3.png)
Operation of MA4SW Series Switches
The simultaneous application of a negative DC current to the low loss port and positive DC current to the isolated port is required for proper operation of the MA4SW series of PIN switches. The backside area of the die is the RF and DC ground return and the DC return is through the common Port J1. A constant current source should be used to supply the DC control currents. The control voltages at these points will not exceed ±1.5 volts for supply currents up to ±50mA. In the low loss state, the series diode must be forward biased and the shunt diode reverse biased. On all isolated ports, the shunt diode is forward biased and the series diode is reverse biased.

Driver Connections and Drivers

**MA4SW110 (SPST)**

<table>
<thead>
<tr>
<th>DC Control Current (mA)</th>
<th>RF Output State</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2</td>
<td>J1-J2</td>
</tr>
<tr>
<td>-20</td>
<td>low loss</td>
</tr>
<tr>
<td>+20</td>
<td>Isolation</td>
</tr>
</tbody>
</table>

Compatible MACOM Driver MADR-007097-000100

**MA4SW210 (SPDT)**

<table>
<thead>
<tr>
<th>DC Control Current (mA)</th>
<th>RF Output States</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2</td>
<td>J3</td>
</tr>
<tr>
<td>-20</td>
<td>+20</td>
</tr>
<tr>
<td>+20</td>
<td>-20</td>
</tr>
</tbody>
</table>

Compatible MACOM Driver MADR-007097-000100

**MA4SW310 (SP3T)**

<table>
<thead>
<tr>
<th>DC Control Current (mA)</th>
<th>RF Output States</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2</td>
<td>J3</td>
</tr>
<tr>
<td>-20</td>
<td>+20</td>
</tr>
<tr>
<td>+20</td>
<td>-20</td>
</tr>
<tr>
<td>+20</td>
<td>+20</td>
</tr>
</tbody>
</table>

Compatible MACOM Driver MADR-009190-000100

6. RLC values are for an operation frequency of 2-18 GHz and bias current of ±20 mA per port.
Chip Outline Drawings

### MA4SW110

**DIM** | **INCHES** | **MM**
--- | --- | ---
A  | 0.014 | 0.35 | 0.018 | 0.45 |
B  | 0.025 | 0.64 | 0.029 | 0.74 |
C  | 0.008 | 0.20 | Ref. | |
D  | 0.004 | 0.10 | 0.006 | 0.15 |
E  | 0.004 | 0.10 | Ref. | |
F  | 0.003 | 0.08 | Ref. | |
G  | 0.003 | 0.08 | Ref. | |
H  | 0.020 | 0.52 | Ref. | |

**DIM** | **INCHES** | **MM**
--- | --- | ---
A  | 0.029 | 0.73 | 0.033 | 0.83 |
B  | 0.004 | 0.10 | 0.006 | 0.15 |
C  | 0.004 | 0.10 | Ref. | |
D  | 0.005 | 0.13 | Ref. | |
E  | 0.009 | 0.23 | Ref. | |
F  | 0.023 | 0.58 | Ref. | |
G  | 0.007 | 0.17 | Ref. | |
H  | 0.004 | 0.10 | Ref. | |

### MA4SW210

**DIM** | **INCHES** | **MM**
--- | --- | ---
A  | 0.046 | 1.16 | 0.050 | 1.26 |
B  | 0.036 | 0.92 | 0.040 | 1.02 |
C  | 0.019 | 0.48 | Ref. | |
D  | 0.014 | 0.36 | Ref. | |
E  | 0.004 | 0.10 | Ref. | |
F  | 0.005 | 0.13 | Ref. | |
G  | 0.004 | 0.10 | 0.006 | 0.15 |
H  | 0.005 | 0.12 | Ref. | |
J  | 0.004 | 0.10 | Ref. | |

7. Topside and backside metallization is gold, 2.5 µm thick typical.
8. Yellow areas indicate ribbon/wire bonding pads.

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Cleanliness
The chips should be handled in a clean environment free of dust and organic contamination.

Wire / Ribbon Bonding
Thermo compression wedge bonding using 0.003" x 0.00025" ribbon or 0.001" diameter gold wire is recommended. A work stage temperature of 150°C - 200°C, tool tip temperature of 120°C - 150°C and a downward force of 18 to 22 grams should be used. If ultrasonic energy is necessary, it should be adjusted to the minimum level required to achieve a good bond. Excessive power or force will fracture the silicon beneath the bond pad causing it to lift. RF bond wires and ribbons should be kept as short as possible for optimum RF performance.

Chip Mounting
HMIC switches have Ti-Pt-Au backside metallization and can be mounted using a gold-tin eutectic solder or conductive epoxy. Mounting surface must be free of contamination and flat.

Eutectic Die Attachment
80/20, gold-tin, solder is recommended. A re-flow oven or hot gas die bonder with a temperature setting of 290°C is normally used to melt the solder. The chip should not be exposed to temperatures greater than 320°C for more than 20 seconds. Typically no more than three seconds at peak temperature is required for attachment. RoHS compliant solders may also be used but solders rich in tin should be avoided as they will scavenge the backside gold and/or cause gold embrittlement.

Epoxy Die Attachment
A minimum amount of epoxy, 1 - 2 mils thick, should be used to attach chip. A thin epoxy fillet should be visible around the outer perimeter of the chip after placement. Epoxy cure time is typically 1 hour at 150°C.
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