MA4SPS552

SURMOUNT™ PIN Diode

Features
- Surface Mount Device
- No Wirebonds Required
- Rugged Silicon-Glass Construction
- Silicon Nitride Passivation
- Polymer Scratch Protection
- Ultra-Low Parasitic Capacitance and Inductance
- Higher Power Handling (Efficient Heat sinking)
- RoHS* Compliant

Description
The MA4SPS552 is a silicon-glass PIN diode chip fabricated with MACOM’s patented HMIC process. This device features two silicon pedestals embedded in a low loss glass. The diode is formed on the top of one pedestal and connections to the backside of the device are facilitated by making the pedestal sidewalls conductive. Selective backside metallization is applied producing a surface mount device. The topside is fully encapsulated with silicon nitride and has an additional polymer layer for scratch protection. These protective coatings prevent damage to the junction and the anode air-bridge during handling and assembly.

These packageless devices are suitable for usage in moderate incident power (5 W CW) or higher incident peak power (200 W, 1 µs, 0.001 Duty) series, shunt, or series-shunt switches. Small parasitic inductance, 0.7 nH, and excellent RC constant (0.20 ps) make the devices ideal for TR switch and accessory switch circuits, where higher P1db and IP3 values are required.

These diodes can also be used in π, T, Tapered Resistance, and Switched-Pad Attenuator Control Circuits for 50 Ω or 75 Ω systems.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MA4SPS552</td>
<td>100 pc. die in carrier</td>
</tr>
<tr>
<td>MADP-000552-12810T</td>
<td>3000 pc. pocket tape on reel</td>
</tr>
</tbody>
</table>


MA-COM Technology Solutions Inc. (MACOM) and its affiliates reserve the right to make changes to the product(s) or information contained herein without notice.
Visit www.macom.com for additional data sheets and product information.

For further information and support please visit: https://www.macom.com/support
**Surmount™ Pin Diode**

**Electrical Specifications @ +25°C**

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
<tbody>
<tr>
<td>C_T</td>
<td>Capacitance</td>
<td>-40 V, 1 MHz, -40 V, 1 GHz</td>
<td>pF</td>
<td>0.08</td>
<td>0.06</td>
<td>0.14</td>
</tr>
<tr>
<td>R_S</td>
<td>Resistance</td>
<td>100 mA, 100 MHz, 20 mA, 100 MHz</td>
<td>Ω</td>
<td>1.7</td>
<td>2.4</td>
<td>—</td>
</tr>
<tr>
<td>V_F</td>
<td>Forward Voltage</td>
<td>100 mA, 10 mA</td>
<td>V</td>
<td>1.00</td>
<td>0.88</td>
<td>1.25</td>
</tr>
<tr>
<td>V_R</td>
<td>Reverse Voltage</td>
<td>-10 µA</td>
<td>V</td>
<td>-200</td>
<td>-275</td>
<td>—</td>
</tr>
<tr>
<td>I_R</td>
<td>Reverse Leakage Current</td>
<td>40 V</td>
<td>nA</td>
<td>—</td>
<td>-10</td>
<td>—</td>
</tr>
<tr>
<td>R_THJC</td>
<td>Thermal Resistance</td>
<td>Steady State</td>
<td>°C/W</td>
<td>—</td>
<td>30</td>
<td>—</td>
</tr>
<tr>
<td>T_L</td>
<td>Lifetime</td>
<td>+10 mA / -6 mA (50% - 90% V)</td>
<td>µs</td>
<td>—</td>
<td>2.5</td>
<td>—</td>
</tr>
</tbody>
</table>

**Absolute Maximum Ratings**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dissipated RF &amp; DC Power</td>
<td>1 W</td>
</tr>
<tr>
<td>Forward Current</td>
<td>100 mA</td>
</tr>
<tr>
<td>Reverse Voltage</td>
<td>-200 V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-65°C to +150°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+175°C</td>
</tr>
<tr>
<td>Mounting Temperature</td>
<td>+260°C</td>
</tr>
</tbody>
</table>

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. MACOM does not recommend sustained operation near these survivability limits.

**Handling Procedures**

Please observe the following precautions to avoid damage:

**Static Sensitivity**

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.
Typical Performance Curves

**Capacitance vs. Frequency**

![Capacitance vs. Frequency Graph](image)

**Capacitance vs. Voltage**

![Capacitance vs. Voltage Graph](image)

**Series Resistance vs. Frequency**

![Series Resistance vs. Frequency Graph](image)

**Series Resistance vs. Current**

![Series Resistance vs. Current Graph](image)

**Series Resistance vs. Reverse Bias**

![Series Resistance vs. Reverse Bias Graph](image)
Typical Performance Curves

**Insertion Loss vs. Frequency**

**Isolation vs. Frequency**

**Input Return Loss vs. Frequency**

**Output Return Loss vs. Frequency**
Handling
All semiconductor chips should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of plastic tipped tweezers or vacuum pickups is strongly recommended for individual components. Bulk handling should insure that abrasion and mechanical shock are minimized.

Die Attach
Attachment to a circuit board is made simple through the use of surface mount technology. Mounting pads are conveniently located on the bottom surface of these devices and are removed from the active junction locations. These devices are well suited for solder attachment onto hard and soft substrates. The use of 80Au/20Sn and 60Sn/40Pb solder is recommended. Conductive epoxy for attachment may also be used.

When soldering these devices to a hard substrate, hot gas die bonding is preferred. We recommend utilizing a vacuum tip and force of 60 to 100 grams applied normal to the top surface of the device.

When soldering to soft substrates, it is recommended to use a lead-tin interface at the circuit board mounting pads. Position the die so that its mounting pads are aligned with the circuit board mounting pads and reflow the solder by heating the circuit trace near the mounting pad while applying 60 to 100 grams of force perpendicular to the top surface of the die. Equal Heat must be applied to both ohmic contacts. Since the HMIC glass is transparent, the edges of the mounting pads closest to each other can be visually inspected through the die after attach is completed.

Recommended temperature and re-flow profiles for 60/40, Sn/Pb and RoHS compliant solders are provided in Application Note M538, "Surface Mounting Instructions".