Features

- Surface Mount
- No Wire Bonding Required
- Rugged Silicon-Glass Construction
- Silicon Nitride Passivation
- Polymer Scratch Protection
- Low Parasitic Capacitance and Inductance
- Higher Average and Peak Power Handling
- RoHS Compliant* and 260º Reflow Compatible

Description

This device is a Silicon-Glass PIN diode chip fabricated with M/A-COM Technology Solutions patented HMIC™ process. This device features two silicon pedestals embedded in a low loss, low dispersion glass. The diode is formed on the top of one pedestal and connections to the backside of the device are facilitated by making the pedestal side-walls electrically conductive. Selective backside metallization is applied producing a surface mount device. This vertical topology provides for exceptional heat transfer. The topside is fully encapsulated with silicon nitride and has an additional polymer layer for scratch and impact protection. These protective coatings prevent damage to the junction and the anode air-bridge during handling and assembly.

Applications

These surmount devices are suitable for usage in moderate incident power (10W C.W.) or higher incident peak power (500W) series, shunt, or series-shunt switches. Lower parasitic inductance, 0.1 to 0.2nH, and excellent RC constant (0.45pS), make the devices ideal for higher frequency switch elements compared to their plastic device counterparts.

Absolute Maximum Ratings @ T_A = 25°C (unless otherwise specified)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Forward Current</td>
<td>250mA</td>
</tr>
<tr>
<td>Reverse Voltage</td>
<td>-200V</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-55°C to +125°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-55°C to +150°C</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>+175°C</td>
</tr>
<tr>
<td>Dissipated Power (RF &amp; DC)</td>
<td>1.8W</td>
</tr>
<tr>
<td>Mounting Temperature</td>
<td>+280°C for 30 seconds</td>
</tr>
</tbody>
</table>

### Electrical Specifications @ $T_A = +25^\circ C$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Units</th>
<th>MA4SPS421</th>
<th>MA4SPS422</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capacitance</td>
<td>$C_T$</td>
<td>$0V, 1MHz^1$</td>
<td>pF</td>
<td>0.130</td>
<td>0.175</td>
</tr>
<tr>
<td>Capacitance</td>
<td>$C_T$</td>
<td>$0V, 1GHz^{1,3}$</td>
<td>pF</td>
<td>0.080</td>
<td></td>
</tr>
<tr>
<td>Capacitance</td>
<td>$C_T$</td>
<td>$-40V, 1MHz^1$</td>
<td>pF</td>
<td>0.090</td>
<td>0.125</td>
</tr>
<tr>
<td>Capacitance</td>
<td>$C_T$</td>
<td>$-40V, 1GHz^{1,3}$</td>
<td>pF</td>
<td>0.070</td>
<td></td>
</tr>
<tr>
<td>Resistance</td>
<td>$R_S$</td>
<td>$+10mA, 1GHz^{2,3}$</td>
<td>$\Omega$</td>
<td>6.200</td>
<td></td>
</tr>
<tr>
<td>Forward Voltage</td>
<td>$V_F$</td>
<td>$+10mA$</td>
<td>V</td>
<td>0.900</td>
<td>0.950</td>
</tr>
<tr>
<td>Reverse Leakage Current</td>
<td>$I_R$</td>
<td>$-200V$</td>
<td>$\mu A$</td>
<td>$</td>
<td>\cdot10</td>
</tr>
<tr>
<td>Input Third Order Intercept Point</td>
<td>IIP$^3$</td>
<td>$F_1 = 1000MHz$</td>
<td>$dBm$</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$F_2 = 1010MHz$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>Input Power = $+10dBm$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{bias} = +10mA$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Minority Carrier Lifetime</td>
<td>$T_L$</td>
<td>$+10mA / -6mA$</td>
<td>$\mu S$</td>
<td>5</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$(50% - 90% V)$</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>C.W. Thermal Resistance</td>
<td>$R_{\Theta JL}$</td>
<td>$I_H = 0.5A$, $I_L = 10mA^4$</td>
<td>°C/W</td>
<td>80</td>
<td>70</td>
</tr>
</tbody>
</table>

1. Total Capacitance, $C_T$, is equivalent to the sum of Junction Capacitance and Parasitic Capacitance. $C_T = C_J$ (Junction Capacitance) + $C_{PAR}$ (Parasitic Capacitance)

2. Series resistance $R_S$ is equivalent to the total diode resistance: $R_S = R_J$ (Junction Capacitance Junction Resistance) + $R_O$ (Ohmic Resistance)

3. $R_S$ and $C_T$ are measured on an HP4291A Impedance Analyzer with die mounted in an ODS-186 package.

4. Steady-state $R_{\Theta JL}$ measured with die mounted in an ODS-186 package.
MA4SPS42X Series

SURMOUNT™ PIN Diodes
MA4SPS421, MA4SPS422

Typical Performance @ $T_{\text{AMB}} = +25^\circ\text{C}$

Rs vs. Current (100MHz & 1GHz)

- **MA4SPS421**
  - Rs (ohms)
  - Current (A)

- **MA4SPS422**
  - Rs (ohms)

Ct vs Voltage (@ 100MHz & 1GHz)

- **MA4SPS421**
  - Ct (F)
  - Voltage (V)

- **MA4SPS422**
  - Ct (F)

Rp vs Voltage (@ 100MHz & 1GHz)

- **MA4SPS421**
  - Rp (ohms)
  - Voltage (V)

- **MA4SPS422**
  - Rp (ohms)

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Typical Performance @ $T_A = +25^\circ C$

- **Ct vs Frequency (@ 0, 5 & 40V)**
  - **MA4SPS421**
  - **MA4SPS422**

- **Ls vs Frequency (@ 10mA & 100mA)**
  - **MA4SPS421**
  - **MA4SPS422**

- **Rs vs Frequency (@ 10mA & 100mA)**
  - **MA4SPS421**
  - **MA4SPS422**
MA4SPS421 ADS SPICE Model

- PinDiodeModel
- NLPINM1
- Is=3.5E-12A
- Vi=0.0V
- Un=900cm^2/V-sec
- Wi=100μm
- Rr=11KΩ
- Cmin=0.06pF
- Tau= 5μS
- Rs=0.1Ω
- Cj0=0.07pF
- Vj=0.7V
- M=0.5
- Fc=0.5
- Imax=3.1 E+8 A/m^2
- Kf=0.0
- Af=1.0

MA4SPS421 Schematic Diagram

Notes: Rs = 2 * Rvia + Rp
MA4SPS42X Series

MA4SPS422 ADS SPICE

PinDiodeModel: NLPINM1
Ffe = 1.0
Af = 1.0
Kf = 0.0
Imax = 7.8E+7 A/m^2
Fc = 0.5
M = 0.5
Vj = 0.7 V
Cj0 = 0.13 pF
Rs = 0.1 Ω
Rr = 9 KΩ

Notes:
Rs = 2 * Rvia + Rp

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Handling

All semiconductor chips should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of plastic tipped tweezers or vacuum pickups is strongly recommended for individual components. Bulk handling should insure that abrasion and mechanical shock are minimized.

Bonding

Attachment to a circuit board is made simple through the use of surface mount technology. Mounting pads are conveniently located on the bottom surface of these devices and are removed from the active junction locations. These devices are well suited for solder attachment onto hard and soft substrates. The use of 80/20, Au/Sn, 60/40, Sn/Pb or RoHS compliant solders is recommended. Conductive silver epoxy solder may also be used but could result in an increase in series and thermal resistance.

When soldering these devices to a hard substrate, hot gas die bonding is preferred. We recommend utilizing a vacuum tip and force of 60 to 100 grams applied normal to the top surface of the device. When soldering to soft substrates, it is recommended to use a lead-tin interface at the circuit board mounting pads. Position the die so that its mounting pads are aligned with the circuit board mounting pads and reflow the solder by heating the circuit trace near the mounting pad while applying 60 to 100 grams of force perpendicular to the top surface of the die. The solder joint must not be made one at a time, creating unequal heat flow and thermal stress. Solder reflow should not be performed by causing heat to flow through the top surface of the die. Since the HMIC glass is transparent, the edges of the mounting pads closest to each other can be visually inspected through the die after attach is completed.

Recommended temperature and re-flow profiles for 60/40, Sn/Pb and RoHS compliant solders are provided in Application Note M538, “Surface Mounting Instructions”. Application Note M541 “Bonding and Handling Procedures for Chip Diode Devices” provides handling and assembly recommendations.

Ordering Information

The MA4SPS42X series of SURMOUNTS may be ordered in either gel packs or tape and reeled by adding the appropriate suffix per the table below. Tape and reel dimensions are provided in Application Note M513 located on the M/A-COM Tech website @ www.macomtech.com.

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Gel Pack</th>
<th>Tape and Reel Pocket Tape</th>
</tr>
</thead>
<tbody>
<tr>
<td>MA4SPS421</td>
<td>MADP-000421-12940P</td>
<td></td>
</tr>
<tr>
<td>MA4SPS422</td>
<td>MADP-000422-12950P</td>
<td></td>
</tr>
</tbody>
</table>
# MA4SPS42X Series

**SURMOUNT™ PIN Diodes**

MA4SPS421, MA4SPS422

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<table>
<thead>
<tr>
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</thead>
<tbody>
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