Features
- Low Series Resistance: 2.6 Ω
- Low Capacitance: 40 fF
- Fast Switching Speed: 40 ns
- Silicon Nitride Passivation
- Polyimide Scratch Protection
- Designed for Automated Pick and Place Insertion
- Rugged Design
- RoHS Compliant

Description
The MA4FCP300 is a silicon flip chip PIN diode which is fabricated using MACOMs’ patented HMIC process. The epitaxial silicon and processes used are designed to produce repeatable electrical characteristics and extremely low parasitics. The diode is fully passivated with silicon nitride and also has an additional layer of polyimide for scratch protection. These protective coatings prevent damage to the junction area during manual or automated handling making it suitable for pick and place insertion.

Applications
The small outline and low, 0.12 ps, RC product, make the device useful for multi-throw switch and switched phase shifter circuits requiring <40 ns switching speeds at frequencies up to 18 GHz.

1269 Package Outline

1269 Package Dimensions

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Packaging</th>
</tr>
</thead>
<tbody>
<tr>
<td>MA4FCP300</td>
<td>Die in Waffle Pack</td>
</tr>
<tr>
<td>MADP-000300-12690P</td>
<td>Tape &amp; Reel</td>
</tr>
</tbody>
</table>

1. Reference Application Note M513 for reel size information.

2. Yellow hatched areas indicate backside ohmic gold contacts.

3. Total backside metal thickness 0.1 μm.
Electrical Specifications: \( T_A = +25^\circ C \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Units</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
</tr>
</thead>
</table>
| Total Capacitance\(^4,6\) (\(C_T\)) | -10 V, 1 MHz  
-10 V, 1 GHz | pF    | —     | 0.040 | 0.080 |
| Series Resistance\(^5,6\) (R\(_S\))   | 50 mA, 100 MHz  
50 mA, 1 GHz | Ω     | —     | 2.1   | 2.6   |
| Forward Voltage (\(V_F\))      | 100 mA          | V     | —     | 1.1   | 1.5   |
| Reverse Voltage (\(V_R\))      | -10 µA          | V     | -70   | -100  | —     |
| Reverse Current (\(I_R\))      | -40 V           | µA    | —     | —     | 10    |
| Lifetime (\(T_L\))             | 50 - 90%, @ 10 mA / -6 mA | ns    | —     | 140   | —     |
| Thermal Resistance\(^7\) (\(\theta\)) | Steady State   | °C/W  | —     | 450   | —     |

4. Total capacitance is equivalent to the sum of junction capacitance \(C_J\) and any parasitic capacitances.
5. Series resistance \(R_S\) is equivalent to the total diode series resistance including the junction resistance \(R_J\).
6. \(R_S\) and \(C_T\) measured on an HP4291A with die mounted in an ODS-186 package.
7. Steady-state Thermal Resistance measured with die mounted in an ODS-186 package.

Absolute Maximum Ratings\(^8,9\)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Forward Current</td>
<td>100 mA</td>
</tr>
<tr>
<td>Reverse Voltage</td>
<td>-100 V</td>
</tr>
<tr>
<td>Dissipated Power</td>
<td>150 mW</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-55°C to +150°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-55°C to +150°C</td>
</tr>
<tr>
<td>Mounting Temperature</td>
<td>+300°C for 10 seconds</td>
</tr>
</tbody>
</table>

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (0 - 199 V), HBM, per MIL-STD-883, method 3015.7 devices.

8. Exceeding any one or combination of these limits may cause permanent damage to this device.
9. MACOM does not recommend sustained operation near these survivability limits.
Handling Procedures

All semiconductor chips should be handled with care to avoid damage or contamination from perspiration and skin oils. The use of plastic tipped tweezers or vacuum pickups is strongly recommended for individual components. Bulk handling should ensure that abrasion and mechanical shock are minimized.

Bonding Techniques

The MA4FCP300 is designed for insertion onto hard or soft substrates with the junction and gold pads facing down. They can be attached with electrically conductive epoxy, 80Au/20Sn eutectic solder or any solder with a low tin content. A tin rich solder should not be used as it will scavenge the gold on the surface of the pad or may cause gold embrittlement. Soldering temperatures should not exceed 300°C for 10 seconds. It is recommended that the chips be mounted onto circuits using electrically conductive, Ag, epoxy, approximately 1-2 mils in thickness and cured at approximately 90°C to 150°C per manufacturer’s schedule. For extended cure times, >30 minutes, temperatures must be below 200°C. The die can also be assembled with the junction or contact side up, and wire or ribbon bonds made to the pads.

Circuit Mounting Dimensions (Inches)

0.013”

0.008” (2) PL

0.012” (2) PL

0.008” (2) PL
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