

# CMPA2738060F

60 W, 2.7 - 3.8 GHz, GaN MMIC, Power Amplifier

#### Description

The CMPA2738060F is a packaged, high-power MMIC amplifier producing 85W of saturated output power over the 2.7 - 3.8 GHz frequency range. With 27dB of large signal gain and achieving 50% power-added efficiency or higher, the CMPA2738060F is ideally suited to support a variety of S-Band radar applications.

The CMPA2738060F also supports ease of use and straight-forward system integration. Matched to 50 ohms at both RF ports along with DC blocking capacitors, thermal-management is further enhanced in a bolt-down, flanged package allowing for long-pulse operation.



Package Type: 440219 PN: CMPA2738060F

#### Typical Performance Over 2.7 - 3.8 GHz ( $T_c = 25^{\circ}C$ )

Parameter	2.7 GHz	2.9 GHz	3.1 GHz	3.5 GHz	3.8 GHz	Units
Small Signal Gain	36.1	36.0	34.5	35.7	35.0	dB
Output Power <sup>1</sup>	88.0	86.5	74.0	81.0	81.2	W
Power Gain <sup>1</sup>	29.4	29.4	28.7	29.1	29.1	dB
PAE <sup>1</sup>	52.5	55.5	50.4	53.0	51.0	%

Note:

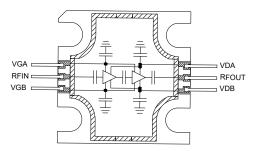
 $^{1}P_{IN} = 20 \text{ dBm}$ 

#### Features

- 35 dB Small Signal Gain
- 80 W Typical P<sub>SAT</sub>
- Operation up to 50 V
- High Breakdown Voltage
- High Temperature Operation
- 0.5" x 0.5" Total Product Size

#### Applications

 Civil and Military Pulsed Radar Amplifiers





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#### Absolute Maximum Ratings (not simultaneous) at 25°C

Parameter	Symbol	Rating	Units	Conditions
Drain-source Voltage	V <sub>DSS</sub>	150	N	
Gate-source Voltage	V <sub>GS</sub>	-10, +2	V <sub>DC</sub>	25°C
Storage Temperature	T <sub>STG</sub>	-65, +150	°C	
Operating Junction Temperature	TJ	225		
Maximum Forward Gate Current	I <sub>GMAX</sub>	12	mA	25°C
Screw Torque	τ	40	in-oz	
	D	0.77	96 /00	300μsec, 20%, 85°C
Thermal Resistance, Junction to Case (packaged) <sup>1</sup>	R <sub>θJC</sub>	1.44	°C/W	CW, 85°C

Note:

 $^{\rm 1}$  Measured for the CMPA2738050F at  $P_{\text{DISS}}$  = 64 W

#### Electrical Characteristics (Frequency = 2.7 GHz to 3.8 GHz unless otherwise stated; $T_c = 25^{\circ}C$ )

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
DC Characteristics						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-3.8	-3.0	-2.3	V	$V_{DS} = 10 \text{ V}, I_{D} = 15.2 \text{ mA}$
Gate Quiescent Voltage	V <sub>GS(Q)</sub>	_	-2.7	_	V <sub>DC</sub>	$V_{DD} = 50 \text{ V}, I_{DQ} = 280 \text{ mA}$
Saturated Drain Current <sup>1</sup>	I <sub>DS</sub>	9.9	14.1	_	A	$V_{DS} = 6.0 \text{ V}, V_{GS} = 2.0 \text{ V}$
Drain-Source Breakdown Voltage	V <sub>BD</sub>	100	-	_	V	$V_{GS} = -8 V$ , $I_{D} = 15.2 mA$
RF Characteristics <sup>2,3</sup>						
Small Signal Gain at 2.7 GHz		_	36.1	_		
Small Signal Gain at 3.1 GHz	S21	_	34.5	_	dB	$V_{DD} = 50 \text{ V}, I_{DQ} = 280 \text{ mA}$
Small Signal Gain at 3.8 GHz		-	35.0	_		
Output Power at 2.7 GHz		_	88.0	_		
Output Power at 3.1 GHz	Pout	-	86.5	-	w	$V_{DD} = 50 \text{ V}, I_{DQ} = 280 \text{ mA}, P_{IN} = 20 \text{ dBm}$
Output Power at 3.8 GHz		-	81.2	-		
Power Added Efficiency at 2.7 GHz		_	52.5	_		
Power Added Efficiency at 3.1 GHz	PAE	—	55.5	—	%	
Power Added Efficiency at 3.8 GHz		_	51.0	_		
Input Return Loss at 2.7 GHz		_	-11.3	_		
Input Return Loss at 3.1 GHz	S11	-	-25.0	-		$V_{DD} = 50 \text{ V}, I_{DQ} = 280 \text{ mA}$
Input Return Loss at 3.8 GHz		_	-11.5	_	dB	
Output Return Loss at 2.7 GHz		_	-8.5	_		
Output Return Loss at 3.1 GHz	S22	_	-11.0	_		
Output Return Loss at 3.8 GHz		_	-8.0	_		
Output Mismatch Stress	VSWR	_	_	5:1	Ψ	No damage at all phase angles, $V_{DD} = 50 \text{ V}$ , $I_{DQ} = 280 \text{ mA}$ , $P_{OUT} = 60 \text{ W}$

Notes:

<sup>1</sup> Scaled from PCM data

<sup>2</sup> All data pulse tested in CMPA2738060F-AMP

 $^3$  Pulse Width = 300  $\mu s$ , Duty Cycle = 20%

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<sup>2</sup> 



Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DO</sub> = 280 mA, PW = 300µs, DC = 20%, P<sub>IN</sub> = 20 dBm, -40°C at P<sub>IN</sub> = 18 dBm, Frequency = 3.1 GHz,  $T_{BASE} = +25^{\circ}\text{C}$ 

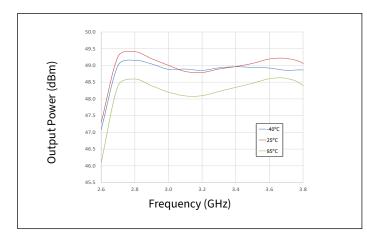


Figure 1. Output Power vs Frequency as a Function of Temperature

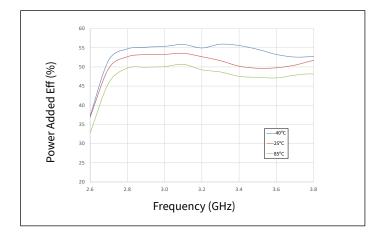


Figure 3. Power Added Eff. vs Frequency as a Function of Temperature

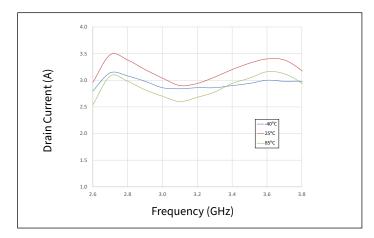


Figure 5. Drain Current vs Frequency as a Function of Temperature

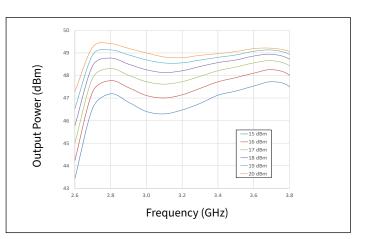


Figure 2. Output Power vs Frequency as a Function of Input Power

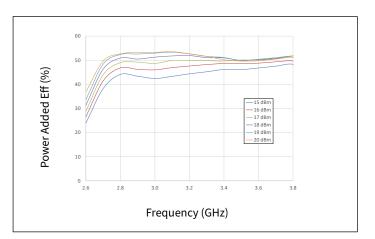
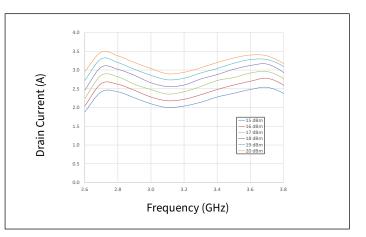
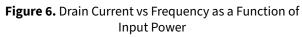


Figure 4. Power Added Eff. vs Frequency as a Function of Input Power





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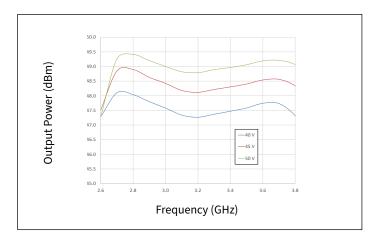


Figure 7. Output Power vs Frequency as a Function of  $V_D$ 

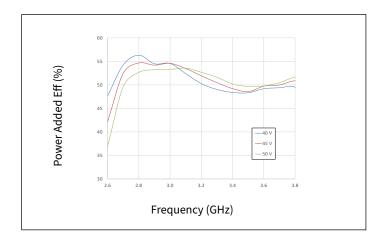


Figure 9. Power Added Eff. vs Frequency as a Function of  $V_D$ 

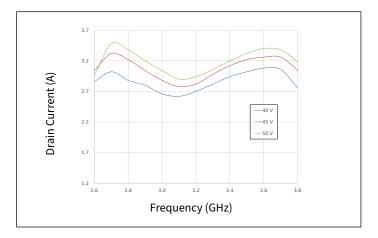


Figure 11. Drain Current vs Frequency as a Function of V<sub>D</sub>

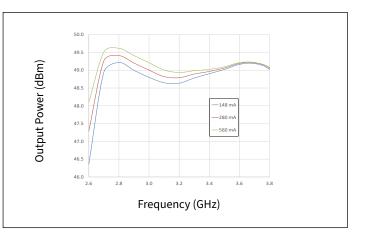
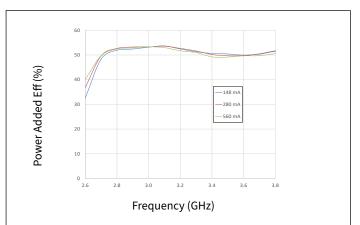
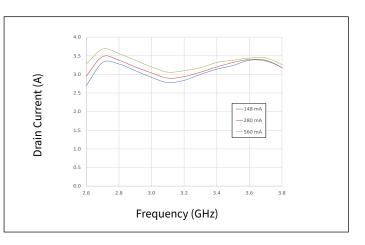


Figure 8. Output Power vs Frequency as a Function of  $I_{DQ}$ 









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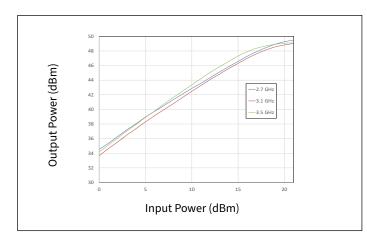


Figure 13. Output Power vs Input Power as a Function of Frequency

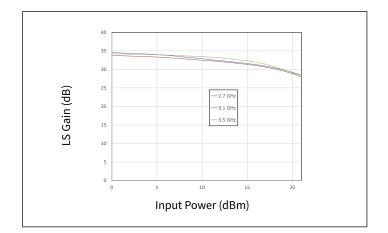


Figure 15. Large Signal Gain vs Input Power as a Function of Frequency

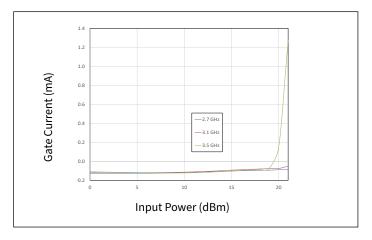


Figure 17. Gate Current vs Input Power as a Function of Frequency

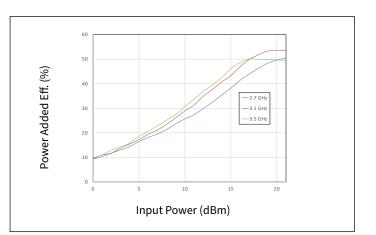


Figure 14. Power Added Eff. vs Input Power as a Function of Frequency

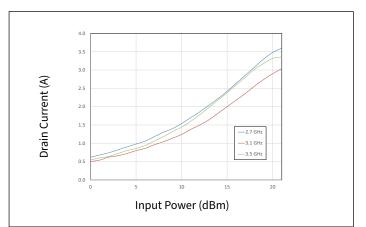


Figure 16. Drain Current vs Input Power as a Function of Frequency

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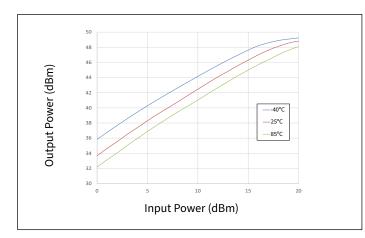


Figure 18. Output Power vs Input Power as a Function of Temperature

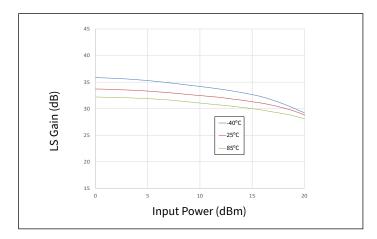


Figure 20. Large Signal Gain vs Input Power as a Function of Temperature

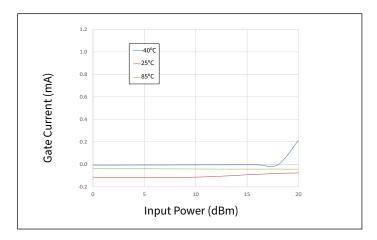


Figure 22. Gate Current vs Input Power as a Function of Temperature

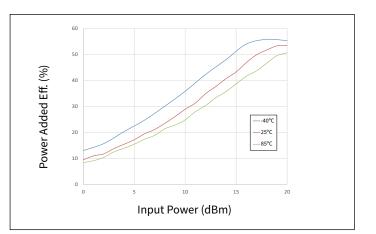


Figure 19. Power Added Eff. vs Input Power as a Function of Temperature

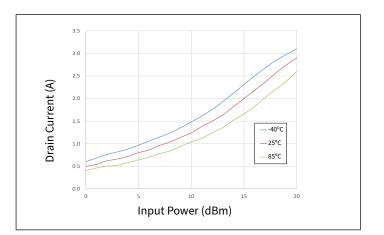


Figure 21. Drain Current vs Input Power as a Function of Temperature

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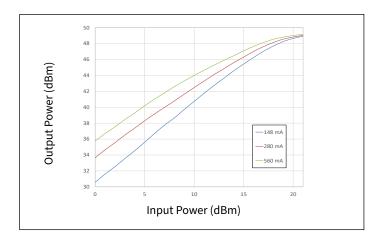


Figure 23. Output Power vs Input Power as a Function of  $I_{DQ}$ 

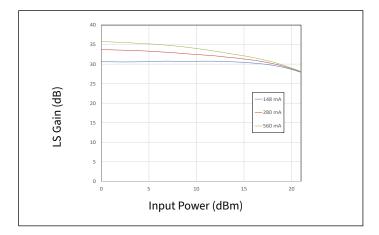


Figure 25. Large Signal Gain vs Input Power as a Function of I<sub>DO</sub>

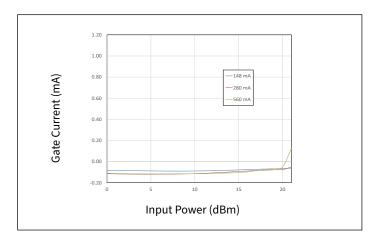


Figure 27. Gate Current vs Input Power as a Function of IDQ

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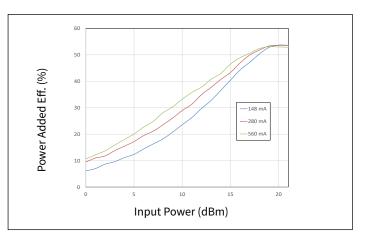


Figure 24. Power Added Eff. vs Input Power as a Function of I<sub>DQ</sub>

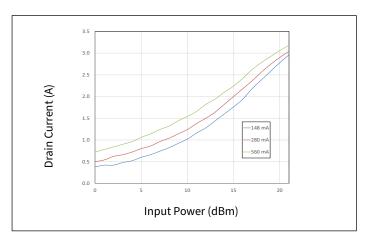


Figure 26. Drain Current vs Input Power as a Function of I<sub>DO</sub>

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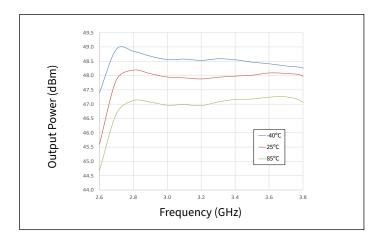


Figure 28. Output Power vs Frequency as a Function of Temperaturee

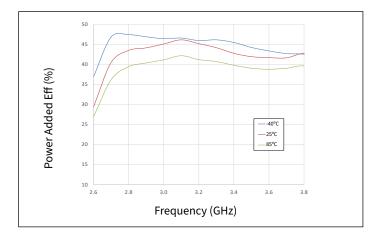


Figure 30. Power Added Eff. vs Frequency as a Function of Temperature

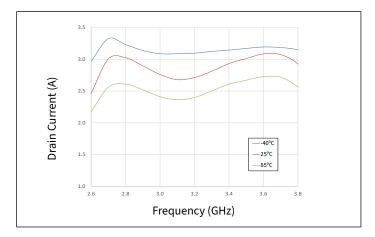


Figure 32. Drain Current vs Frequency as a Function of Temperature

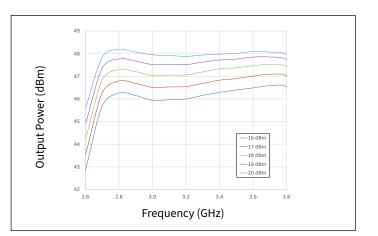


Figure 29. Output Power vs Frequency as a Function of Input Power

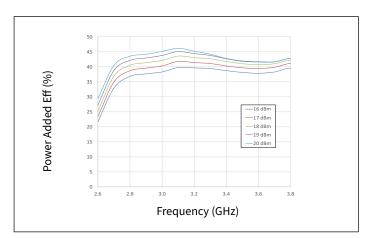
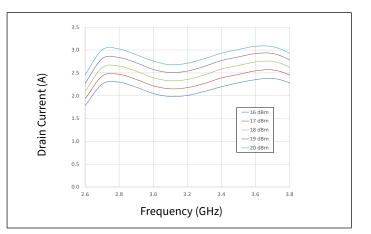
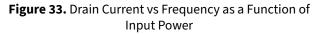


Figure 31. Power Added Eff. vs Frequency as a Function of Input Power





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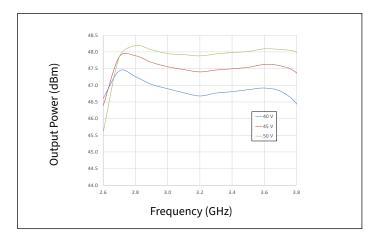


Figure 34. Output Power vs Frequency as a Function of Voltage

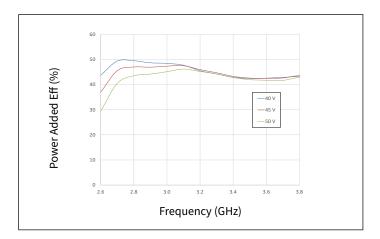


Figure 36. Power Added Eff. vs Frequency as a Function of Voltage

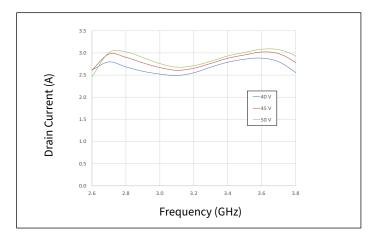


Figure 38. Drain Current vs Frequency as a Function of Voltage

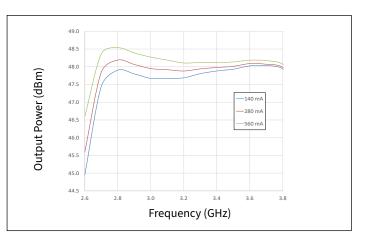


Figure 35. Drain Current vs Frequency as a Function of Input Power

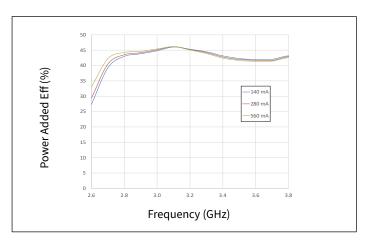
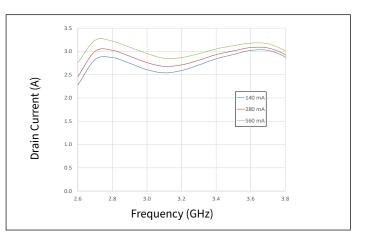
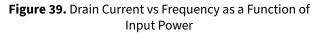


Figure 37. Power Added Eff. vs Frequency as a Function of Input Power





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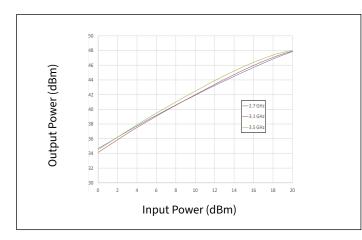


Figure 40. Output Power vs Input Power as a Function of Frequency

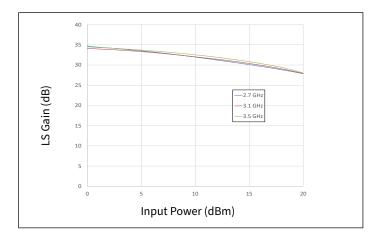


Figure 42. Large Signal Gain vs Input Power as a Function of Frequency

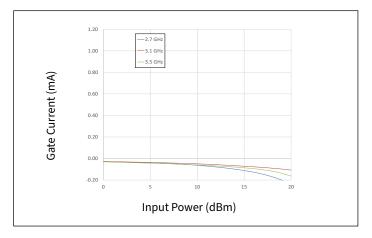


Figure 44. Gate Current vs Input Power as a Function of Frequency

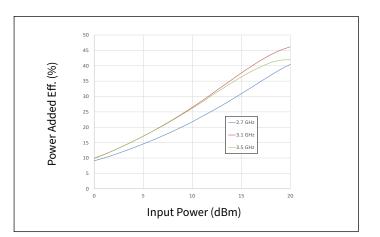


Figure 41. Power Added Eff. vs Input Power as a Function of Frequency

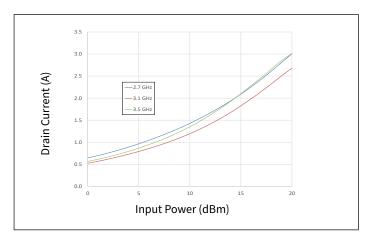


Figure 43. Drain Current vs Input Power as a Function of Frequency

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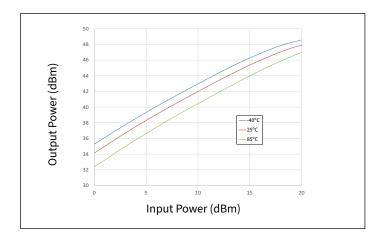


Figure 45. Output Power vs Input Power as a Function of Temperature

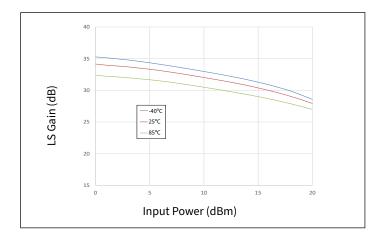


Figure 47. Large Signal Gain vs Input Power as a Function of Temperature

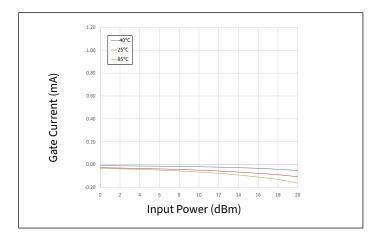


Figure 49. Gate Current vs Input Power as a Function of Temperature

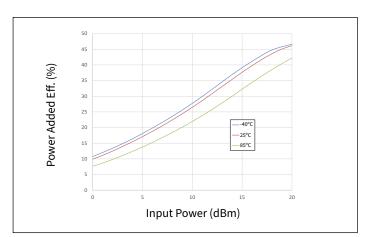


Figure 46. Power Added Eff. vs Input Power as a Function of Temperature

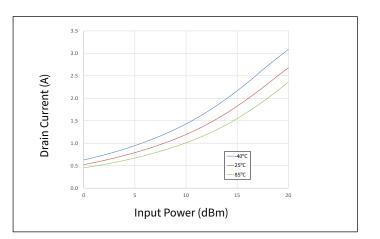


Figure 48. Drain Current vs Input Power as a Function of Temperature

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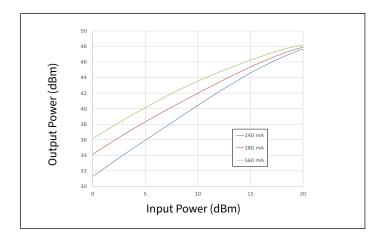


Figure 50. Output Power vs Input Power as a Function of  $I_{DQ}$ 

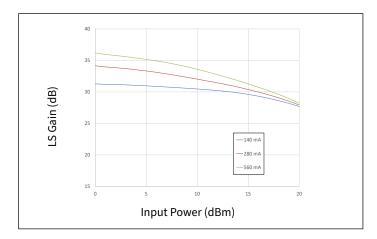


Figure 52. Large Signal Gain vs Input Power as a Function of  $I_{\mbox{\scriptsize DQ}}$ 

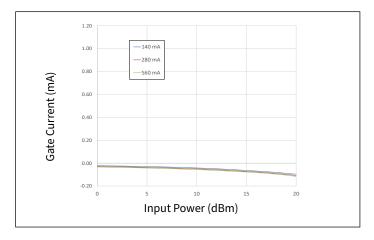


Figure 54. Gate Current vs Input Power as a Function of  $I_{DQ}$ 

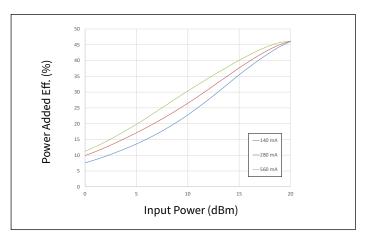


Figure 51. Power Added Eff. vs Input Power as a Function of I<sub>DQ</sub>

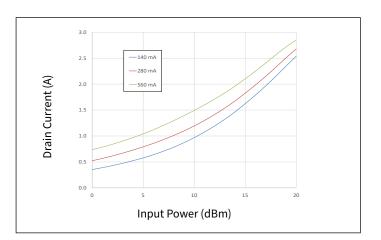


Figure 53. Drain Current vs Input Power as a Function of  $I_{DQ}$ 

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Test conditions unless otherwise noted: V<sub>D</sub> = 50 V, I<sub>DQ</sub> = 280 mA, PW = 300µs, DC = 20%, P<sub>IN</sub> = 20 dBm, Frequency = 3.1 GHz, T<sub>BASE</sub> = +25°C

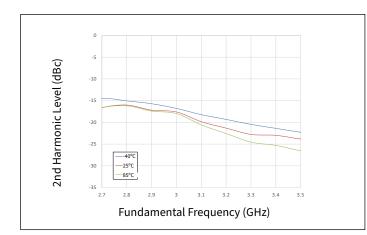


Figure 55. 2nd Harmonic vs Frequency as a Function of Temperature

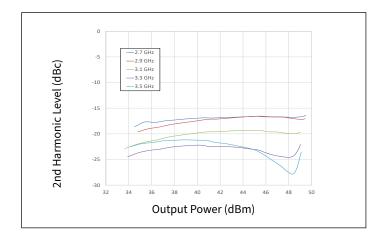


Figure 57. 2nd Harmonic vs Output Power as a Function of Frequency

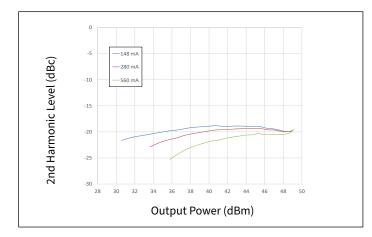


Figure 59. 2nd Harmonic vs Output Power as a Function of IDO

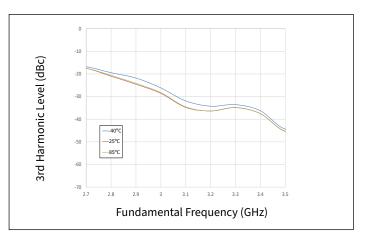


Figure 56. 3rd Harmonic vs Frequency as a Function of Temperature

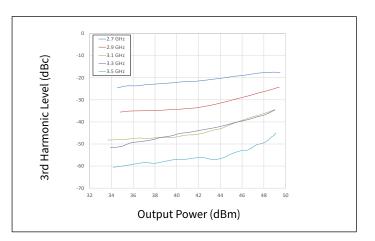
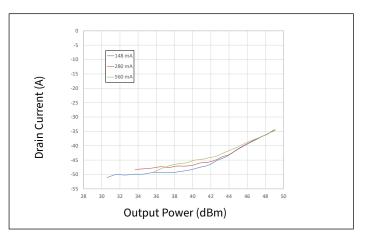
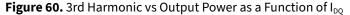


Figure 58. 3rd Harmonic vs Output Power as a Function of Frequency





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Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 280 \text{ mA}$ ,  $P_{IN} = -20 \text{ dBm}$ , Frequency = 3.1 GHz,  $T_{BASE} = +25^{\circ}\text{C}$ 

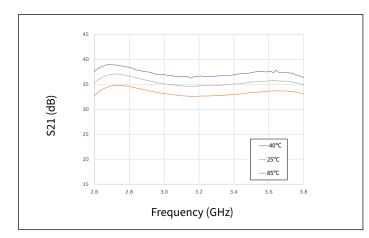


Figure 61. Gain vs Frequency as a Function of Temperature

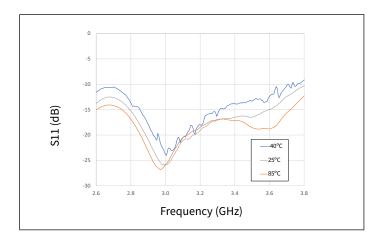


Figure 63. Input RL vs Frequency as a Function of Temperature

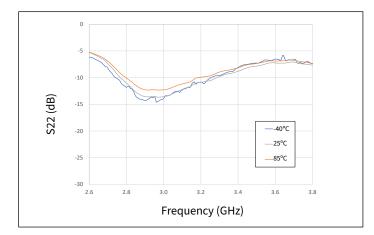


Figure 65. Output RL vs Frequency as a Function of Temperature

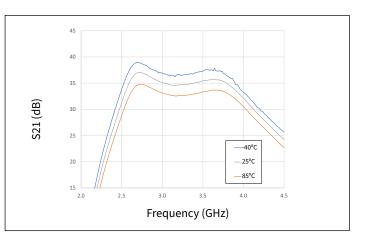


Figure 62. Gain vs Frequency as a Function of Temperature

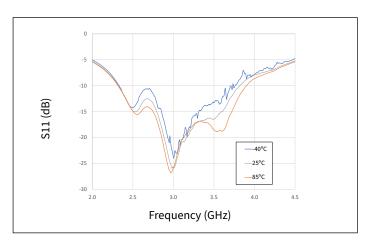
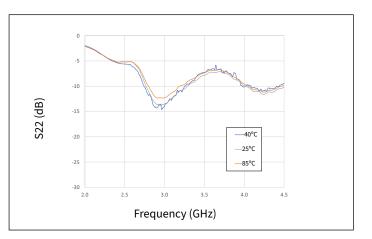
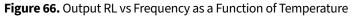


Figure 64. Input RL vs Frequency as a Function of Temperature





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Test conditions unless otherwise noted:  $V_D = 50 \text{ V}$ ,  $I_{DQ} = 280 \text{ mA}$ ,  $P_{IN} = -20 \text{ dBm}$ , Frequency = 3.1 GHz,  $T_{BASE} = +25^{\circ}\text{C}$ 

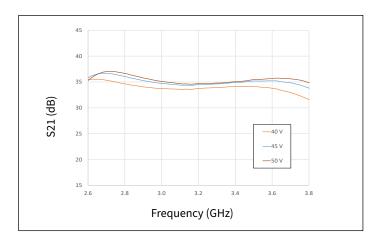


Figure 67. Gain vs Frequency as a Function of Voltage

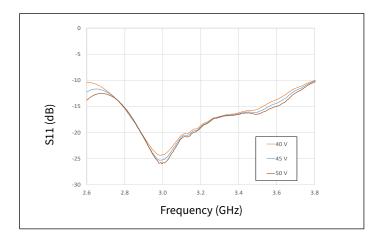


Figure 69. Input RL vs Frequency as a Function Voltage

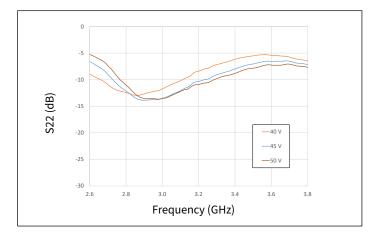


Figure 71. Output RL vs Frequency as a Function of Voltage

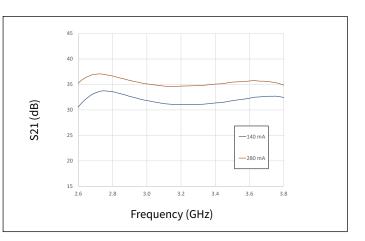
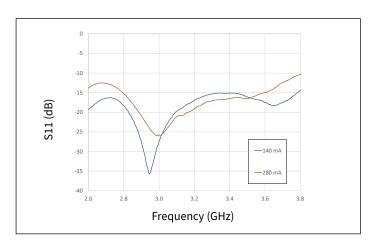
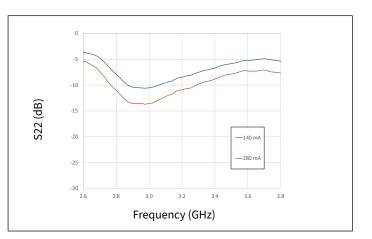


Figure 68. Gain vs Frequency as a Function of  $I_{DQ}$ 



**Figure 70.** Input RL vs Frequency as a Function of  $I_{DQ}$ 

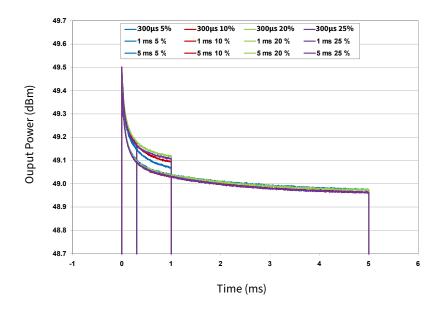


**Figure 72.** Output RL vs Frequency as a Function of  $I_{DQ}$ 

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#### **Typical Pulse Droop Performance**



Pulse Width	Duty Cycle (%)	Droop (dB)
10µs	5-25	0.30
50µs	5-25	0.30
100µs	5-25	0.30
300µs	5-25	0.35
1ms	5-25	0.40
5ms	5-25	0.55

### **Electrostatic Discharge (ESD) Classifications**

Parameter	Symbol	Class	Classification Level	Test Methodology
Human Body Model	НВМ	TBD	ANSI/ESDA/JEDEC JS-001 Table 3	JEDEC JESD22 A114-D
Charge Device Model	CDM	твр	ANSI/ESDA/JEDEC JS-002 Table 3	JEDEC JESD22 C101-C



#### CMPA2738060F-AMP Evaluation Board Bill of Materials

Designator	Description	Qty
C1	CAP, 15000pF, 100V, 0805, X7R	1
C2	CAP, 330μF, 20%, 100V, ELECT, MVY, SMD	1
R1	RES, 1/8W, 1206, +/-5%, 0 OHMS	1
R2	RES, 1/16W, 0603, +/-5%, 10K OHMS	1
L1	FERRITE, 22 OHM, 0805, BLM21PG220SN1	1
J1,J2	CONNECTOR, N-TYPE, FEMALE, W/0.500 SMA FLNG	2
J3	CONNECTOR, HEADER, RT>PLZ .1CEN LK 9POS	1
J4	CONNECTOR, SMB, STRAIGHT JACK, SMD	1
-	PCB, TACONIC, RF-35-0100-CH/CH	1
Q1	CMPA2738060F	1

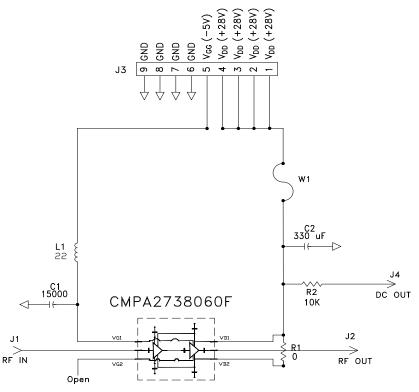
## CMPA2738060F-AMP Demonstration Amplifier Circuit



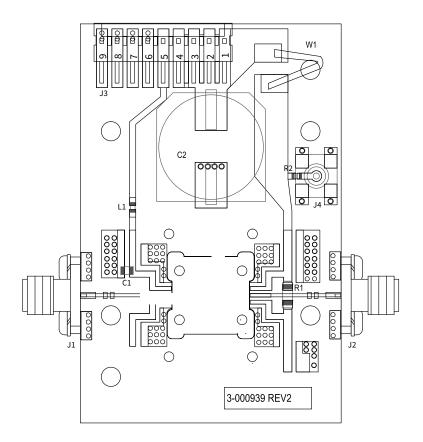
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#### CMPA2738060F-AMP Demonstration Amplifier Circuit Schematic



#### CMPA2738060F-AMP Demonstration Amplifier Circuit Outline

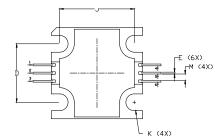


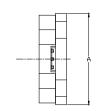
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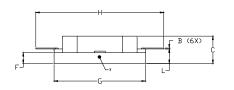
19

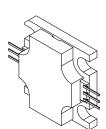


## Product Dimensions CMPA2738060F (Package Type – 440219)









NUT TO SCALE
--------------

PIN	Function
1	V <sub>GG</sub>
2	RFin
3	V <sub>GG</sub>
4	V <sub>DD</sub>
5	RFout
6	V <sub>DD</sub>
7	Source

#### NOTES

1. DIMENSIONING AND TOLERANICING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: INCH.

3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM DF 0.020" BEYOND EDGE OF LID.

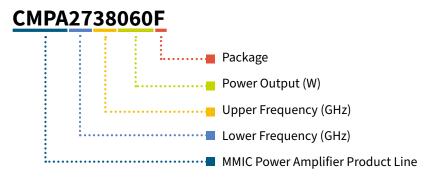
4. LID MAY BE MISALIGNED TO THE BODY OF THE PACKAGE BY A MAXIMUM OF 0.008' IN ANY DIRECTION. 5. ALL PLATED SURFACES ARE NI/AU

	INC	HES	MILLIN	ETERS
DIM	MIN	MAX	MIN	MAX
A	0.495	0.505	12.57	12.82
В	0.003	0.005	0.076	0.127
С	0.140	0.160	3.56	4.06
D	0.315	0.325	8.00	8.25
E	0.008	0.012	0.204	0.304
F	0.055	0.065	1.40	1.65
G	0.495	0.505	12.57	12.82
н	0.695	0.705	17.65	17.91
J	0.403	0.413	10.24	10.49
к	ø.	092	2.3	34
L	0.075	0.085	1.905	2.159
М	0.032	0.040	0.82	1.02

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#### Part Number System



#### Table 1.

Parameter	Value	Units
Lower Frequency	2.7	GHz
Upper Frequency	3.8	GHZ
Power Output	60	W
Package	Flange	_

Note:

<sup>1</sup> Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

#### Table 2.

Character Code	Code Value
A	0
В	1
С	2
D	3
E	4
F	5
G	6
Н	7
J	8
К	9
Examples	1A = 10.0 GHz 2H = 27.0 GHz

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# **Product Ordering Information**

Order Number	Description	Unit of Measure	Image
CMPA2738060F	GaN MMIC	Each	CHR2700055 CHR2700555
CMPA2738060F-AMP	Test board with GaN MMIC installed	Each	

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