Low Noise Amplifier 6 - 18 GHz



CGY2290SUH/C1

Rev. V1

Features

· Operating Frequency Range: 6 to 18 GHz

• Noise Figure: 3.3 dB

Gain: 8.5 dB

Gain Flatness: ±0.5 dB

P1dB: 12 dBm

Input Return Loss:14 dBOutput Return Loss:14 dBTotal Drain Current: 33 mA

Chip Size: 1400 x 1200 μm

100% RF Tested, Known Good Die

• Space and MIL-STD Available

RoHS* Compliant

Applications

Radar

Telecommunications

Instrumentation

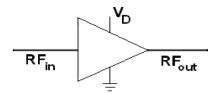
Description

The CGY2290SUH/C1 is a high performance GaAs MMIC Low Noise Amplifier operating from 6 GHz up to 18 GHz.

This device has a low noise figure of under 3.3 dB, with 8.5 dB of gain accross the operating frequency range. The on-chip matching provides better than 14 dB of Input / Output Return Losses. It is part of a new 6 - 18 GHz chipset that is dedicated to Radar, Telecommunication and Instrumentation applications.

The die is manufactured using ED02AH a 0.18 µm gate length pHEMT process. The MMIC uses gold bonding pads, backside metallization and is fully protected with Silicon Nitride passivation to obtain the highest level of reliability.

This technology has been evaluated for Space applications and is on the European Preferred Parts List of the European Space Agency.



Ordering Information

Part Number	Package	
CGY2290SUH/C1	Die	

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



Electrical Specifications: Measured On Reference Board, Freq. = 6 - 18 GHz, $V_{D1} = 5$ V, $I_D = 30$ mA, $T_A = +25$ °C

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	_	dB	_	8.5	_
Noise Figure	_	dB	_	3	_
Reverse Isolation	_	dB	_	-16	_
Input Return Loss	_	dB	_	_	15
Output Return Loss	_	dB	_	_	15
P1dB	_	dBm	_	13	_

Absolute Maximum Ratings^{1,2}

Parameter	Absolute Maximum	
Input Power	TBD dBm	
Voltage	0 to 6 V	
Junction Temperature	+150°C	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-55°C to +150°C	

^{2.} Exceeding any one or combination of these limits may cause permanent damage to this device.

Thermal Characteristics

Parameter	Absolute Maximum	
Thermal Resistance	TBD	

Handling Procedures

Please observe the following precautions to avoid damage:

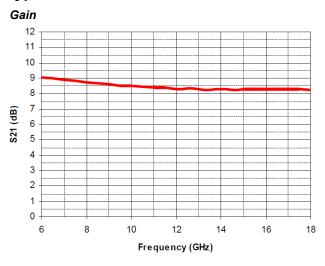
Static Sensitivity

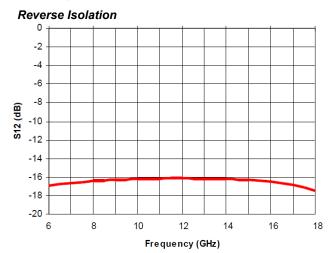
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

MACOM does not recommend sustained operation near these survivability limits.

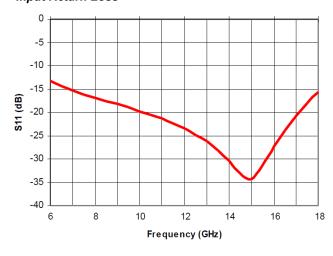


Typical Performance Curves: On Wafer Measurements, +25°C

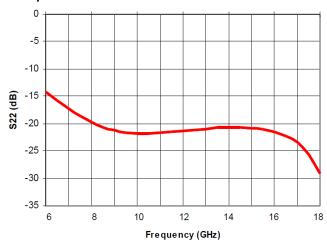




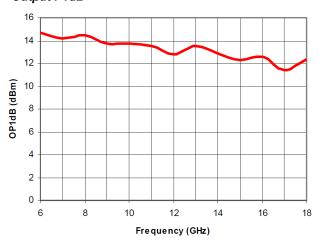
Input Return Loss



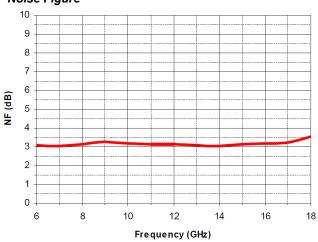
Output Return Loss



Output P1dB



Noise Figure

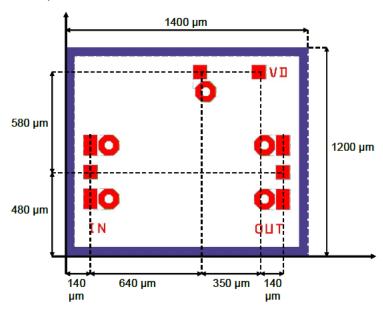




Pad Layout

Chip size = 1400 x 1200 µm

- DC and RF Pads = 80 x 80 μm, top metal = Au
- Chip Thickness 100 µm

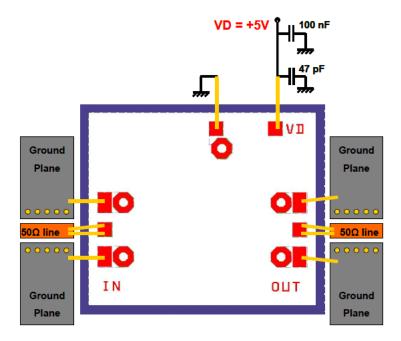


PAD NAME SYMBOL		COORDINATES		DESCRIPTION
PAD NAME	PAD NAME STMBOL X Y	DESCRIPTION		
GND	GND	140	330	Ground
RFin	RF in	140	480	RF Input Port
GND	GND	140	630	Ground
GND	GND	1260	630	Ground
RFout	RF out	1260	480	RF Output Port
GND	GND	1260	330	Ground
VDD	VDD	1120	1060	Positive supply voltage
GND	GND	780	1060	Ground

X=0, Y=0 at bottom left corner.



Bonding Diagram and Assemble Information



The RF interfacing bond wires or ribbon should be kept as short as possible.

The RF lines should be 300 μ m wide or less to minimize discontinuities associated with the connection to the MMIC bond pads.

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