Low Noise Amplifier, X-Band 7.5 - 13 GHz



CGY2221HV/C1 Rev. V1

Features

Suitable for X-Band Applications
Frequency Range: 7.5 - 13 GHz

Single Supply Architecture

Gain: 16 dB

Gain Flatness: ± 0.8 dB
Noise Figure: 1.6 dB
Input Return Loss: 12 dB
Output Return Loss: 15 dB
Maximum Input Power: 31 dBm

Output P1dB: 17 dBmOutput IP3: 29 dBm

Power Supply: 82 mA @ 5 V

4 mm 24-lel QFN

Demonstration Boards Available

RoHS* Compliant

Applications

Radar

Telecommunications

Instrumentations

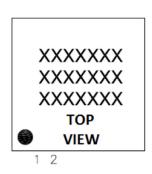
Description

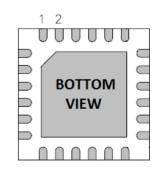
The CGY2221HV/C1 is a high performance GaAs single supply low noise amplifier MMIC designed to operate in the X-band with an extremely high maximum input RF power.

The CGY2221HV/C1 has a low noise figure of 1.6 dB with minimum 16 dB of Gain. The on chip matching provides better than 12 dB of input and output return loss.

The die is manufactured using a 0.13 µm gate length pHEMT technology. The MMIC uses gold bonding pads and backside metallization and is fully protected with Silicon Nitride passivation to obtain the highest level of reliability.

This technology has been evaluated for Space applications and is on the European Preferred Parts List of the European Space Agency. The device is available in a 4 mm 24 lead QFN package.





Pin Configuration¹

Pin	Function		
1, 2, 4 - 15, 17 - 20, 22 - 24	Ground		
3	RF Input		
16	RF Output		
21	Single Supply Voltage		

 The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information

Part Number	Package
CGY2221HV/C1	

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



CGY2221HV/C1 Rev. V1

Electrical Specifications: Measured On Wafer, Freq. = 8 - 12 GHz, V_{DD} = 5 V, T_A = +25°C

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Drain Voltage	_	V	_	5	_
Drain Supply Current	_	mA	72	82	92
Gain	_	dB	16	17	_
Noise Figure	_	dB	1.5	1.6	1.8
Input Return Loss	_	dB	_	12	_
Output Return Loss	_	dB	_	15	_
Reverse Isolation	RFOUT / RFIN	dB	_	-35	-30
P1dB	_	dBm	16	17	_
Output IP3	_	dBm	_	29	_

Absolute Maximum Ratings^{2,3}

Parameter	Absolute Maximum		
Input Power CW 10% Duty Cycle,10 µs pulse	21 dBm 31 dBm		
Drain Voltage	0 to +6 V		
Drain Current	100 mA		
Junction Temperature	+150°C		
Operating Temperature	-55°C to +85°C		
Storage Temperature	-55°C to +150°C		

^{2.} Exceeding any one or combination of these limits may cause permanent damage to this device.

Thermal Characteristics

Parameter	Absolute Maximum
Thermal Resistance	TBD

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

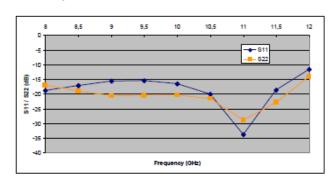
MACOM does not recommend sustained operation near these survivability limits.



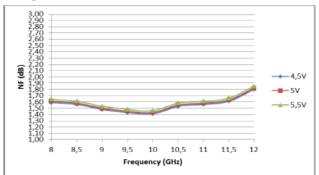
CGY2221HV/C1 Rev. V1

S-Parameters, Noise Figure, P1dB

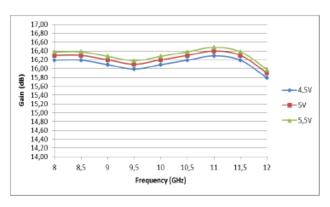
Measured at 25°C, VDD = 5V and ID = 82mA.



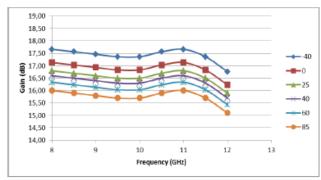
Noise Figure measured at 25°C on board, WRT VDD.



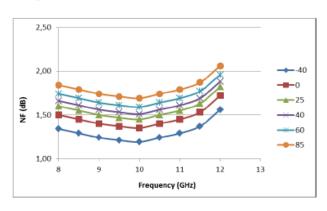
Gain measured at 25°C on board, WRT VDD.



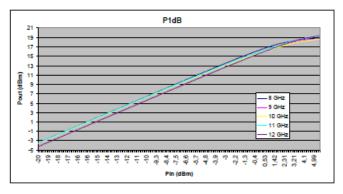
Gain vs.Temperature VDD = 5V ID = 82mA: Measured on Board



NF vs.Temperature: Measured on Board



Output Power: Measured on Board

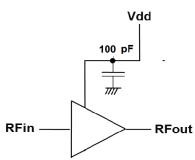




CGY2221HV/C1

Rev. V1

Application Schematic

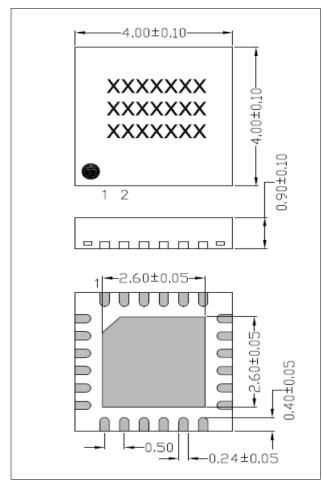


Application Information

To prevent instability of the customer design it is highly recommended to place small chip capacitors as near as possible to the CGY2221HV/C1, here 100 pF recommended as placed in the demonstration board.

Additionally, a 10 nF capacitor can be added on a drain connection to insure low frequency decoupling, the power supply decoupling could be complemented with 1 μ F capacitors.

Outline



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CGY2221HV/C1

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