

CGY2171XBUH/C1

Rev. V1

Features

- Insertion Loss: 5 dB @ 10 GHz
- Attenuation Range: 31.5 dB
- RMS Attenuation Error: 0.25 dB @ 10 GHz
- Input P1dB: 20 dBm
- Return Loss: < -13 dB @ 10 GHz (All States)
- 0 / 5 V Control Lines
- Chip size = 2600 x 1200 μm ± 5 μm
- Tested, Inspected Known Good Die (KGD)
- Samples Available
- Space and MIL-STD Available
- RoHS* Compliant

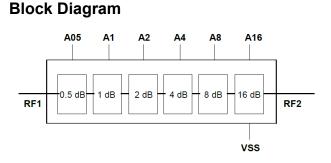
Applications

- Radar
- Telecommunication
- Instrumentation

Description

The CGY2171XBUH/C1 is a high performance GaAs MMIC 6–bit Attenuator operating in L, S, C, and X-band. This device has a nominal attenuation range of 31.5 dB in 0.5 dB steps. It covers the frequency range of 1 to 15 GHz and can be used in Radar, Telecommunication, & Instrumentation applications.

The die is manufactured using a 0.18 µm gate length pHEMT technology. The MMIC uses gold bonding pads and backside metallization and is fully protected with Silicon Nitride passivation to obtain the highest level of reliability. This technology has been evaluated for Space applications and is on the European Preferred Parts List of the European Space Agency.



Pad Configuration^{1,2}

Pad	Function		
RF1	RF Port 1		
A05	0.5 dB cell control		
A1	1 dB cell control		
A2	2 dB cell control		
A4	4 dB cell control		
A8	8 dB cell control		
A16	16 dB cell control		
RF2	RF Port 2		
VSS	Negative Supply Voltage		
GND	Ground (back side)		

1. MACOM recommends connecting No Connection (N/C) pins to ground.

2. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information

Part Number	Package
CGY2171XBUH/C1	Die

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^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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Electrical Specifications: Measured On Wafer Freq. = 10 GHz, V_{SS} = -5 V, I_{SS} = 8 mA, T_A = +25°C

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	_	dB		5	—
Noise Figure	@ Reference State	dB		5	—
Attenuation Range	—	dB	_	31.5	_
Return Loss	@ RF1 & RF2	dB	_	15	_
RMS Attenuation Error ³	—	dB	_	0.25	_
Maximum RMS Attenuation Error ³		dB	_	±1	—
RMS Phase Error ³	_	o	_	5	
Maximum RMS Phase Error ³	_	o		±8	_
P1dB	—	dBm		20	

 The RMS value is the root mean square of the error defined as below: Where x_i is the difference between the measured value and the expected value.

$$x_{\rm rms} = \sqrt{\frac{1}{N} \sum_{i=1}^{N} x_i^2} = \sqrt{\frac{x_1^2 + x_2^2 + \dots + x_N^2}{N}}$$

Absolute Maximum Ratings^{4,5}

Parameter	Absolute Maximum
Attenuation Control Inputs	0 V to +7 V
Source Supply Voltage	-7 V to 0 V
Input Power	TBD
Junction Temperature	+150°C
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +150°C

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

MACOM does not recommend sustained operation near these survivability limits.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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Logic Truth Table

	A05	A1	A2	A4	A 8	A16
Nominal Attenuation	0.5 dB	1 dB	2 dB	4 dB	8 dB	16 dB
Pad	A05	A1	A2	A4	A8	A16
Attenuation Activated	5 V	5 V	5 V	5 V	5 V	5 V
Reference State	0 V	0 V	0 V	0 V	0 V	0 V

Logic Truth Table (Detailed)

	A05	A1	A2	A4	A8	A16
Attenuation (dB)	0.5	1	2	4	8	16
0	0	0	0	0	0	0
0.5	1	0	0	0	0	0
1	0	1	0	0	0	0
2	0	0	1	0	0	0
3	0	1	1	0	0	0
4	0	0	0	1	0	0
5	0	1	0	1	0	0
6	0	0	1	1	0	0
8	0	0	0	0	1	0
10	0	0	1	0	1	0
15	0	1	1	1	1	0
16	0	0	0	0	0	1
20	0	0	0	1	0	1
25	0	1	0	0	1	1
30	0	0	1	1	1	1
31.5	1	1	1	1	1	1

Control Voltage

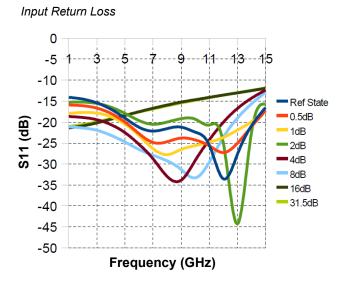
State	Min.	Тур.	Max.	Unit
Low	-0.1	_	+0.1	V
High	4.75	5.00	5.25	V

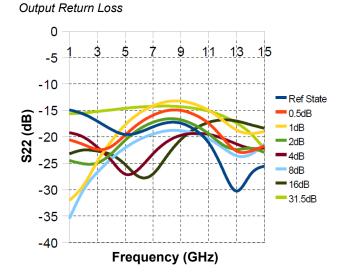
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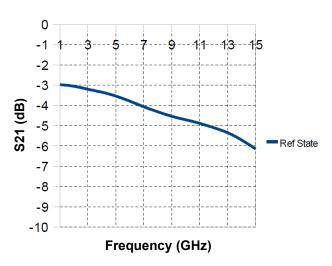


Typical Performance Curves: S-Parameters On Wafer Measurements, calculated with input and output inductance of 0.3 nH

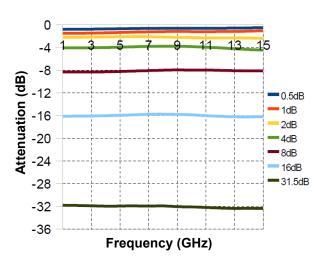




Transmission Loss



Attenuation



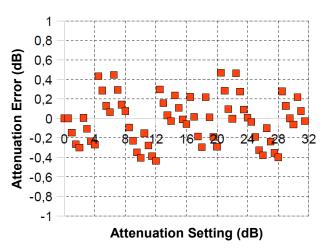
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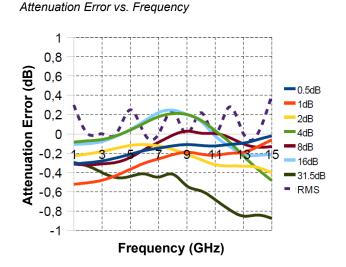
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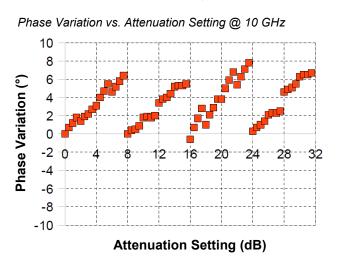
Typical Performance Curves: Attenuation Errors On Wafer Measurements, calculated with input and output inductance of 0.3 nH

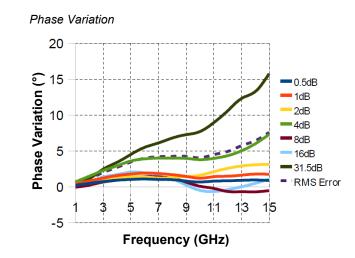
Attenuation Error vs. Attenuation Setting @ 10 GHz





Typical Performance Curves: Phase Shifting Variations On Wafer Measurements, calculated with input and output inductance of 0.3 nH





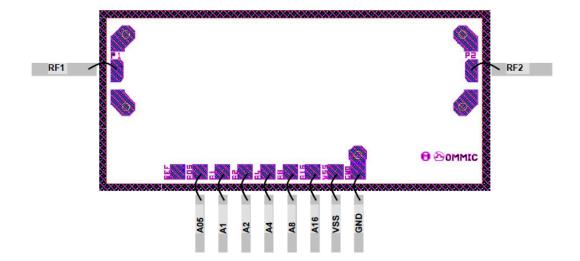
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Bonding Diagram & Assembly Information



RF interface : coplanar or microstrip, bonding \approx 400/500 µm.

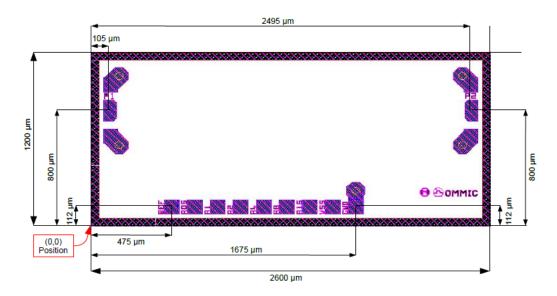
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Mechanical Information



Chip Size = 2600 x 1200 μm (2565 x 1165 μm ± 5 μm) DC Pads = 100 x 100 μm , spacing = 150 μm , top metal = Au RF Pads = 85 x 150 μm , top metal = Au Chip Thickness = 100 μm

Pad Position^{7,8}

Ded Name	Coordinate		Description
Pad Name	X	Y	Description
RF1 (P1)	105	800	RF Port 1
RF2 (P2)	2495	800	RF Port 2
REF	475	112	Reference Output Voltage (do not connect)
A05	625	112	0.5 dB cell control
A1	775	112	1 dB cell control
A2	925	112	2 dB cell control
A4	1075	112	4 dB cell control
A8	1225	112	8 dB cell control
A16	1375	112	16 dB cell control
VSS	1525	112	Negative Supply Voltage
GND	1675	112	Ground (connected to MMIC back side metal)

7. X = 0, Y = 0 at bottom left corner.

8. See mechanical information for more detail.

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