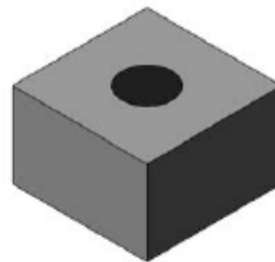


Features

- Schottky Barrier Diode Chips for general purpose applications
- Silicon dioxide passivated
- Compatible with all wire bonding and die attach techniques, with the exception of solder reflow



Maximum Ratings

Operating Temperature: -55 °C to +150 °C
 Storage Temperature: -65 °C to +150 °C

Electrical Specifications @ +25 °C (Unless Otherwise Specified)

Type Number	Minimum Breakdown Voltage	Maximum Forward Voltage	Maximum Forward Voltage	Maximum Reverse Leakage Current		Maximum Capacitance @ $V_R = 0$ Volts $f = 1.0$ MHz
	$V_{BR} @ 10 \mu A$	$V_F @ 1 mA$	$V_F @ I_F$	$I_R @ V_R$		C_T
	Volts	Volts	Volts @ mA	nA	Volts	pF
CD5711	70	0.41	1.0 @15	200	50	2.0
CD5712	20	0.41	1.0 @35	150	16	1.2

Note: Effective Minority Carrier Lifetime (τ) is 100 Pico Seconds

Graphs

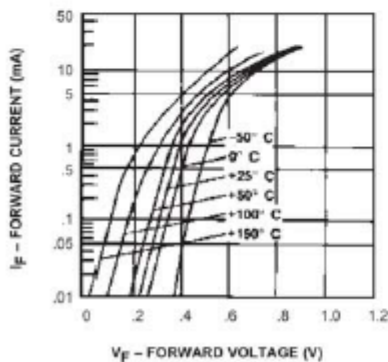


Figure 1.
I-V Curve Showing Typical Forward Voltage Variation with Temperature for Schottky Diode.

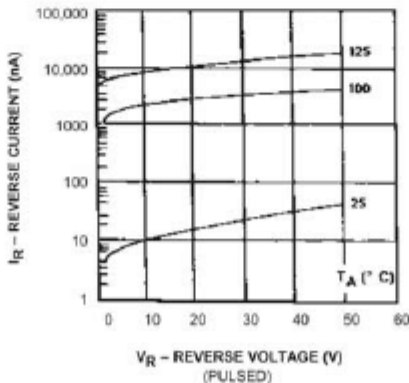


Figure 2.
Typical Variation of Reverse Current (I_R) vs. Reverse Voltage (V_R) at Various Temperatures.

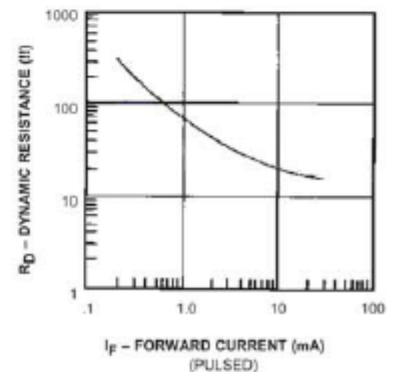
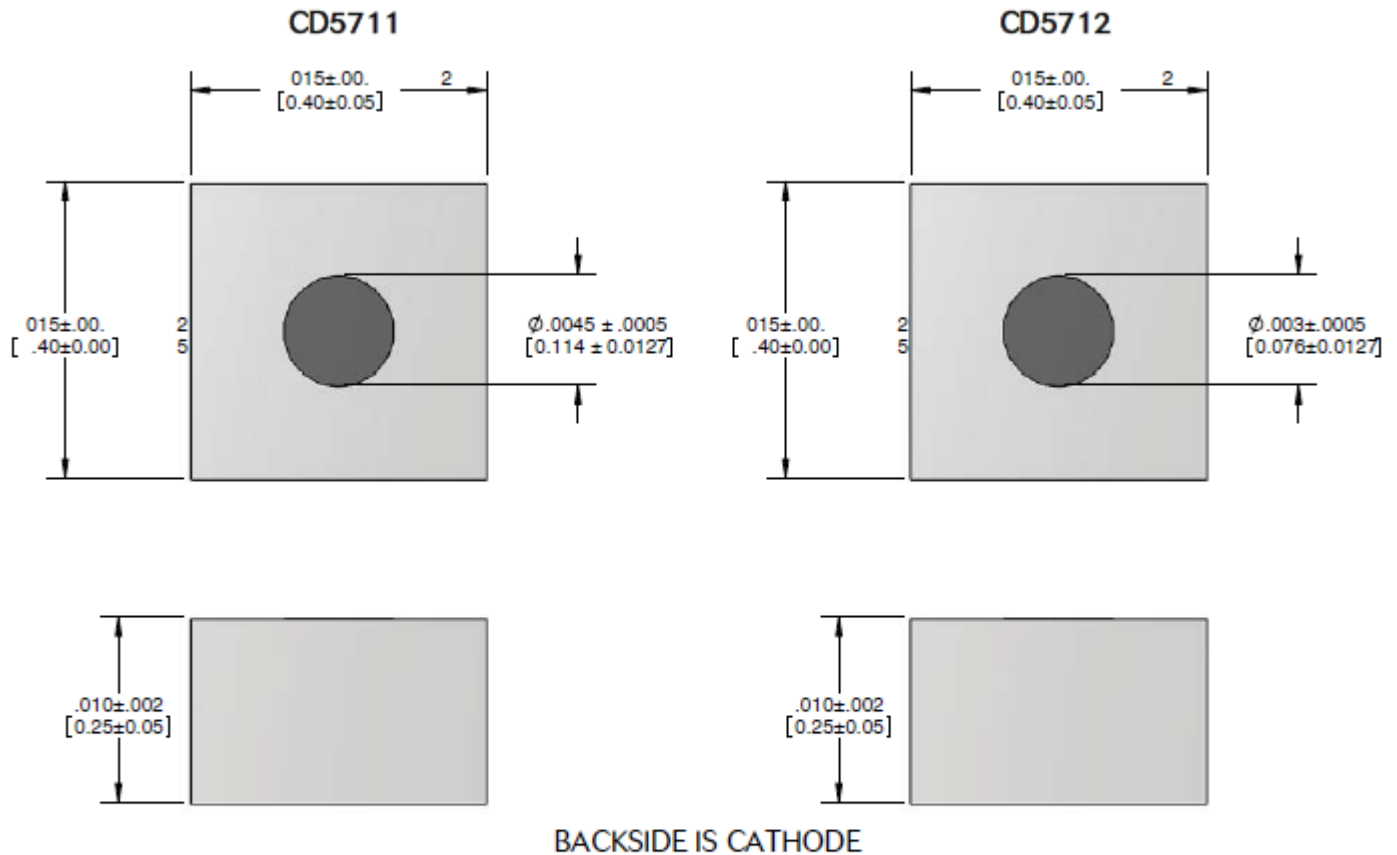


Figure 3.
Typical Dynamic Resistance (R_D) vs. Forward Current (I_F).

Outline Drawing



DESIGN DATA

METALLIZATION: Top: (Anode)
 Back: (Cathode) Au

AL

AL THICKNESS: 50,000 Å Minimum

GOLD THICKNESS: 5,000 Å Minimum

CHIP THICKNESS: 10 Mils

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