Features
- High Reliability Silicon Nitride-Oxide Dielectric
- Low Loss
- Long Term Reliability and Stability
- RoHS* Compliant

Description
The 906R5K-SP03 binary capacitor is designed to facilitate bread-boarding or to use where a trimming capability is required. These devices feature the same dielectric layer and bonding surfaces as our 9000 and 9100 series chip capacitors. By connecting the pads in parallel the capacitance values are additive, so many combinations are possible.

These devices are ideally suited for use in applications requiring a trimming capability or bread-boarding.

Electrical Specifications:  \( T_{C} = +25^\circ \text{C} \) (unless otherwise specified)

<table>
<thead>
<tr>
<th>Part #</th>
<th>Reverse Breakdown ( V_B ) ( I_R = 10 , \mu \text{A} )</th>
<th>Capacitance ( C_J ) ( V_R = 0 , \text{V} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>906R5K-SP03</td>
<td>50</td>
<td>7.15</td>
</tr>
</tbody>
</table>

Absolute Maximum Ratings\(^5,6\)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Moisture Sensitivity Level</td>
<td>1</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-55°C to +150°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +200°C</td>
</tr>
</tbody>
</table>

Handling Procedures
Please observe the following precautions to avoid damage:

Static Sensitivity
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (HBM) devices.

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1. Customer Part Number: TBD
2. Electrical:
   - Dielectric Withstanding: 50 V
   - Capacitance: 6.5 pF
   - Capacitance Tolerance: ±10%
   - Bonding Pad Minimum: N/A
   - VB Minimum: 50 V
5. Visual Inspection: Per MIL-STD-883
6. Metalization:
   - Backside metal Au 50-100
   - Contact metal Au 20-100 thick

Dimensions are in inches (mm)