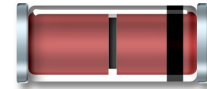
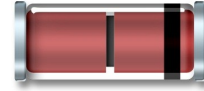


Silicon Switching Diode

Rev. V2

Features

- Available in JAN, JANTX, and JANTXV per MIL-PRF-19500/116
- Metallurgically Bonded
- Hermetically Sealed
- Double Plug Construction



Maximum Ratings

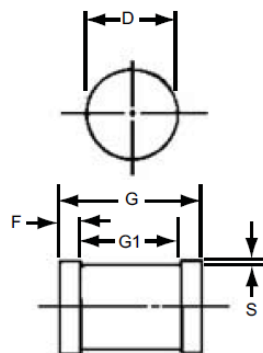
Operating & Storage Temperature: -65°C to +175°C
 Operating Current: 125 mA @ $T_A = +75^\circ\text{C}$
 Derating Factor: 2.0 mA/°C above $T_A = +25^\circ\text{C}$
 Surge Current A: 2.00 A, sinewave, $P_w = 8.3$ ms
 Surge Current B: 0.704 A, square, $P_w = 8.3$ ms
 DC Reverse Voltage (VRWM): 75 V

Electrical Specifications @ +25°C (unless otherwise Specified)

JEDEC TYPE#	VBR @ 100 μA	VRWM	IO	Vf1 IF = 10 mA	Vf2 IF = 50 mA	Trr ¹	IR1 @ 20 Vdc	IR2 @ 75 Vdc	IR3 @ 20 Vdc $T_A = 150^\circ\text{C}$	IR4 @ 75 Vdc $T_A = 150^\circ\text{C}$	Capacitance @ 0 V	Capacitance @ 1.5 V
	Volts	Volts (pk)	mA	Vdc	Vdc	nsec	nA	μA	μA	μA	pF	pF
1N914UR	100	75	75	0.8	1.2	5	25	0.5	35	75	4.0	2.8
1N4148UR-1	100	75	200	0.8	1.2	5	35	0.5	35	75	4.0	2.8

1. Note1: IF = IR = 10 mA, RL = 100 ohms.

Outline Drawing



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
D	1.60	1.70	0.063	0.067
F	0.41	0.55	0.016	0.022
G	3.30	3.70	.130	.146
G1	2.54 REF.		.100 REF.	
S	0.03 MIN.		.001 MIN.	

LEADED DESIGN DATA

CASE: DO-213AA, Hermetically sealed glass case. (MELF, SOD-80, LL34)

LEAD FINISH: Tin / Lead

THERMAL RESISTANCE: ($R_{\theta JC}$): 100 °C/W maximum at L = 0 inch

THERMAL IMPEDANCE: ($Z_{\theta JX}$): 25 °C/W maximum

POLARITY: Diode to be operated with the banded (cathode) end positive.

MOUNTING POSITION: Any.

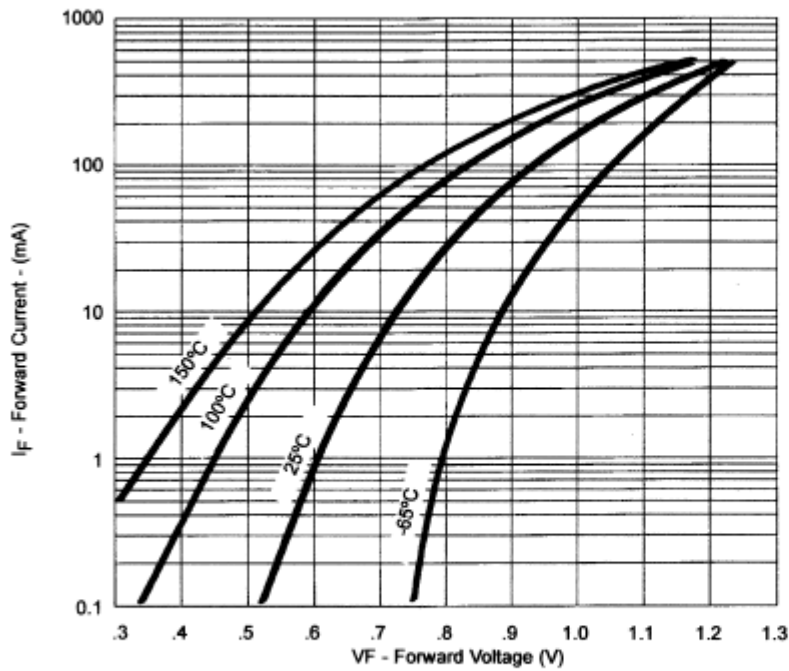
1N914UR & 1N4148UR-1



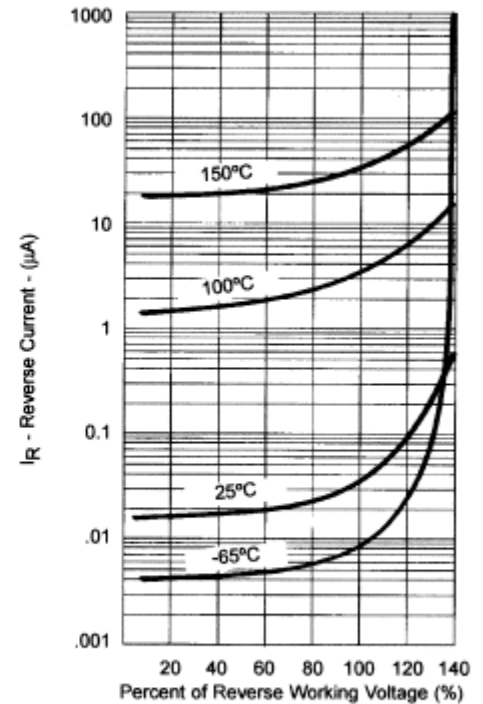
Silicon Switching Diode

Rev. V2

Graphs



Typical Forward Current vs Forward Voltage



Typical Reverse Current vs Reverse Voltage

NOTE : All temperatures shown on graphs are junction temperatures

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