MASWSS0167 Without Blocking Capacitors (To observe low frequency performance)

Demonstration of lower frequency operation
> Please demonstrate low frequency operation on the **MASWSS0167**.

> Datasheet Page 2 Note 4:

  4. Insertion Loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for 100 MHz - 500 MHz, 39 pF for 0.5 - 3 GHz.

> To operate the switch with positive control, we recommend using large value blocking capacitors (at least 1000pF).

> S-Parameters (embedded below, right click to open) are included, de-embedded, to make it easier to integrate into the system. This data was taken without blocking capacitors, to get accurate low frequency data.

> Note that the 0 Ohm resistors used probably look a bit inductive at higher frequencies, hence the observed roll off at the high end of the band.
Insertion Loss

Insertion Loss over Bias and Switch State

freq, GHz

0.0 0.2 0.4 0.6 0.8 1.0 1.2 1.4 1.6 1.8 2.0 2.2 2.4 2.6 2.8 3.0 3.2 3.4 3.6 3.8 4.0

-2.0 -1.5 -1.0 -0.5 0.0

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Isolation over Bias and Switch State

freq, GHz

Isolation
S22 (RF1&2)

RF1 & 2 Return Loss over Bias and Switch State

Isolation Cases
Further Notes

> At low frequencies, linearity of switch devices is typically reduced. It is important to consider that the datasheet has information extracted at 2.5V. 3 or 5V control could also be used, which will improve linearity in general.

> By linearity, we refer to metrics such as compression, intermodulation distortion, harmonics, etc.

> A 20K resistor, from the RF Common port, pulling up to the most positive control voltage (must be the same as VChigh), also can help to improve linearity when operating at very low voltages.