MASWSS0103

SPDT High Isolation CATV Switch
5 – 1000 MHz

Features
- 75 Ohm Impedance
- Input Terminated
- Positive Voltage Control
- High Isolation: 65 dB at 870 MHz
- 0.5 micron GaAs PHEMT Process
- Lead-Free 4 mm 20-Lead PQFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description
M/A-COM’s MASWSS0103 is a GaAs PHEMT MMIC single pole double throw (SPDT) switch in a lead-free 4 mm 20-lead PQFN package. The MASWSS0103 is ideally suited for applications where low control voltage, high isolation, small size and low cost are required.

Typical applications are to replace mechanical relays in CATV systems. This part can be used in all 75 Ω systems operating up to 1 GHz.

The MASWSS0103 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>MASWSS0103TR-3000</td>
<td>13 inch, 3000 piece reel</td>
</tr>
<tr>
<td>MASWSS0103SMB</td>
<td>Sample Test Board (Includes 5 Samples)</td>
</tr>
</tbody>
</table>

1: Reference Application Note M513 for reel size information.


For further information and support please visit: https://www.macom.com/support

Visit www.macom.com for additional data sheets and product information.
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Absolute Maximum Ratings 3,4

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Absolute Maximum</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Power (5 - 1000 MHz, 2.9 V Control)</td>
<td>+32 dBm</td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>+8.5 volts</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-40°C to +85°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to +150°C</td>
</tr>
</tbody>
</table>

2. External 0.01 µF DC blocking capacitors are required on all RF In/Out and RF ground ports. See Application Schematic.

3. Exceeding any one or combination of these limits may cause permanent damage.
4. M/A-COM does not recommend sustained operation near these survivability limits.

Truth Table 5

<table>
<thead>
<tr>
<th>V1</th>
<th>V2</th>
<th>RFC - RF1</th>
<th>RFC - RF2</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0</td>
<td>On</td>
<td>Off</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>Off</td>
<td>On</td>
</tr>
</tbody>
</table>

5. 1 = +2.9 to +5 V, 0 = 0 ± 0.2 V.

Application Schematic

C1 - C9 = 0.01 µF, RF Bypass Capacitors
C10 - C11 = 0.01 µF, Logic Control Decoupling Capacitors
Typical Performance Curves

**Insertion Loss**

**RF1 Isolation**

-25°C
-40°C
-85°C

**RF2 Isolation**

-25°C
-40°C
-85°C

**On Return Loss**

-25°C
-40°C
-85°C

**Off Return Loss**

-25°C
-40°C
-85°C

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**Handling Procedures**

Please observe the following precautions to avoid damage:

**Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

† Reference Application Note M538 for lead-free solder reflow recommendations.