

Features

- High Speed CMOS Technology
- Complementary Outputs
- Positive Voltage Control
- Low Power Dissipation
- Plastic SOIC Package for SMT Applications
- Tape and Reel Packaging Available
- Lead-Free SOIC-8 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free “Green” Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of DR65-0109

Description

M/A-COM's MADRCC0005 is a Single channel driver used to translate TTL control inputs into complementary gate voltages for GaAs FET microwave switches and attenuators. High speed analog CMOS technology is utilized to achieve low power dissipation at moderate to high speeds, encompassing most microwave switching applications.

Ordering Information

| Part Number | Package |
|--------------|-----------------|
| MADRCC0005 | Bulk Packaging |
| MADRCC0005TR | 1000 piece reel |

Note: Reference Application Note M513 for reel size information.

Guaranteed Operating Ranges

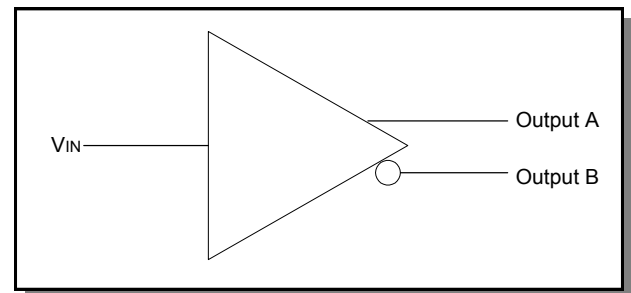
| Symbol | Parameter ¹ | Unit | Min | Typical | Max |
|---------------------------------------|-----------------------------------|------|------|---------|------|
| V _{CC} | Positive DC Supply Voltage | V | 4.5 | 5.0 | 5.5 |
| V _{EE} | Negative DC Supply Voltage | V | -5.5 | -5.0 | -4.5 |
| V _{CC-V_{EE}} | Positive to Negative Supply Range | V | 9.0 | 10.0 | 11.0 |
| T _A | Operating Ambient Temperature | °C | -40 | +25 | +85 |
| I _{OH} | DC Output Current - HIGH | mA | — | — | -1.0 |
| I _{OL} | DC Output Current - LOW | mA | — | — | 1.0 |
| T _{rise} , T _{fall} | Maximum Input Rise or Fall Time | nS | — | — | 500 |

1. All voltages are relative to GND

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Logic Diagram



Pin Configuration

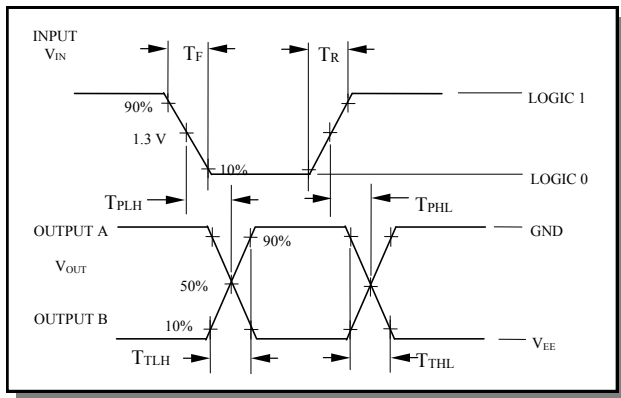
| Pin No. | Function |
|---------|-----------------|
| 1 | V _{CC} |
| 2 | V _{IN} |
| 3 | GND |
| 4 | GND |
| 5 | GND |
| 6 | Output A |
| 7 | Output B |
| 8 | V _{EE} |

AC & DC Characteristics Over Guaranteed Operating Range

| Symbol | Parameter | Test Conditions | | Units | Min | Typ | Max |
|-------------------------------------|-------------------------------------|------------------------------------------|-------------------------------------------------------------------|-------|------|-----|-----------------------|
| V _{IH} | Input HIGH Voltage | Guaranteed HIGH Input Voltage | | V | 2.0 | — | - |
| V _{IL} | Input LOW Voltage | Guaranteed LOW Input Voltage | | V | - | — | 0.8 |
| V _{OH} | Output HIGH Voltage | I _{OH} = -1 mA | V _{EE} = Max | V | -0.1 | — | - |
| V _{OL} | Output LOW Voltage | I _{OL} = 1 mA | V _{EE} = Max | V | — | — | V _{EE} + 0.1 |
| I _{IN} | Input Leakage Current | V _{IN} = V _{CC} or GND | V _{EE} = Min | μA | -1.0 | 0 | 1.0 |
| I _{CC} | Quiescent Supply Current | V _{CC} = Max | V _{EE} = Min V _{IN} = V _{CC} or GND | μA | — | — | 400 |
| T _{PHL} , T _{PLH} | Propagation Delay | Guaranteed -40° C to +85° C | | nS | — | — | 50 |
| T _{THL} , T _{TLH} | Output Transition Time | Guaranteed -40° C to +85° C | | nS | — | — | 25 |
| | Delay Skew, Output A to Output B | Guaranteed -40° C to +85° C | | nS | — | — | 8 |

See Switching Wave Forms for the definition of the switching terms.
Supplies must be by-passed with .01 μF Capacitors.

Switching Waveforms



Absolute Maximum Ratings^{2,3}

| Parameter | Absolute Maximum |
|-----------------------------------|------------------------|
| V _{CC} | - .5V to + 6.0 V |
| V _{EE} | - 6.0 V to - .5 V |
| V _{CC} - V _{EE} | 12 V |
| V _{IN} ⁴ | V _{CC} + .5 V |
| V _{OUT} | V _{EE} - .5 V |
| Storage Temperature | -65°C to +150°C |

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

Handling Procedures

Please observe the following precautions to avoid damage:

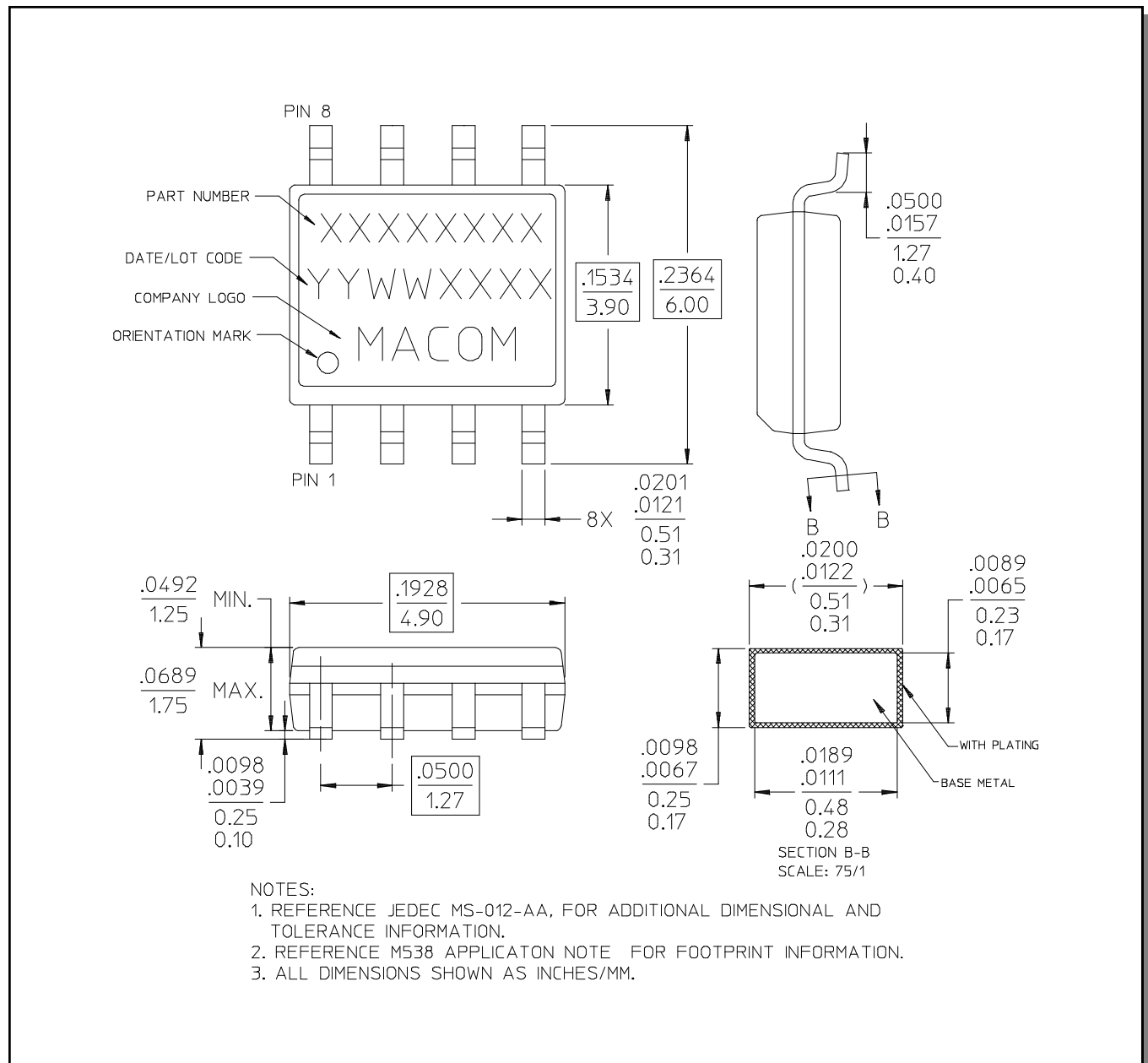
Static Sensitivity

Silicon Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Truth Table

| Input | Outputs | |
|-----------------|-----------------|-----------------|
| V _{IN} | A | B |
| 0 | V _{EE} | GND |
| 1 | GND | V _{EE} |

Lead-Free, SOIC-8[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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